SPEC® CPU2017 Floating Point Speed Result

Dell Inc.

PowerEdge C6420 (Intel Xeon Gold 6136, 3.00 GHz)

SPECspeed2017_fp_base = 101

SPECspeed2017_fp_peak = Not Run

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.
Test Date: Dec-2017
Hardware Availability: Sep-2017
Software Availability: Sep-2017

603.bwaves_s 48
607.cactuBSSN_s 48
619.lbm_s 48
621.wrf_s 48
627.cam4_s 48
628.pop2_s 48
638.imagick_s 48
644.nab_s 48
649.fotonik3d_s 48
654.roms_s 48

### Hardware

CPU Name: Intel Xeon Gold 6136
Max MHz.: 3700
Nominal: 3000
Enabled: 24 cores, 2 chips, 2 threads/core
Orderable: 1,2 chips
Cache L1: 32 KB I + 32 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 24.75 MB I+D on chip per chip
Other: None
Memory: 192 GB (12 x 16 GB 2Rx8 PC4-2666V-R)
Storage: 480 GB SATA SSD
Other: None

### Software

OS: CentOS Linux release 7.4.1708 (Core)
Compiler: C/C++: Version 18.0.0.128 of Intel C/C++
Fortran: Version 18.0.0.128 of Intel Fortran
Parallel: Yes
Firmware: Version 1.0.8 released Jul-2017
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: None
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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
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<tr>
<td>603.bwaves_s</td>
<td>48</td>
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<td>429</td>
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<td>628.pop2_s</td>
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</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:

KMP_AFFINITY = "granularity=fine,compact"
OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>/proc/sys/vm/drop_caches

No: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
No: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
No: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

This benchmark result is intended to provide perspective on past performance using the historical hardware and/or software described on this result page.

The system as described on this result page was formerly

(Continued on next page)
SPEC CPU2017 Floating Point Speed Result

Dell Inc.

PowerEdge C6420 (Intel Xeon Gold 6136, 3.00 GHz)

| SPECspeed2017_fp_base = 101 |
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Test Sponsor: Dell Inc.
Tested by: Dell Inc.
Test Date: Dec-2017
Hardware Availability: Sep-2017
Software Availability: Sep-2017

General Notes (Continued)

generally available. At the time of this publication, it may not be shipping, and/or may not be supported, and/or may fail to meet other tests of General Availability described in the SPEC OSG Policy document, http://www.spec.org/osg/policy.html

This measured result may not be representative of the result that would be measured were this benchmark run with hardware and software available as of the publication date.

Platform Notes

BIOS settings:
Virtualization Technology disabled
System Profile set to Custom
CPU Power Management set to Maximum Performance
Memory Frequency set to Maximum Performance
Turbo Boost enabled
C States disabled
Memory Patrol Scrub disabled
PCI ASPM L1 Link Power Management disabled
Sysinfo program /root/cpu2017/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on localhost.localdomain Wed Dec 20 20:29:49 2017

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6136 CPU @ 3.00GHz
  2 "physical id"s (chips)
  48 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 12
siblings : 24
  physical 0: cores 0 3 4 5 6 7 16 18 19 20 21 22
  physical 1: cores 0 1 2 3 4 9 10 16 18 19 25 26

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 48
On-line CPU(s) list: 0-47
Thread(s) per core: 2

(Continued on next page)
**SPEC CPU2017 Floating Point Speed Result**

**Dell Inc.**

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**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.

**Test Date:** Dec-2017  
**Hardware Availability:** Sep-2017  
**Software Availability:** Sep-2017

---

**Platform Notes (Continued)**

- Core(s) per socket: 12
- Socket(s): 2
- NUMA node(s): 2
- Vendor ID: GenuineIntel
- CPU family: 6
- Model: 85
- Model name: Intel(R) Xeon(R) Gold 6136 CPU @ 3.00GHz
- Stepping: 4
- CPU MHz: 3000.000
- BogoMIPS: 6000.00
- Virtualization: VT-x
- L1d cache: 32K
- L1i cache: 32K
- L2 cache: 1024K
- L3 cache: 25344K

**NUMA node0 CPU(s):**

- 0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30,32,34,36,38,40,42,44,46

**NUMA node0 CPU(s):**

- 1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31,33,35,37,39,41,43,45,47

**Flags:**

- fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good xtopology nonstop_tsc aperfmperf eagerfpu nmi pcmu lmxm fmmode stkesr tsu msrstar intelmpi vsx tm2 ssse3 fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3nowprefetch epb cat_13 cdql_13 intel_pt tpr_shadow vmmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 iberms invpcid rtm cmpq mpdx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsavec xsaveopt xgetbv1 cqm_llc cqm_occup_llc cqm_mbb_total cqm_mbb_local dtherm ida arat pln pts

```
/proc/cpuinfo cache data
  cache size : 25344 KB
```

**From numactl --hardware**

```
WARNING: a numactl 'node' might or might not correspond to a physical chip.
```

**available:**

- node 0 cpus: 0 2 4 6 8 10 12 14 16 18 20 22 24 26 28 30 32 34 36 38 40 42 44 46
- node 0 size: 96965 MB
- node 0 free: 91600 MB
- node 1 cpus: 1 3 5 7 9 11 13 15 17 19 21 23 25 27 29 31 33 35 37 39 41 43 45 47
- node 1 size: 98304 MB
- node 1 free: 94342 MB
- node distances:
  - node 0
    - 0: 10 21
    - 1: 21 10

(Continued on next page)
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Platform Notes (Continued)

From /proc/meminfo

- MemTotal: 196690028 kB
- HugePages_Total: 128
- Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*

- centos-release: CentOS Linux release 7.4.1708 (Core)
- centos-release-upstream: Derived from Red Hat Enterprise Linux 7.4 (Source)
- os-release:
  - NAME="CentOS Linux"
  - VERSION="7 (Core)"
  - ID="centos"
  - ID_LIKE="rhel fedora"
  - VERSION_ID="7"
  - PRETTY_NAME="CentOS Linux 7 (Core)"
  - ANSI_COLOR="0;31"
  - CPE_NAME="cpe:/o:centos:centos:7"

- redhat-release: CentOS Linux release 7.4.1708 (Core)
- system-release: CentOS Linux release 7.4.1708 (Core)
- system-release-cpe: cpe:/o:centos:centos:7

uname -a:

Linux localhost.localdomain 3.10.0-693.5.2.el7.x86_64 #1 SMP Fri Oct 20 20:32:50 UTC 2017 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Dec 20 17:54

SPEC is set to: /root/cpu2017

<table>
<thead>
<tr>
<th>Filesystem</th>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>/dev/sda2</td>
<td>xfs</td>
<td>433G</td>
<td>16G</td>
<td>417G</td>
<td>4%</td>
<td>/</td>
</tr>
</tbody>
</table>

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

- BIOS Dell Inc. 1.0.8 07/12/2017
- Memory:
  - 3x 002C00B3002C 18ASF2G72PDZ-2G6D1 16 GB 2 rank 2666
  - 9x 00A000B300AD HMA82GR7AFR8N-VK 16 GB 2 rank 2666
  - 4x Not Specified Not Specified

(End of data from sysinfo program)
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Compiler Version Notes

==============================================================================
CC  619.lbm_s(base) 638.imagick_s(base) 644.nab_s(base)
------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
==============================================================================
FC  607.cactuBSSN_s(base)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
==============================================================================
FC  603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base)
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ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
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==============================================================================
CC  621.wrf_s(base) 627.cam4_s(base) 628.pop2_s(base)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

Base Compiler Invocation

C benchmarks:
icc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

(Continued on next page)
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Base Compiler Invocation (Continued)

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
   -assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP

Fortran benchmarks:
-DSPEC_OPENMP -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
   -nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
   -nostandard-realloc-lhs -align array32byte

Benchmarks using Fortran, C, and C++:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
   -nostandard-realloc-lhs -align array32byte
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### Base Other Flags

C benchmarks:
- `-m64 -std=c11`

Fortran benchmarks:
- `-m64`

Benchmarks using both Fortran and C:
- `-m64 -std=c11`

Benchmarks using Fortran, C, and C++:
- `-m64 -std=c11`

The flags files that were used to format this result can be browsed at:

You can also download the XML flags sources by saving the following links:

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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