# SPEC® CPU2017 Integer Rate Result

**Epsylon Sp. z o.o. Sp. Komandytowa**

**Epsylon 210 RE1 (Intel Xeon Gold 6134, 3.20 GHz)**

<table>
<thead>
<tr>
<th>SPECrate2017_int_base</th>
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</tr>
</thead>
<tbody>
<tr>
<td>SPECrate2017_int_peak</td>
<td>111</td>
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**CPU2017 License:** 9081  
**Test Sponsor:** Epsylon Sp. z o.o. Sp. Komandytowa  
**Tested by:** Epsylon Sp. z o.o. Sp. Komandytowa

- **Test Date:** Apr-2018  
- **Hardware Availability:** Sep-2017  
- **Software Availability:** Mar-2018

| Copies | 0 | 10 | 20 | 30 | 40 | 50 | 60 | 70 | 80 | 90 | 100 | 110 | 120 | 130 | 140 | 150 | 160 | 170 | 180 | 190 | 200 | 210 | 220 | 230 |
|--------|---|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|
| 500.perlbench_r | 32 | 81.6 | | | | | | | | | | | | | | | | | | | | | | | |
| 502.gcc_r | 32 | 89.6 | | | | | | | | | | | | | | | | | | | | | | | |
| 505.mcf_r | 32 | 61.9 | | | | | | | | | | | | | | | | | | | | | | | |
| 520.omnetpp_r | 32 | 56.5 | | | | | | | | | | | | | | | | | | | | | | | |
| 523.xalancbmk_r | 32 | | | | | | | | | | | | | | | | | | | | | | | | |
| 525.x264_r | 32 | | | | | | | | | | | | | | | | | | | | | | | | |
| 531.deepsjeng_r | 32 | 90.3 | | | | | | | | | | | | | | | | | | | | | | | |
| 541.leela_r | 32 | 89.1 | | | | | | | | | | | | | | | | | | | | | | | |
| 548.exchange2_r | 32 | | | | | | | | | | | | | | | | | | | | | | | | |
| 557.xz_r | 32 | | | | | | | | | | | | | | | | | | | | | | | | |

**Hardware**

- **CPU Name:** Intel Xeon Gold 6134  
- **Max MHz.:** 3700  
- **Nominal:** 3200  
- **Enabled:** 16 cores, 2 chips, 2 threads/core  
- **Orderable:** 1.2 chips  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **L2:** 1 MB I+D on chip per core  
- **L3:** 24.75 MB I+D on chip per chip  
- **Other:** None  
- **Memory:** 128 GB (8 x 16 GB 2Rx4 PC4-2666V-R)  
- **Storage:** 1 x 960 GB SSD SATA III  
- **Other:** None

**Software**

- **OS:** Red Hat Enterprise Linux Server release 7.4 (Maipo)  
- **Compiler:** C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux; Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux
- **Parallel:** No  
- **Firmware:** Version BIOS 2.0b released Mar-2018  
- **File System:** ext4  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 32/64-bit  
- **Other:** jemalloc memory allocator library V5.0.1
SPEC CPU2017 Integer Rate Result

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Results Table

<table>
<thead>
<tr>
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<th>Seconds</th>
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<td>81.7</td>
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<td>520.omnetpp_r</td>
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<td>491</td>
<td>70.3</td>
</tr>
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</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/cpu2017.1.0/lib/ia32:/cpu2017.1.0/lib/intel64:/cpu2017.1.0/je5.0.1-32:/cpu2017.1.0/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32 GB RAM memory using Redhat Enterprise Linux 7.4

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches

(Continued on next page)
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**General Notes (Continued)**

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

jemalloc:
configured and built at default for 32bit (i686) and 64bit (x86_64) targets;
built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5;
sources available via jemalloc.net or https://github.com/jemalloc/jemalloc/releases

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**Platform Notes**

BIOS Settings:
Power Technology = Custom
Turbo Mode = Enable
Enhanced Halt State (C1E) = Disable
CPU C6 report = Disabled
Package C State = No limit
Software Controlled T-States = Disable
Hyper-Threading (All) = Enable
Enforce POR = Disable
Memory Frequency = Auto
Patrol Scrub = Disabled
IMC Interleaving = Auto
SNC = Disabled

Sysinfo program /cpu2017.1.0/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618b7091c0f
running on SUT Thu Apr 12 14:38:08 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6134 CPU @ 3.20GHz
  2 "physical id"'s (chips)
  32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 8
  siblings : 16
  physical 0: cores 0 2 3 9 16 19 26 27
  physical 1: cores 0 2 3 9 16 19 26 27

From lscpu:

(Continued on next page)
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eterio 210 RE1 (Intel Xeon Gold 6134, 3.20 GHz)

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**Platform Notes (Continued)**

Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 32
On-line CPU(s) list: 0-31
Thread(s) per core: 2
Core(s) per socket: 8
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6134 CPU @ 3.20GHz
Stepping: 4
CPU MHz: 3201.000
CPU max MHz: 3201.0000
CPU min MHz: 1200.0000
BogoMIPS: 6400.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 25344K
NUMA node0 CPU(s): 0-7,16-23
NUMA node1 CPU(s): 8-15,24-31
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 fma cx16 xptr xstore pclmulqdq x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch eplt cat_13 cdpl_13 invpcid_single intel_pt spec_ctrl ibpb_support tpr_shadow vmmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsaves xsavec xgetbv1 cmp_l1c cmp_occ不限 cmp_mbml_total cmp_mbml_local dtherm ida arat pln pts

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

node 0 cpus: 0 1 2 3 4 5 6 7 16 17 18 19 20 21 22 23
node 0 size: 65200 MB
node 0 free: 63091 MB
node 1 cpus: 8 9 10 11 12 13 14 15 24 25 26 27 28 29 30 31
node 1 size: 65536 MB

(Continued on next page)
Platform Notes (Continued)

node 1 free: 63814 MB
node distances:
node 0 1
  0: 10 21
  1: 21 10

From /proc/meminfo
  MemTotal:  131464776 kB
  HugePages_Total:  0
  Hugepagesize:  2048 kB

From /etc/*release* /etc/*version*
  os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.4 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VARIANT="Server"
    VARIANT_ID="server"
    VERSION_ID="7.4"
    PRETTY_NAME="Red Hat Enterprise Linux"
  redhat-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
  system-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
  system-release-cpe: cpe:/o:redhat:enterprise_linux:7.4:ga:server

uname -a:
  Linux SUT 3.10.0-693.21.1.el7.x86_64 #1 SMP Fri Feb 23 18:54:16 UTC 2018 x86_64 x86_64
  x86_64 GNU/Linux

run-level 3 Apr 12 14:33

SPEC is set to: /cpu2017.1.0

Filesystem  Type  Size  Used Avail Use% Mounted on
/dev/sda1    ext4   825G  68G  716G   9%  /

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

  BIOS American Megatrends Inc. 2.0b 03/06/2018
  Memory:
    8x Samsung M393A2G40EB2-CTD 16 GB 2 rank 2666

(End of data from sysinfo program)
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#### Compiler Version Notes

```
CC  500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)  
    525.x264_r(base, peak) 557.xz_r(base, peak)

icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

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CC  500.perlbench_r(peak) 502.gcc_r(peak)

icc (ICC) 18.0.0 20170811  
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CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)  
    541.leela_r(base)

icpc (ICC) 18.0.0 20170811  
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CXXC 520.omnetpp_r(peak) 523.xalancbmk_r(peak) 531.deepsjeng_r(peak)  
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icpc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

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FC  548.exchange2_r(base, peak)

ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
```

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#### Base Compiler Invocation

**C benchmarks:**
- icc

**C++ benchmarks:**
- icpc

(Continued on next page)
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Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-W1, -z, muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:
-W1, -z, muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

Fortran benchmarks:
-W1, -z, muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
-L/usr/local/je5.0.1-64/lib -ljemalloc

Base Other Flags

C benchmarks:
-m64 -std=c11

C++ benchmarks:
-m64

(Continued on next page)
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Fortran benchmarks:
-m64

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Peak Compiler Invocation

C benchmarks:

Peak Portability Flags

<table>
<thead>
<tr>
<th>Benchmark</th>
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<tbody>
<tr>
<td>perlbench_r</td>
<td>-DSPEC_LP64 -DSPEC_LINUX_X64</td>
</tr>
<tr>
<td>gcc_r</td>
<td>-D_FILE_OFFSET_BITS=64</td>
</tr>
<tr>
<td>mcf_r</td>
<td>-DSPEC_LP64</td>
</tr>
<tr>
<td>omnetpp_r</td>
<td>-DSPEC_LP64</td>
</tr>
<tr>
<td>xalancbmk_r</td>
<td>-D_FILE_OFFSET_BITS=64 -DSPEC_LINUX</td>
</tr>
<tr>
<td>x264_r</td>
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<tr>
<td>xz_r</td>
<td>-DSPEC_LP64</td>
</tr>
</tbody>
</table>

Peak Optimization Flags

C benchmarks:

500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-fno-strict-overflow -L/usr/local/je5.0.1-64/lib
-ljemalloc

502.gcc_r: -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32
-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-32/lib -ljemalloc

(Continued on next page)
Peak Optimization Flags (Continued)

505.mcf_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

525.x264_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=3 -fno-alias -L/usr/local/je5.0.1-64/lib -ljemalloc

557.xz_r: Same as 505.mcf_r

C++ benchmarks:

520.omnetpp_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc


531.deepsjeng_r: Same as 520.omnetpp_r

541.leela_r: Same as 520.omnetpp_r

Fortran benchmarks:


Peak Other Flags

C benchmarks (except as noted below):

-m64 -std=c11

502.gcc_r: -m32 -std=c11

C++ benchmarks (except as noted below):

-m64

523.xalancbmk_r: -m32
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**Fortran benchmarks:**

`-m64`

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**Peak Other Flags (Continued)**

The flags files that were used to format this result can be browsed at:


You can also download the XML flags sources by saving the following links:


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