# SPEC® CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Epsylon Sp. z o.o. Sp. Komandytowa

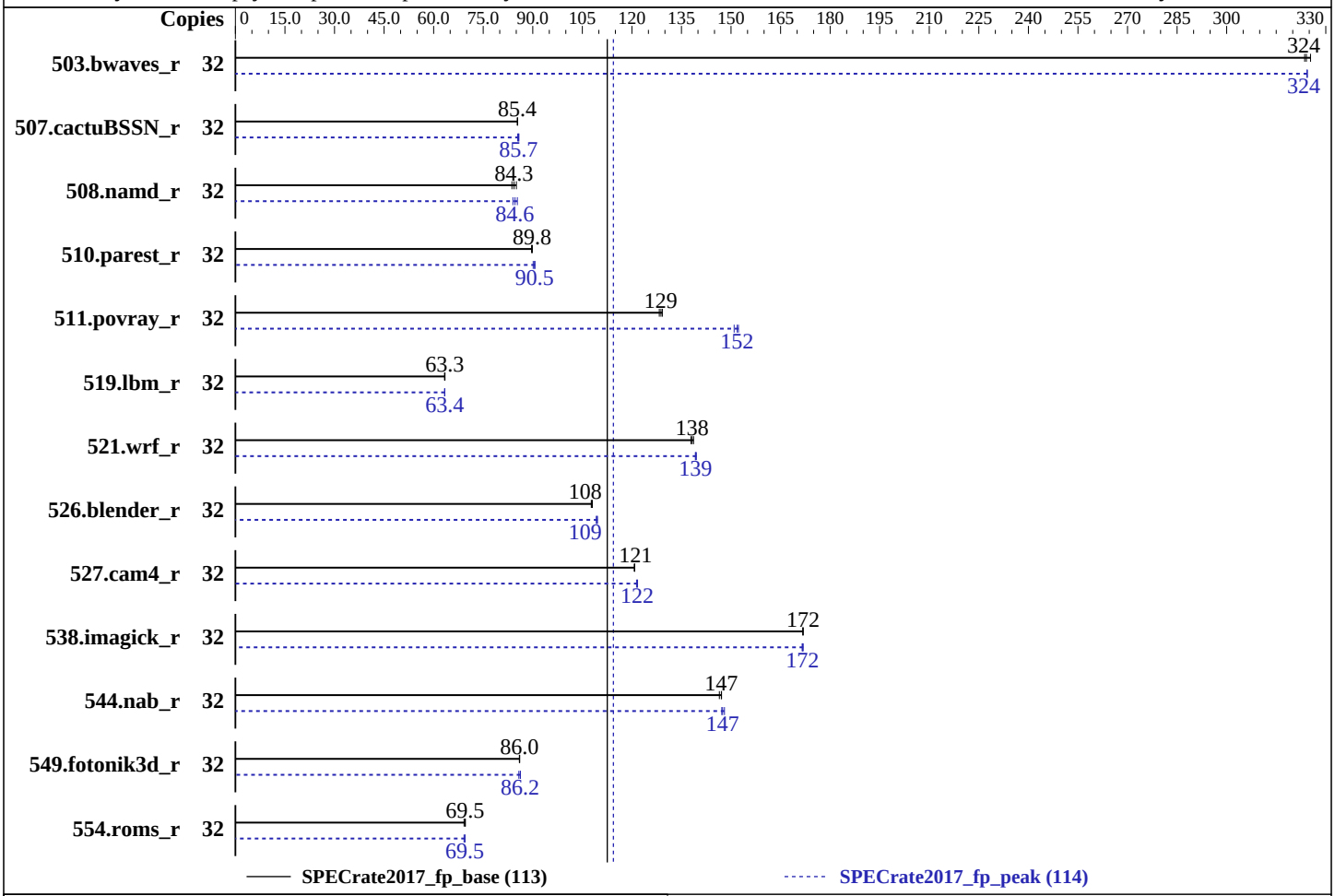
## eterio 210 RE1 (Intel Xeon Gold 6134, 3.20 GHz)

**SPECrate2017_fp_base = 113**

**SPECrate2017_fp_peak = 114**

**CPU2017 License:** 9081
**Test Sponsor:** Epsylon Sp. z o.o. Sp. Komandytowa
**Tested by:** Epsylon Sp. z o.o. Sp. Komandytowa

**Test Date:** Apr-2018
**Hardware Availability:** Sep-2017
**Software Availability:** Mar-2018




## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon Gold 6134 |
| Max MHz.: | 3700 |
| Nominal: | 3200 |
| Enabled: | 16 cores, 2 chips, 2 threads/core |
| Orderable: | 1,2 chips |
| Cache L1: | 32 KB I + 32 KB D on chip per core |
| L2: | 1 MB I+D on chip per core |
| L3: | 24.75 MB I+D on chip per chip |
| Other: | None |
| Memory: | 128 GB (8 x 16 GB 2Rx4 PC4-2666V-R) |
| Storage: | 1 x 960 GB SSD SATA III |
| Other: | None |

## Software

| | |
|---|---|
| OS: | Red Hat Enterprise Linux Server release 7.4 (Maipo)<br>3.10.0-693.21.1.el7.x86_64 |
| Compiler: | C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux;<br>Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux |
| Parallel: | No |
| Firmware: | Version BIOS 2.0b released Mar-2018 |
| File System: | ext4 |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 64-bit |
| Peak Pointers: | 64-bit |
| Other: | None |

# SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Epsylon Sp. z o.o. Sp. Komandytowa

eterio 210 RE1 (Intel Xeon Gold 6134, 3.20 GHz)

SPECrate2017_fp_base = 113

SPECrate2017_fp_peak = 114

**CPU2017 License:** 9081
**Test Sponsor:** Epsylon Sp. z o.o. Sp. Komandytowa
**Tested by:** Epsylon Sp. z o.o. Sp. Komandytowa

**Test Date:** Apr-2018
**Hardware Availability:** Sep-2017
**Software Availability:** Mar-2018

## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 503.bwaves_r | 32 | 986 | 325 | **990** | **324** | 992 | 324 | 32 | **989** | **324** | 989 | 324 | 989 | 324 |
| 507.cactuBSSN_r | 32 | **474** | **85.4** | 474 | 85.4 | 475 | 85.4 | 32 | 472 | 85.8 | 474 | 85.4 | **473** | **85.7** |
| 508.namd_r | 32 | 358 | 85.0 | 363 | 83.7 | **361** | **84.3** | 32 | **359** | **84.6** | 356 | 85.3 | 362 | 84.0 |
| 510.parest_r | 32 | 932 | 89.9 | 934 | 89.6 | **932** | **89.8** | 32 | 923 | 90.7 | **925** | **90.5** | 929 | 90.1 |
| 511.povray_r | 32 | 582 | 128 | 578 | 129 | **580** | **129** | 32 | 495 | 151 | 491 | 152 | **492** | **152** |
| 519.lbm_r | 32 | 533 | 63.3 | 532 | 63.4 | **532** | **63.3** | 32 | **532** | **63.4** | 532 | 63.4 | 533 | 63.3 |
| 521.wrf_r | 32 | **518** | **138** | 517 | 139 | 520 | 138 | 32 | 514 | 140 | 515 | 139 | **515** | **139** |
| 526.blender_r | 32 | **452** | **108** | 453 | 108 | 451 | 108 | 32 | **446** | **109** | 446 | 109 | 445 | 110 |
| 527.cam4_r | 32 | 463 | 121 | 464 | 121 | **463** | **121** | 32 | **460** | **122** | 461 | 121 | 460 | 122 |
| 538.imagick_r | 32 | 463 | 172 | **463** | **172** | 463 | 172 | 32 | 463 | 172 | 464 | 172 | **464** | **172** |
| 544.nab_r | 32 | 366 | 147 | **366** | **147** | 368 | 146 | 32 | **365** | **147** | 364 | 148 | 366 | 147 |
| 549.fotonik3d_r | 32 | 1449 | 86.1 | 1451 | 86.0 | **1450** | **86.0** | 32 | 1446 | 86.3 | **1447** | **86.2** | 1454 | 85.8 |
| 554.roms_r | 32 | **732** | **69.5** | 735 | 69.2 | 730 | 69.6 | 32 | 732 | 69.5 | **732** | **69.5** | 735 | 69.2 |

**SPECrate2017_fp_base = 113**

**SPECrate2017_fp_peak = 114**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

## General Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/cpu2017.1.0/lib/ia32:/cpu2017.1.0/lib/intel64:/cpu2017.1.0/je5.0.1-32:/cpu2017.1.0/je5.0.1-64"

 Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32 GB RAM
 memory using Redhat Enterprise Linux 7.4

 Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
 is mitigated in the system as tested and documented.
 Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
 is mitigated in the system as tested and documented.
 Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
 is mitigated in the system as tested and documented.
```

**(Continued on next page)**

## General Notes (Continued)

```
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>       /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
```

## Platform Notes

```
BIOS Settings:
Power Technology = Custom
Turbo Mode = Enable
Enhanced Halt State (C1E) = Disable
CPU C6 report = Disabled
Package C State = No limit
Software Controlled T-States = Disable
Hyper-Threading (All) = Enable
Enforce POR = Disable
Memory Frequency = Auto
Patrol Scrub = Disabled
IMC Interleaving = Auto
SNC = Disabled

Sysinfo program /cpu2017.1.0/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on SUT Thu Apr 12 22:23:19 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
   https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) Gold 6134 CPU @ 3.20GHz
      2  "physical id"s (chips)
      32 "processors"
   cores, siblings (Caution: counting these is hw and system dependent. The following
   excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
      cpu cores : 8
      siblings  : 16
      physical 0: cores 0 2 3 9 16 19 26 27
      physical 1: cores 0 2 3 9 16 19 26 27

From lscpu:
     Architecture:          x86_64
     CPU op-mode(s):        32-bit, 64-bit
```

**(Continued on next page)**

## Platform Notes (Continued)

```
      Byte Order:            Little Endian
      CPU(s):                32
      On-line CPU(s) list:   0-31
      Thread(s) per core:    2
      Core(s) per socket:    8
      Socket(s):             2
      NUMA node(s):          2
      Vendor ID:             GenuineIntel
      CPU family:            6
      Model:                 85
      Model name:            Intel(R) Xeon(R) Gold 6134 CPU @ 3.20GHz
      Stepping:              4
      CPU MHz:               3201.000
      CPU max MHz:           3201.0000
      CPU min MHz:           1200.0000
      BogoMIPS:              6400.00
      Virtualization:        VT-x
      L1d cache:             32K
      L1i cache:             32K
      L2 cache:              1024K
      L3 cache:              25344K
      NUMA node0 CPU(s):     0-7,16-23
      NUMA node1 CPU(s):     8-15,24-31
      Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
      pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
      lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
      aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 fma
      cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
      xsave avx f16c rdrand lahf_lm abm 3dnowprefetch epb cat_l3 cdp_l3 invpcid_single
      intel_pt spec_ctrl ibpb_support tpr_shadow vnmi flexpriority ept vpid fsgsbase
      tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a avx512f avx512dq
      rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1
      cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local dtherm ida arat pln pts

  /proc/cpuinfo cache data
     cache size : 25344 KB

  From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a
  physical chip.
    available: 2 nodes (0-1)
    node 0 cpus: 0 1 2 3 4 5 6 7 16 17 18 19 20 21 22 23
    node 0 size: 65200 MB
    node 0 free: 63063 MB
    node 1 cpus: 8 9 10 11 12 13 14 15 24 25 26 27 28 29 30 31
    node 1 size: 65536 MB
    node 1 free: 63795 MB
    node distances:
```

**(Continued on next page)**

## Platform Notes (Continued)

```
  node   0   1
    0:  10  21
    1:  21  10

From /proc/meminfo
   MemTotal:       131464776 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
   os-release:
      NAME="Red Hat Enterprise Linux Server"
      VERSION="7.4 (Maipo)"
      ID="rhel"
      ID_LIKE="fedora"
      VARIANT="Server"
      VARIANT_ID="server"
      VERSION_ID="7.4"
      PRETTY_NAME="Red Hat Enterprise Linux"
   redhat-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
   system-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:7.4:ga:server

uname -a:
   Linux SUT 3.10.0-693.21.1.el7.x86_64 #1 SMP Fri Feb 23 18:54:16 UTC 2018 x86_64 x86_64
   x86_64 GNU/Linux

run-level 3 Apr 12 14:33

SPEC is set to: /cpu2017.1.0
   Filesystem     Type  Size  Used Avail Use% Mounted on
   /dev/sda1      ext4  825G   85G  699G  11% /

Additional information from dmidecode follows.  WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
  BIOS American Megatrends Inc. 2.0b 03/06/2018
  Memory:
   8x Samsung M393A2G40EB2-CTD 16 GB 2 rank 2666

(End of data from sysinfo program)
```

## Compiler Version Notes

```
==============================================================================
 CC  519.lbm_r(base) 538.imagick_r(base, peak) 544.nab_r(base)
------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------

==============================================================================
CC   519.lbm_r(peak) 544.nab_r(peak)
------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------

==============================================================================
 CXXC 508.namd_r(base) 510.parest_r(base)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------

==============================================================================
CXXC 508.namd_r(peak) 510.parest_r(peak)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------

==============================================================================
 CC  511.povray_r(base) 526.blender_r(base)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------

==============================================================================
CC   511.povray_r(peak) 526.blender_r(peak)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------

==============================================================================
```


**(Continued on next page)**

## Compiler Version Notes (Continued)

```
 FC  507.cactuBSSN_r(base)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------

==============================================================================
FC   507.cactuBSSN_r(peak)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------

==============================================================================
 FC  503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------

==============================================================================
FC   554.roms_r(peak)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------

==============================================================================
 CC  521.wrf_r(base) 527.cam4_r(base)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------

==============================================================================
CC   521.wrf_r(peak) 527.cam4_r(peak)
------------------------------------------------------------------------------
```


**(Continued on next page)**

## Compiler Version Notes (Continued)

```
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------
```

## Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Fortran benchmarks:
`ifort`

Benchmarks using both Fortran and C:
`ifort icc`

Benchmarks using both C and C++:
`icpc icc`

Benchmarks using Fortran, C, and C++:
`icpc icc ifort`

## Base Portability Flags

503.bwaves_r: `-DSPEC_LP64`
507.cactuBSSN_r: `-DSPEC_LP64`
508.namd_r: `-DSPEC_LP64`
510.parest_r: `-DSPEC_LP64`
511.povray_r: `-DSPEC_LP64`
519.lbm_r: `-DSPEC_LP64`
521.wrf_r: `-DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian`
526.blender_r: `-DSPEC_LP64 -DSPEC_LINUX -funsigned-char`
527.cam4_r: `-DSPEC_LP64 -DSPEC_CASE_FLAG`
538.imagick_r: `-DSPEC_LP64`
544.nab_r: `-DSPEC_LP64`
549.fotonik3d_r: `-DSPEC_LP64`
554.roms_r: `-DSPEC_LP64`

## Base Optimization Flags

C benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -nostandard-realloc-lhs
-align array32byte

Benchmarks using both Fortran and C:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -nostandard-realloc-lhs
-align array32byte

Benchmarks using both C and C++:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3

Benchmarks using Fortran, C, and C++:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -nostandard-realloc-lhs
-align array32byte

## Base Other Flags

C benchmarks:
-m64 -std=c11

C++ benchmarks:
-m64

Fortran benchmarks:
-m64

Benchmarks using both Fortran and C:
-m64 -std=c11

Benchmarks using both C and C++:
-m64 -std=c11

**(Continued on next page)**

## Base Other Flags (Continued)

Benchmarks using Fortran, C, and C++:
`-m64 -std=c11`

## Peak Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Fortran benchmarks:
`ifort`

Benchmarks using both Fortran and C:
`ifort icc`

Benchmarks using both C and C++:
`icpc icc`

Benchmarks using Fortran, C, and C++:
`icpc icc ifort`

## Peak Portability Flags

`Same as Base Portability Flags`

## Peak Optimization Flags

C benchmarks:

519.lbm_r: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-ipo -xCORE-AVX512 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3`

538.imagick_r: `-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3`

544.nab_r: `Same as 519.lbm_r`

**(Continued on next page)**

## Peak Optimization Flags (Continued)

C++ benchmarks:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX512 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Fortran benchmarks:

503.bwaves_r: -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3
-nostandard-realloc-lhs -align array32byte

549.fotonik3d_r: Same as 503.bwaves_r

554.roms_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX512
-O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs
-align array32byte

Benchmarks using both Fortran and C:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX512 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

Benchmarks using both C and C++:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX512 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Benchmarks using Fortran, C, and C++:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX512 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

## Peak Other Flags

C benchmarks:
-m64 -std=c11

C++ benchmarks:
-m64

Fortran benchmarks:
-m64

**(Continued on next page)**

## Peak Other Flags (Continued)

Benchmarks using both Fortran and C:
`-m64 -std=c11`

Benchmarks using both C and C++:
`-m64 -std=c11`

Benchmarks using Fortran, C, and C++:
`-m64 -std=c11`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.html`
`http://www.spec.org/cpu2017/flags/Epsylon-Platform-Flags-RevA-Mar-2018-For-Supermicro-Platform.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.xml`
`http://www.spec.org/cpu2017/flags/Epsylon-Platform-Flags-RevA-Mar-2018-For-Supermicro-Platform.xml`


SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.


For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2018-04-12 16:23:19-0400.
Report generated on 2018-10-31 17:45:21 by CPU2017 PS formatter v6067.
Originally published on 2018-05-01.