### Lenovo Global Technology

**ThinkSystem SN550**

(2.10 GHz, Intel Xeon Gold 6130)

<table>
<thead>
<tr>
<th>SPECspeed2017 fp_base</th>
<th>105</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed2017 fp_peak</td>
<td>107</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

| threads | 0 | 20.0 | 40.0 | 60.0 | 80.0 | 100 | 120 | 140 | 160 | 180 | 200 | 220 | 240 | 260 | 280 | 300 | 320 | 340 | 360 | 380 | 400 | 420 | 440 | 460 | 480 |
|---------|---|------|------|------|------|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| 603.bwaves_s | 32 | 129 |
| 607.cactuBSSN_s | 32 | 42.5 | 131 |
| 619.ibm_s | 32 | 81.4 |
| 621.wrf_s | 32 | 86.1 |
| 627.cam4_s | 32 | 73.2 |
| 628.pop2_s | 32 | 63.0 |
| 638.imagick_s | 32 | 89.2 |
| 644.nab_s | 32 | 166 |
| 649.fotonik3d_s | 32 | 80.2 |
| 654.roms_s | 32 | 138 |

**Hardware**

| **CPU Name:** Intel Xeon Gold 6130  
| **Max MHz.:** 3700  
| **Nominal:** 2100  
| **Enabled:** 32 cores, 2 chips  
| **Orderable:** 1.2 chips  
| **Cache L1:** 32 KB I + 32 KB D on chip per core  
| **L2:** 1 MB I+D on chip per core  
| **L3:** 22 MB I+D on chip per chip  
| **Memory:** 768 GB (24 x 32 GB 2Rx4 PC4-2666V-R)  
| **Storage:** 1 x 800 GB SAS SSD  
| **Other:** None |

| **Software**
| **OS:** SUSE Linux Enterprise Server 12 SP2 (x86_64)  
| **Kernel:** 4.4.114-92.64-default  
| **Compiler:** C/C++: Version 18.0.0.128 of Intel C/C++  
| **Compiler for Linux:** Fortran: Version 18.0.0.128 of Intel Fortran  
| **Firmware:** Lenovo BIOS Version IVE113W 1.12 released Feb-2018  
| **File System:** xfs  
| **System State:** Run level 3 (multi-user)  
| **Base Pointers:** 64-bit  
| **Peak Pointers:** 64-bit  
| **Other:** None |
SPEC CPU2017 Floating Point Speed Result

Lenovo Global Technology
ThinkSystem SN550
(2.10 GHz, Intel Xeon Gold 6130)

CPU2017 License: 9017  Test Date:  May-2018
Test Sponsor: Lenovo Global Technology  Hardware Availability: Aug-2017
Tested by: Lenovo Global Technology  Software Availability: Feb-2018

Results Table

<table>
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<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
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<td>619.lbm_s</td>
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<td>621.wrf_s</td>
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<td>162</td>
<td>81.4</td>
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<td>82.4</td>
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<td>627.cam4_s</td>
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<td>73.2</td>
<td>121</td>
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<tr>
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<td>32</td>
<td>191</td>
<td>62.3</td>
<td>191</td>
<td>62.0</td>
<td>190</td>
<td>62.5</td>
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<td>89.2</td>
<td>156</td>
<td>92.2</td>
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<td>644.nab_s</td>
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<td>166</td>
<td>105</td>
<td>166</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
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<td>116</td>
<td>78.9</td>
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<td>114</td>
<td>138</td>
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</tbody>
</table>

SPECspeed2017_fp_base = 105
SPECspeed2017_fp_peak = 107

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017.1.0.2.ic18.0/lib/ia32:/home/cpu2017.1.0.2.ic18.0/lib/intel64"
LD_LIBRARY_PATH = "$LD_LIBRARY_PATH:/home/cpu2017.1.0.2.ic18.0/je5.0.1-32:/home/cpu2017.1.0.2.ic18.0/je5.0.1-64"
OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
    sync; echo 3> /proc/sys/vm/drop_caches
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance
Hyper-Threading set to Disable

(Continued on next page)
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SPECspeed2017_fp_peak = 107
SPECspeed2017_fp_base = 105

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Software Availability: Feb-2018

Platform Notes (Continued)

Adjacent Cache Prefetch set to Disable
MONITOR/MWAIT set to Enable
LLC dead line alloc set to Disable
Sysinfo program /home/cpu2017.1.0.2.ic18.0/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on SN550 Mon May 28 15:02:57 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6130 CPU @ 2.10GHz
            2  "physical id"s (chips)
            32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 16
siblings : 16
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 32
On-line CPU(s) list: 0-31
Thread(s) per core: 1
Core(s) per socket: 16
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6130 CPU @ 2.10GHz
Stepping: 4
CPU MHz: 2095.091
BogoMIPS: 4190.18
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 22528K
NUMA node0 CPU(s): 0-15
NUMA node1 CPU(s): 16-31
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov

(Continued on next page)
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**Platform Notes (Continued)**

```
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmpref eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
fma cx16 xtrr pdcm pcd dca ssse3_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb invpcid_single pln pts
dtherm intel_pt rsb_ctxtsw spec_ctrl retsep kaiser tpr_shadow vmmi fxe>xri>pre tsc_adjust
vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 3rds invpcid rtm cqm mp
axv512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt
xsavec xgetbv1 cqm_11c cqm_occp_11c
```

```
From `numactl --hardware`  WARNING: a numactl 'node' might or might not correspond to a
physical chip.

available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
node 0 size: 386646 MB
node 0 free: 385649 MB
node 1 cpus: 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31
node 1 size: 387040 MB
node 1 free: 386194 MB
node distances:
node 0  1
0: 10  21
1: 21  10
```

```
From `/proc/meminfo`
MemTotal: 792254828 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
```

```
From `/etc/*release*`  /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or release.
# Please check /etc/os-release for details about this release.

os-release:
NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"
```

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**SPEC CPU2017 Floating Point Speed Result**  

**SPECspeed2017_fp_base = 105**  
**SPECspeed2017_fp_peak = 107**

**Platform Notes (Continued)**

```plaintext
uname -a:
    Linux SN550 4.4.114-92.64-default #1 SMP Thu Feb 1 19:18:19 UTC 2018 (c6ce5db) x86_64
    x86_64 x86_64 GNU/Linux

run-level 3 May 28 09:51

SPEC is set to: /home/cpu2017.1.0.2.ic18.0
    Filesystem Type Size Used Avail Use% Mounted on
    /dev/sda4        xfs  687G  170G  518G  25% /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
    BIOS Lenovo -[IVE113W-1.12]- 02/06/2018
Memory:
    24x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666

(End of data from sysinfo program)
```

## Compiler Version Notes

```
---
CC  619.lbm_s(base) 638.imagick_s(base, peak) 644.nab_s(base, peak)
---

icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
---

CC  619.lbm_s(peak)
---

icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
---

FC  607.cactuBSSN_s(base)
---

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
---

(Continued on next page)
```
Lenovo Global Technology
ThinkSystem SN550
(2.10 GHz, Intel Xeon Gold 6130)

Copyright 2017-2018 Standard Performance Evaluation Corporation

SPEC CPU2017 Floating Point Speed Result

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<td>Software Availability</td>
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**Compiler Version Notes (Continued)**

==============================================================================
| FC   | 607.cactuBSSN_s(peak) |
|----------------------------------------------------------------------------|
| icpc (ICC) 18.0.0 20170811 |
| Copyright (C) 1985-2017 Intel Corporation. All rights reserved. |
| icc (ICC) 18.0.0 20170811 |
| Copyright (C) 1985-2017 Intel Corporation. All rights reserved. |
| ifort (IFORT) 18.0.0 20170811 |
| Copyright (C) 1985-2017 Intel Corporation. All rights reserved. |

==============================================================================
| FC  | 603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base) |
|----------------------------------------------------------------------------|
| ifort (IFORT) 18.0.0 20170811 |
| Copyright (C) 1985-2017 Intel Corporation. All rights reserved. |

==============================================================================
| FC   | 603.bwaves_s(peak) 649.fotonik3d_s(peak) 654.roms_s(peak) |
|----------------------------------------------------------------------------|
| ifort (IFORT) 18.0.0 20170811 |
| Copyright (C) 1985-2017 Intel Corporation. All rights reserved. |

==============================================================================
| CC  | 621.wrf_s(base) 627.cam4_s(base, peak) 628.pop2_s(base) |
|----------------------------------------------------------------------------|
| ifort (IFORT) 18.0.0 20170811 |
| Copyright (C) 1985-2017 Intel Corporation. All rights reserved. |
| icc (ICC) 18.0.0 20170811 |
| Copyright (C) 1985-2017 Intel Corporation. All rights reserved. |

==============================================================================
| CC   | 621.wrf_s(peak) 628.pop2_s(peak) |
|----------------------------------------------------------------------------|
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SPEC CPU2017 Floating Point Speed Result

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Test Date: May-2018
Hardware Availability: Aug-2017
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Base Compiler Invocation

C benchmarks:
  icc

Fortran benchmarks:
  ifort

Benchmarks using both Fortran and C:
  ifort icc

Benchmarks using Fortran, C, and C++:
  icpc icc ifort

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cmp4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
  -assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
  -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
  -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP

Fortran benchmarks:
  -DSPEC_OPENMP -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
  -ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
  -nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:
  -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
  -qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
  -nostandard-realloc-lhs -align array32byte

(Continued on next page)
## Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++:
- `-xCORE-AVX2`
- `-ipo`
- `-O3`
- `-no-prec-div`
- `-qopt-prefetch`
- `-ffinite-math-only`
- `-qopt-mem-layout-trans=3`
- `-qopenmp`
- `-DSPEC_OPENMP`
- `-nostandard-realloc-lhs`
- `-align array32byte`

## Base Other Flags

C benchmarks:
- `-m64`
- `-std=c11`

Fortran benchmarks:
- `-m64`

Benchmarks using both Fortran and C:
- `-m64`
- `-std=c11`

Benchmarks using Fortran, C, and C++:
- `-m64`
- `-std=c11`

## Peak Compiler Invocation

C benchmarks:
- `icc`

Fortran benchmarks:
- `ifort`

Benchmarks using both Fortran and C:
- `ifort icc`

Benchmarks using Fortran, C, and C++:
- `icpc icc ifort`

## Peak Portability Flags

Same as Base Portability Flags
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### Peak Optimization Flags

**C benchmarks:**

619.lbm_s:  
-prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2  
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div  
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp  
-DSPEC_OPENMP

638.imagick_s:  
-ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp  
-DSPEC_OPENMP

644.nab_s: Same as 638.imagick_s

**Fortran benchmarks:**

-prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP  
-DSPEC_OPENMP -O2 -xCORE-AVX2 -qopt-prefetch -ipo -O3  
-ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3 -qopenmp  
-nostandard-realloc-lhs -align array32byte

**Benchmarks using both Fortran and C:**

621.wrf_s:  
-prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2  
-qopt-prefetch -ipo -O3 -ffinite-math-only -no-prec-div  
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp  
-DSPEC_OPENMP -nastandard-realloc-lhs -align array32byte

627.cam4_s:  
-ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp  
-DSPEC_OPENMP -nastandard-realloc-lhs -align array32byte

628.pop2_s: Same as 621.wrf_s

**Benchmarks using Fortran, C, and C++:**

-prof-gen(pass 1) -prof-use(pass 2) -O2 -xCORE-AVX2 -qopt-prefetch  
-ipo -O3 -ffinite-math-only -no-prec-div -qopt-mem-layout-trans=3  
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -nastandard-realloc-lhs  
-align array32byte

### Peak Other Flags

**C benchmarks:**

-m64 -std=c11

(Continued on next page)
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Peak Other Flags (Continued)

Fortran benchmarks:
- m64

Benchmarks using both Fortran and C:
- m64 -std=c11

Benchmarks using Fortran, C, and C++:
- m64 -std=c11

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-C.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-SKL-C.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2018-05-28 03:02:57-0400.