## SPEC® CPU2017 Integer Rate Result

### Hardware

- **CPU Name:** Intel Xeon Silver 4112  
- **Max MHz.:** 3000  
- **Nominal:** 2600  
- **Enabled:** 8 cores, 2 chips, 2 threads/core  
- **Orderable:** 1,2 chips  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **L2:** 1 MB I+D on chip per core  
- **L3:** 8.25 MB I+D on chip per chip  
- **Other:** None  
- **Memory:** 384 GB (12 x 32 GB 2Rx4 PC4-2666V-R, running at 2400)  
- **Storage:** 1 x 1200 GB SAS, 10000 RPM  
- **Other:** None

### Software

- **OS:** Red Hat Enterprise Linux Server release 7.3 (Maipo) 3.10.0-693.11.6.el7.x86_64  
- **Compiler:** C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux; Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux  
- **Parallel:** No  
- **Firmware:** Version 0.59 Released Feb-2018  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 32/64-bit  
- **Other:** jemalloc: jemalloc memory allocator library V5.0.1

### Benchmark Results

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>SPECrate2017_int_base</th>
<th>SPECrate2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>perlbench_r</td>
<td>16</td>
<td>40.5</td>
<td>46.6</td>
</tr>
<tr>
<td>gcc_r</td>
<td>16</td>
<td>38.2</td>
<td>38.2</td>
</tr>
<tr>
<td>mcf_r</td>
<td>16</td>
<td>45.0</td>
<td>45.0</td>
</tr>
<tr>
<td>omnetpp_r</td>
<td>16</td>
<td>27.7</td>
<td>27.7</td>
</tr>
<tr>
<td>xalancbmk_r</td>
<td>16</td>
<td>56.1</td>
<td>56.1</td>
</tr>
<tr>
<td>x264_r</td>
<td>16</td>
<td>35.6</td>
<td>35.6</td>
</tr>
<tr>
<td>deepsjeng_r</td>
<td>16</td>
<td>37.9</td>
<td>37.9</td>
</tr>
<tr>
<td>leela_r</td>
<td>16</td>
<td>34.3</td>
<td>34.3</td>
</tr>
<tr>
<td>exchange2_r</td>
<td>16</td>
<td>81.5</td>
<td>81.5</td>
</tr>
<tr>
<td>xz_r</td>
<td>16</td>
<td>31.8</td>
<td>31.8</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 3175  
**Test Date:** Jun-2018  
**Test Sponsor:** Huawei  
**Hardware Availability:** Jul-2017  
**Software Availability:** Jan-2018
Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>perlbench</td>
<td>16</td>
<td>804</td>
<td>31.7</td>
<td>775</td>
<td>32.9</td>
<td>775</td>
<td>32.9</td>
<td>16</td>
<td>630</td>
<td>40.5</td>
<td>631</td>
<td>40.3</td>
<td>629</td>
<td>40.5</td>
</tr>
<tr>
<td>gcc</td>
<td>16</td>
<td>596</td>
<td>38.0</td>
<td>593</td>
<td>38.2</td>
<td>588</td>
<td>38.5</td>
<td>16</td>
<td>505</td>
<td>44.9</td>
<td>502</td>
<td>45.1</td>
<td>503</td>
<td>45.0</td>
</tr>
<tr>
<td>mcf</td>
<td>16</td>
<td>465</td>
<td>55.6</td>
<td>460</td>
<td>56.2</td>
<td>461</td>
<td>56.1</td>
<td>16</td>
<td>465</td>
<td>56.2</td>
<td>460</td>
<td>56.2</td>
<td>461</td>
<td>56.1</td>
</tr>
<tr>
<td>omnetpp</td>
<td>16</td>
<td>770</td>
<td>27.3</td>
<td>757</td>
<td>27.7</td>
<td>754</td>
<td>27.9</td>
<td>16</td>
<td>746</td>
<td>28.1</td>
<td>750</td>
<td>28.0</td>
<td>749</td>
<td>28.0</td>
</tr>
<tr>
<td>xalancbmk</td>
<td>16</td>
<td>375</td>
<td>45.1</td>
<td>365</td>
<td>46.3</td>
<td>370</td>
<td>45.7</td>
<td>16</td>
<td>304</td>
<td>55.7</td>
<td>304</td>
<td>55.6</td>
<td>304</td>
<td>55.5</td>
</tr>
<tr>
<td>x264</td>
<td>16</td>
<td>335</td>
<td>83.7</td>
<td>327</td>
<td>85.7</td>
<td>333</td>
<td>84.2</td>
<td>16</td>
<td>320</td>
<td>87.6</td>
<td>320</td>
<td>87.6</td>
<td>321</td>
<td>87.3</td>
</tr>
<tr>
<td>deepsjeng</td>
<td>16</td>
<td>484</td>
<td>37.9</td>
<td>484</td>
<td>37.9</td>
<td>484</td>
<td>37.9</td>
<td>16</td>
<td>484</td>
<td>37.9</td>
<td>484</td>
<td>37.9</td>
<td>484</td>
<td>37.9</td>
</tr>
<tr>
<td>leela</td>
<td>16</td>
<td>772</td>
<td>34.3</td>
<td>772</td>
<td>34.3</td>
<td>781</td>
<td>33.9</td>
<td>16</td>
<td>754</td>
<td>35.1</td>
<td>756</td>
<td>35.1</td>
<td>755</td>
<td>35.1</td>
</tr>
<tr>
<td>exchange2</td>
<td>16</td>
<td>512</td>
<td>81.9</td>
<td>515</td>
<td>81.4</td>
<td>514</td>
<td>81.5</td>
<td>16</td>
<td>512</td>
<td>81.9</td>
<td>514</td>
<td>81.5</td>
<td>518</td>
<td>80.9</td>
</tr>
<tr>
<td>xz</td>
<td>16</td>
<td>543</td>
<td>31.8</td>
<td>543</td>
<td>31.8</td>
<td>542</td>
<td>31.9</td>
<td>16</td>
<td>541</td>
<td>31.9</td>
<td>542</td>
<td>31.9</td>
<td>544</td>
<td>31.8</td>
</tr>
</tbody>
</table>

SPECrate2017_int_base = 43.7
SPECrate2017_int_peak = 46.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/spec/lib/ia32:/spec/lib/intel64:/spec/je5.0.1-32:/spec/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>/proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
jemalloc: configured and built at default for 32bit (i686) and 64bit (x86_64) targets;
jemalloc: built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5;
jemalloc: sources available from jemalloc.net or

(Continued on next page)
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Huawei XH321 V5 (Intel Xeon Silver 4112)

SPEC CPU2017 Integer Rate Result

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Huawei XH321 V5 (Intel Xeon Silver 4112)

SPECrate2017_int_base = 43.7

SPECrate2017_int_peak = 46.6

CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

Test Date: Jun-2018
Hardware Availability: Jul-2017
Software Availability: Jan-2018

General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Power Policy Set to Performance
SNC Set to Enabled
IMC Interleaving Set to 1-way Interleave
XPT Prefetch Set to Enabled
ADDDC Sparing Set to Disabled
Sysinfo program /spec/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on localhost.localdomain Sat Jun 16 02:09:20 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Silver 4112 CPU @ 2.60GHz
  2 "physical id"s (chips)
  16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 4
siblings   : 8
physical 0: cores 0 2 3 4
physical 1: cores 0 1 4 5

From lscpu:

Architecture:       x86_64
CPU op-mode(s):     32-bit, 64-bit
Byte Order:         Little Endian
CPU(s):             16
On-line CPU(s) list: 0-15
Thread(s) per core: 2
Core(s) per socket: 4
Socket(s):          2
NUMA node(s):       2
Vendor ID:          GenuineIntel

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Platform Notes (Continued)

CPU family:  6
Model:  85
Model name: Intel(R) Xeon(R) Silver 4112 CPU @ 2.60GHz
Stepping:  4
CPU MHz: 2600.000
BogoMIPS: 5205.53
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 8448K
NUMA node0 CPU(s): 0-3,8-11
NUMA node1 CPU(s): 4-7,12-15

/platform/cpuinfo cache data
  cache size : 8448 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
  available: 2 nodes (0-1)
  node 0 cpus: 0 1 2 3 8 9 10 11
  node 0 size: 195701 MB
  node 0 free: 190930 MB
  node 1 cpus: 4 5 6 7 12 13 14 15
  node 1 size: 196608 MB
  node 1 free: 191904 MB
  node distances:
    node 0 1
    0:  10  21
    1:  21  10

From /proc/meminfo
  MemTotal: 395141652 kB
  HugePages_Total: 0
  Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
  os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.3 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="7.3"
    PRETTY_NAME="Red Hat Enterprise Linux Server 7.3 (Maipo)"
    ANSI_COLOR="0;31"
    CPE_NAME="cpe:/o:redhat:enterprise_linux:7.3:GA:server"
    redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)

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Huawei

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Tested by: Huawei

Platform Notes (Continued)

- system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)

```
uname -a:
Linux localhost.localdomain 3.10.0-693.11.6.el7.x86_64 #1 SMP Thu Dec 28 14:23:39 EST 2017 x86_64 x86_64 x86_64 GNU/Linux
run-level 3 Jun 16 02:06
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

- BIOS INSYDE Corp. 0.59 02/24/2018
- Memory:
  - 4x NO DIMM NO DIMM
  - 12x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666, configured at 2400

(End of data from sysinfo program)

Compiler Version Notes

```
---
CC  500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
   525.x264_r(base, peak) 557.xz_r(base, peak)
---
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
---
CC  500.perlbench_r(peak) 502.gcc_r(peak)
---
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
---
CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)
   541.leela_r(base)
---
icpc (ICC) 18.0.0 20170811
```

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Compiler Version Notes (Continued)

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------------------------------------------------------------------------------
==============================================================================
CXXC 520.omnetpp_r(peak) 523.xalancbmk_r(peak) 531.deepsjeng_r(peak) 541.leela_r(peak)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
==============================================================================
FC  548.exchange2_r(base, peak)
------------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
## SPEC CPU2017 Integer Rate Result

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<tr>
<th>Huawei XH321 V5 (Intel Xeon Silver 4112)</th>
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</tr>
</thead>
<tbody>
<tr>
<td><strong>CPU2017 License:</strong> 3175</td>
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<td></td>
</tr>
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<td></td>
</tr>
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</table>

### Base Optimization Flags

**C benchmarks:**
- `-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div`
- `-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc`

**C++ benchmarks:**
- `-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div`
- `-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc`

**Fortran benchmarks:**
- `-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div`
- `-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte`
- `-L/usr/local/je5.0.1-64/lib -ljemalloc`

### Base Other Flags

**C benchmarks:**
- `-m64 -std=c11`

**C++ benchmarks:**
- `-m64`

**Fortran benchmarks:**
- `-m64`

### Peak Compiler Invocation

**C benchmarks:**
- `icc`

**C++ benchmarks:**
- `icpc`

**Fortran benchmarks:**
- `ifort`

### Peak Portability Flags

- `500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64`
- `502.gcc_r: -D_FILE_OFFSET_BITS=64`

(Continued on next page)
Peak Portability Flags (Continued)

505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:

500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-fno-strict-overflow -L/usr/local/je5.0.1-64/lib
-ljemalloc

502.gcc_r: -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32
-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-32/lib -ljemalloc

505.mcf_r: basepeak = yes

525.x264_r: -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -fno-alias
-L/usr/local/je5.0.1-64/lib -ljemalloc

557.xz_r: -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib
-ljemalloc

C++ benchmarks:

520.omnetpp_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-64/lib -ljemalloc

523.xalancbmk_r: -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32
-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-32/lib -ljemalloc

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## Huawei

**Huawei XH321 V5 (Intel Xeon Silver 4112)**

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</thead>
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</tr>
</tbody>
</table>

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</tr>
</thead>
<tbody>
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</tr>
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</table>

**CPU2017 License:** 3175

**Test Sponsor:** Huawei

**Tested by:** Huawei

### Peak Optimization Flags (Continued)

531.deepsjeng_r: basepeak = yes

541.leela_r: Same as 520.omnetpp_r

Fortran benchmarks:
- `-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div`
- `-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte`
- `-L/usr/local/je5.0.1-64/lib -ljemalloc`

### Peak Other Flags

C benchmarks (except as noted below):
- `-m64 -std=c11`

502.gcc_r: `-m32 -std=c11`

C++ benchmarks (except as noted below):
- `-m64`

523.xalancbmk_r: `-m32`

Fortran benchmarks:
- `-m64`

The flags files that were used to format this result can be browsed at

- [http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html](http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html)

You can also download the XML flags sources by saving the following links:

- [http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml](http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml)

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2018-06-16 02:09:19-0400.


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