**Huawei XH321 V5 (Intel Xeon Silver 4112)**

**CPU2017 License:** 3175  
**Test Sponsor:** Huawei  
**Tested by:** Huawei  
**Test Date:** Jun-2018  
**Hardware Availability:** Jul-2017  
**Software Availability:** Jan-2018

**SPECrate2017_fp_base = 50.3**  
**SPECrate2017_fp_peak = 51.9**

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>SPECrate2017_fp_base</th>
<th>SPECrate2017_fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>16</td>
<td>40.5</td>
<td></td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>16</td>
<td>33.6</td>
<td></td>
</tr>
<tr>
<td>508.namd_r</td>
<td>16</td>
<td>33.8</td>
<td></td>
</tr>
<tr>
<td>510.parest_r</td>
<td>16</td>
<td>38.4</td>
<td></td>
</tr>
<tr>
<td>511.povray_r</td>
<td>16</td>
<td>40.0</td>
<td></td>
</tr>
<tr>
<td>519.ibm_r</td>
<td>16</td>
<td>36.6</td>
<td></td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>16</td>
<td>40.5</td>
<td></td>
</tr>
<tr>
<td>526.blender_r</td>
<td>16</td>
<td>46.1</td>
<td></td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>16</td>
<td>39.3</td>
<td></td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>16</td>
<td>43.4</td>
<td></td>
</tr>
<tr>
<td>544.nab_r</td>
<td>16</td>
<td>67.1</td>
<td></td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>16</td>
<td>54.5</td>
<td></td>
</tr>
<tr>
<td>554.roms_r</td>
<td>16</td>
<td>34.5</td>
<td></td>
</tr>
</tbody>
</table>

### Hardware

- **CPU Name:** Intel Xeon Silver 4112  
- **Max MHZ.:** 3000  
- **Nominal:** 2600  
- **Enabled:** 8 cores, 2 chips, 2 threads/core  
- **Orderable:** 1.2 chips  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **Cache L2:** 1 MB I+D on chip per core  
- **Cache L3:** 8.25 MB I+D on chip per chip  
- **Other:** None  
- **Memory:** 384 GB (12 x 32 GB 2Rx4 PC4-2666V-R, running at 2400)  
- **Storage:** 1 x 1200 GB SAS, 10000 RPM  
- **Other:** None

### Software

- **OS:** Red Hat Enterprise Linux Server release 7.3  
  (Maipo)  
- **Compiler:** C/C++: Version 18.0.0.128 of Intel C/C++  
  Compiler for Linux;  
  Fortran: Version 18.0.0.128 of Intel Fortran  
  Compiler for Linux  
- **Parallel:** No  
- **Firmware:** Version 0.59 Released Feb-2018  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 64-bit  
- **Other:** None
RESULTS

SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Huawei

Huawei XH321 V5 (Intel Xeon Silver 4112)

SPECrate2017_fp_base = 50.3

SPECrate2017_fp_peak = 51.9

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>16</td>
<td>861</td>
<td>186</td>
<td>906</td>
<td>177</td>
<td>906</td>
<td>177</td>
<td>906</td>
<td>177</td>
<td>906</td>
<td>177</td>
<td>906</td>
<td>177</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>16</td>
<td>501</td>
<td>40.5</td>
<td>503</td>
<td>40.3</td>
<td>501</td>
<td>40.5</td>
<td>503</td>
<td>40.3</td>
<td>503</td>
<td>40.3</td>
<td>503</td>
<td>40.3</td>
</tr>
<tr>
<td>508.namd_r</td>
<td>16</td>
<td>451</td>
<td>33.7</td>
<td>456</td>
<td>33.3</td>
<td>451</td>
<td>33.7</td>
<td>456</td>
<td>33.3</td>
<td>456</td>
<td>33.3</td>
<td>456</td>
<td>33.3</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>16</td>
<td>1113</td>
<td>37.6</td>
<td>1085</td>
<td>38.6</td>
<td>1090</td>
<td>38.4</td>
<td>1090</td>
<td>38.4</td>
<td>1090</td>
<td>38.4</td>
<td>1090</td>
<td>38.4</td>
</tr>
<tr>
<td>511.povray_r</td>
<td>16</td>
<td>687</td>
<td>54.4</td>
<td>690</td>
<td>54.1</td>
<td>689</td>
<td>54.2</td>
<td>689</td>
<td>54.2</td>
<td>689</td>
<td>54.2</td>
<td>689</td>
<td>54.2</td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>16</td>
<td>469</td>
<td>36.8</td>
<td>466</td>
<td>36.6</td>
<td>466</td>
<td>36.6</td>
<td>466</td>
<td>36.6</td>
<td>466</td>
<td>36.6</td>
<td>466</td>
<td>36.6</td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>16</td>
<td>661</td>
<td>54.2</td>
<td>668</td>
<td>53.6</td>
<td>662</td>
<td>54.1</td>
<td>662</td>
<td>54.1</td>
<td>662</td>
<td>54.1</td>
<td>662</td>
<td>54.1</td>
</tr>
<tr>
<td>526.blender_r</td>
<td>16</td>
<td>529</td>
<td>46.1</td>
<td>528</td>
<td>46.2</td>
<td>530</td>
<td>46.0</td>
<td>530</td>
<td>46.0</td>
<td>530</td>
<td>46.0</td>
<td>530</td>
<td>46.0</td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>16</td>
<td>713</td>
<td>39.3</td>
<td>710</td>
<td>39.4</td>
<td>715</td>
<td>39.2</td>
<td>715</td>
<td>39.2</td>
<td>715</td>
<td>39.2</td>
<td>715</td>
<td>39.2</td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>16</td>
<td>593</td>
<td>67.1</td>
<td>593</td>
<td>67.1</td>
<td>593</td>
<td>67.1</td>
<td>593</td>
<td>67.1</td>
<td>593</td>
<td>67.1</td>
<td>593</td>
<td>67.1</td>
</tr>
<tr>
<td>544.nab_r</td>
<td>16</td>
<td>461</td>
<td>58.4</td>
<td>460</td>
<td>58.5</td>
<td>462</td>
<td>58.2</td>
<td>462</td>
<td>58.2</td>
<td>462</td>
<td>58.2</td>
<td>462</td>
<td>58.2</td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>16</td>
<td>1135</td>
<td>54.9</td>
<td>1143</td>
<td>54.5</td>
<td>1164</td>
<td>53.6</td>
<td>1164</td>
<td>53.6</td>
<td>1164</td>
<td>53.6</td>
<td>1164</td>
<td>53.6</td>
</tr>
<tr>
<td>554.roms_r</td>
<td>16</td>
<td>737</td>
<td>34.5</td>
<td>730</td>
<td>34.8</td>
<td>741</td>
<td>34.3</td>
<td>741</td>
<td>34.3</td>
<td>741</td>
<td>34.3</td>
<td>741</td>
<td>34.3</td>
</tr>
</tbody>
</table>

SPECrate2017_fp_base = 50.3

SPECrate2017_fp_peak = 51.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor.

For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/spec/lib/ia32:/spec/lib/intel64:/spec/je5.0.1-32:/spec/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

(Continued on next page)
 SPEC CPU2017 Floating Point Rate Result

Huawei

Huawei XH321 V5 (Intel Xeon Silver 4112)

<table>
<thead>
<tr>
<th>SPECrate2017_fp_base = 50.3</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate2017_fp_peak = 51.9</td>
</tr>
</tbody>
</table>

CPU2017 License: 3175
Test Sponsor: Huawei
Test Date: Jun-2018
Tested by: Huawei
Hardware Availability: Jul-2017
Software Availability: Jan-2018

General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Power Policy Set to Performance
SNC Set to Enabled
IMC Interleaving Set to 1-way Interleave
XPT Prefetch Set to Enabled
ADDDC Sparing Set to Disabled
Sysinfo program /spec/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on localhost.localdomain Sat Jun 16 11:26:15 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Silver 4112 CPU @ 2.60GHz
  2 "physical id"s (chips)
  16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 4
  siblings : 8
physical 0: cores 0 2 3 4
physical 1: cores 0 1 4 5

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 16
On-line CPU(s) list: 0-15
Thread(s) per core: 2
Core(s) per socket: 4
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Silver 4112 CPU @ 2.60GHz

(Continued on next page)
Huawei

Huawei XH321 V5 (Intel Xeon Silver 4112)

<table>
<thead>
<tr>
<th>SPECrate2017_fp_base</th>
<th>50.3</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate2017_fp_peak</td>
<td>51.9</td>
</tr>
</tbody>
</table>

CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

Platform Notes (Continued)

Stepping: 4
CPU MHz: 2600.000
BogoMIPS: 5205.53
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 8448K
NUMA node0 CPU(s): 0-3,8-11
NUMA node1 CPU(s): 4-7,12-15

/proc/cpuinfo cache data
cache size : 8448 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 8 9 10 11
node 0 size: 195701 MB
node 0 free: 190388 MB
node 1 cpus: 4 5 6 7 12 13 14 15
node 1 size: 196608 MB
node 1 free: 191437 MB
node distances:
  node   0   1
  0:  10  21
  1:  21  10

From /proc/meminfo
MemTotal:       395141652 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
os-release:
  NAME="Red Hat Enterprise Linux Server"
  VERSION="7.3 (Maipo)"
  ID="rhel"
  ID_LIKE="fedora"
  VERSION_ID="7.3"
  PRETTY_NAME="Red Hat Enterprise Linux Server 7.3 (Maipo)"
  ANSI_COLOR="0;31"
  CPE_NAME="cpe:/o:redhat:enterprise_linux:7.3:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)

(Continued on next page)
Huawei

Huawei XH321 V5 (Intel Xeon Silver 4112)

SPEC CPU2017 Floating Point Rate Result

SPECrate2017_fp_base = 50.3
SPECrate2017_fp_peak = 51.9

CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

Test Date: Jun-2018
Hardware Availability: Jul-2017
Software Availability: Jan-2018

Platform Notes (Continued)

uname -a:
    Linux localhost.localdomain 3.10.0-693.11.6.el7.x86_64 #1 SMP Thu Dec 28 14:23:39 EST 2017 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jun 16 02:06

SPEC is set to: /spec

Filesystem Type Size Used Avail Use% Mounted on
/dev/sda8 xfs 325G 30G 296G 10% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS INSYDE Corp. 0.59 02/24/2018
Memory:
    4x NO DIMM NO DIMM
    12x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666, configured at 2400

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
CC  519.lbm_r(base) 538.imagick_r(base, peak) 544.nab_r(base)
------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
CXXC 508.namd_r(base) 510.parest_r(base)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

(Continued on next page)
Huawei
Huawei XH321 V5 (Intel Xeon Silver 4112)

SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

SPECrate2017_fp_base = 50.3
SPECrate2017_fp_peak = 51.9

CPU2017 License: 3175
Test Date: Jun-2018
Test Sponsor: Huawei
Hardware Availability: Jul-2017
Tested by: Huawei
Software Availability: Jan-2018

Compiler Version Notes (Continued)

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
CC 511.povray_r(base) 526.blender_r(base)
==============================================================================

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
iccc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
CC 511.povray_r(peak) 526.blender_r(peak)
==============================================================================

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
iccc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
FC 507.cactuBSSN_r(base)
==============================================================================

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
iccc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
FC 507.cactuBSSN_r(peak)
==============================================================================

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
iccc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
FC 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base)
(Continued on next page)
Huawei

Huawei XH321 V5 (Intel Xeon Silver 4112)

<table>
<thead>
<tr>
<th>SPECrate2017_fp_peak</th>
<th>SPECrate2017_fp_base</th>
</tr>
</thead>
<tbody>
<tr>
<td>51.9</td>
<td>50.3</td>
</tr>
</tbody>
</table>

CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei
Test Date: Jun-2018
Hardware Availability: Jul-2017
Software Availability: Jan-2018

Compiler Version Notes (Continued)

ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
FC 554.roms_r(peak)

ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
CC 521.wrf_r(base) 527.cam4_r(base)

ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
CC 521.wrf_r(peak) 527.cam4_r(peak)

ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using both C and C++:
icpc icc

(Continued on next page)
SPEC CPU2017 Floating Point Rate Result

Huawei

Huawei XH321 V5 (Intel Xeon Silver 4112)

<table>
<thead>
<tr>
<th>SPECrate2017_fp_base</th>
<th>50.3</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate2017_fp_peak</td>
<td>51.9</td>
</tr>
</tbody>
</table>

CPU2017 License: 3175
Test Sponsor: Huawei
Test Date: Jun-2018
Hardware Availability: Jul-2017
Tested by: Huawei
Software Availability: Jan-2018

Base Compiler Invocation (Continued)

Benchmarks using Fortran, C, and C++:

icpc icc ifort

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

Benchmarks using both C and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=3

(Continued on next page)
SPEC CPU2017 Floating Point Rate Result

Huawei
Huawei XH321 V5 (Intel Xeon Silver 4112)

SPECrate2017_fp_base = 50.3
SPECrate2017_fp_peak = 51.9

CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

Test Date: Jun-2018
Hardware Availability: Jul-2017
Software Availability: Jan-2018

Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

Base Other Flags

C benchmarks:
-m64 -std=c11

C++ benchmarks:
-m64

Fortran benchmarks:
-m64

Benchmarks using both Fortran and C:
-m64 -std=c11

Benchmarks using both C and C++:
-m64 -std=c11

Benchmarks using Fortran, C, and C++:
-m64 -std=c11

Peak Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using both C and C++:
icpc icc

(Continued on next page)
Peak Compiler Invocation (Continued)

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Peak Portability Flags
Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
519.lbm_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

538.imagick_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3

544.nab_r: Same as 519.lbm_r

C++ benchmarks:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Fortran benchmarks:
503.bwaves_r: basepeak = yes
549.fotonik3d_r: basepeak = yes

554.roms_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs
-align array32byte

Benchmarks using both Fortran and C:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

(Continued on next page)
Huawei

Huawei XH321 V5 (Intel Xeon Silver 4112)

<table>
<thead>
<tr>
<th>SPECrate2017_fp_base</th>
<th>50.3</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate2017_fp_peak</td>
<td>51.9</td>
</tr>
</tbody>
</table>

CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

Test Date: Jun-2018
Hardware Availability: Jul-2017
Software Availability: Jan-2018

Peak Optimization Flags (Continued)

Benchmarks using both C and C++:
- prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
- no-prec-div -qopt-prefetch -ffinite-math-only
- qopt-mem-layout-trans=3

Benchmarks using Fortran, C, and C++:
507.cactuBSSN_r: basepeak = yes

Peak Other Flags

C benchmarks:
- m64 -std=c11

C++ benchmarks:
- m64

Fortran benchmarks:
- m64

Benchmarks using both Fortran and C:
- m64 -std=c11

Benchmarks using both C and C++:
- m64 -std=c11

Benchmarks using Fortran, C, and C++:
- m64 -std=c11

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml
http://www.spec.org/cpu2017/flags/Huawei-Platform-Settings-SKL-V1.9-revC.xml
Huawei

Huawei XH321 V5 (Intel Xeon Silver 4112)

<table>
<thead>
<tr>
<th>SPECrate2017_fp_base</th>
<th>50.3</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate2017_fp_peak</td>
<td>51.9</td>
</tr>
</tbody>
</table>

CPU2017 License: 3175
Test Date: Jun-2018
Test Sponsor: Huawei
Hardware Availability: Jul-2017
Tested by: Huawei
Software Availability: Jan-2018

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2018-06-16 11:26:13-0400.
Report generated on 2018-10-31 19:00:54 by CPU2017 PDF formatter v6067.
Originally published on 2018-07-10.