**Huawei**

**Huawei 5288 V5 (Intel Xeon Bronze 3104)**

**CPU2017 License:** 3175  
**Test Sponsor:** Huawei  
**Tested by:** Huawei  
**Test Date:** Aug-2018  
**Hardware Availability:** Jul-2017  
**Software Availability:** Jan-2018

<table>
<thead>
<tr>
<th>SPECrate2017_fp_base = 46.3</th>
<th>SPECrate2017_fp_peak = 47.6</th>
</tr>
</thead>
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</tr>
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<td>Huawei 5288 V5 (Intel Xeon Bronze 3104)</td>
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</tr>
</tbody>
</table>

### Hardware

- **CPU Name:** Intel Xeon Bronze 3104  
- **Max MHz.:** 1700  
- **Nominal:** 1700  
- **Enabled:** 12 cores, 2 chips  
- **Orderable:** 1,2 chips  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **Cache L2:** 1 MB I+D on chip per core  
- **Cache L3:** 8.25 MB I+D on chip per chip  
- **Memory:** 384 GB (24 x 16 GB 2Rx8 PC4-2666V-R, running at 2133)  
- **Storage:** 1 x 1200 GB SAS, 10000 RPM  
- **Other:** None

### Software

- **OS:** Red Hat Enterprise Linux Server release 7.4 (Maipo)  
- **Compiler:** C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux; Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux  
- **Parallel:** No  
- **Firmware:** Version 0.62 Released Mar-2018  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 64-bit  
- **Other:** None
Huawei 5288 V5 (Intel Xeon Bronze 3104)

SPECrate2017_fp_base = 46.3
SPECrate2017_fp_peak = 47.6

CPU2017 License: 3175
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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>12</td>
<td>528</td>
<td>228</td>
<td>529</td>
<td>228</td>
<td>531</td>
<td>227</td>
<td>529</td>
<td>228</td>
<td>526</td>
<td>229</td>
<td></td>
<td></td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>12</td>
<td>417</td>
<td>36.5</td>
<td>417</td>
<td>36.4</td>
<td>417</td>
<td>36.5</td>
<td>417</td>
<td>36.4</td>
<td>417</td>
<td>36.5</td>
<td></td>
<td></td>
</tr>
<tr>
<td>508.namd_r</td>
<td>12</td>
<td>433</td>
<td>26.3</td>
<td>446</td>
<td>25.6</td>
<td>432</td>
<td>26.4</td>
<td>454</td>
<td>25.1</td>
<td>431</td>
<td>26.5</td>
<td></td>
<td></td>
</tr>
<tr>
<td>510.parest_r</td>
<td>12</td>
<td>925</td>
<td>33.9</td>
<td>927</td>
<td>33.9</td>
<td>925</td>
<td>33.9</td>
<td>910</td>
<td>34.5</td>
<td>910</td>
<td>34.5</td>
<td></td>
<td></td>
</tr>
<tr>
<td>511.povray_r</td>
<td>12</td>
<td>658</td>
<td>42.6</td>
<td>659</td>
<td>42.5</td>
<td>661</td>
<td>42.4</td>
<td>569</td>
<td>49.2</td>
<td>568</td>
<td>49.3</td>
<td></td>
<td></td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>12</td>
<td>216</td>
<td>58.6</td>
<td>216</td>
<td>58.7</td>
<td>219</td>
<td>57.7</td>
<td>216</td>
<td>58.6</td>
<td>216</td>
<td>57.7</td>
<td></td>
<td></td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>12</td>
<td>601</td>
<td>44.8</td>
<td>600</td>
<td>44.8</td>
<td>600</td>
<td>44.8</td>
<td>579</td>
<td>46.5</td>
<td>576</td>
<td>46.7</td>
<td></td>
<td></td>
</tr>
<tr>
<td>526.blender_r</td>
<td>12</td>
<td>533</td>
<td>34.3</td>
<td>533</td>
<td>34.3</td>
<td>532</td>
<td>34.3</td>
<td>533</td>
<td>34.3</td>
<td>533</td>
<td>34.3</td>
<td></td>
<td></td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>12</td>
<td>666</td>
<td>31.5</td>
<td>665</td>
<td>31.6</td>
<td>666</td>
<td>31.5</td>
<td>600</td>
<td>35.0</td>
<td>594</td>
<td>35.4</td>
<td></td>
<td></td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>12</td>
<td>531</td>
<td>56.2</td>
<td>536</td>
<td>55.7</td>
<td>531</td>
<td>56.2</td>
<td>531</td>
<td>56.2</td>
<td>536</td>
<td>55.7</td>
<td></td>
<td></td>
</tr>
<tr>
<td>544.nab_r</td>
<td>12</td>
<td>489</td>
<td>41.3</td>
<td>489</td>
<td>41.3</td>
<td>488</td>
<td>41.4</td>
<td>484</td>
<td>41.7</td>
<td>483</td>
<td>41.8</td>
<td></td>
<td></td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>12</td>
<td>767</td>
<td>60.9</td>
<td>769</td>
<td>60.8</td>
<td>767</td>
<td>61.0</td>
<td>767</td>
<td>60.9</td>
<td>767</td>
<td>61.0</td>
<td></td>
<td></td>
</tr>
<tr>
<td>554.roms_r</td>
<td>12</td>
<td>539</td>
<td>35.4</td>
<td>545</td>
<td>35.0</td>
<td>540</td>
<td>35.3</td>
<td>517</td>
<td>36.9</td>
<td>518</td>
<td>36.8</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
Memory using Redhat Enterprise Linux 7.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>/proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
umactl --interleave=all runcpu <etc>
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
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Huawei 5288 V5 (Intel Xeon Bronze 3104)

SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

SPECrate2017_fp_base = 46.3
SPECrate2017_fp_peak = 47.6

CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Power Policy Set to Performance
XPT Prefetch Set to Enabled
Sysinfo program /spec2017/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e45f6ad54c135fd618bcc091c0f
running on localhost.localdomain Tue Aug 7 05:44:43 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Bronze 3104 CPU @ 1.70GHz
  2 "physical id"s (chips)
  12 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 6
siblings : 6
physical 0: cores 0 1 2 3 4 5
physical 1: cores 0 1 2 3 4 5

From lscpu:

Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 12
On-line CPU(s) list: 0-11
Thread(s) per core: 1
Core(s) per socket: 6
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Bronze 3104 CPU @ 1.70GHz
Stepping: 4
CPU MHz: 1700.000
BogoMIPS: 3400.00

(Continued on next page)
SPEC CPU2017 Floating Point Rate Result

Huawei

Huawei 5288 V5 (Intel Xeon Bronze 3104)

| SPECrate2017_fp_peak = 47.6 |
| SPECrate2017_fp_base = 46.3 |

CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

Test Date: Aug-2018
Hardware Availability: Jul-2017
Software Availability: Jan-2018

Platform Notes (Continued)

Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 8448K
NUMA node0 CPU(s): 0-5
NUMA node1 CPU(s): 6-11
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtsscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmperf eagerfpu pni pclmulqdq dtses64 ds_cpl vmx smx est tm2 ssse3 fma cx16 xtpcr
dpm cpcd dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx
f16c rdrand lahf_lm abm 3dnowprefetch epb cat_13 cdp_13 invpctid_single intel_pt
spec_ctrl ibpb_support tpr_shadow vnni flexpriority ept vpid fsgsbase tsc_adjust
bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a avx512f avx512dq rdseed adx
smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsaves xcetbvl cqm_llc
cqm_occup_llc cqm mdbm_total cqm mdbm_local dtherm arat pln pts

/proc/cpuinfo cache data
  cache size: 8448 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
  physical chip.
  available: 2 nodes (0-1)
  node 0 cpus: 0 1 2 3 4 5
  node 0 size: 194741 MB
  node 0 free: 189355 MB
  node 1 cpus: 6 7 8 9 10 11
  node 1 size: 196608 MB
  node 1 free: 191590 MB
  node distances:
    node 0 1
    0: 10 21
    1: 21 10

From /proc/meminfo
  MemTotal: 394174584 kB
  HugePages_Total: 0
  Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
  os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.4 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VARIANT="Server"

(Continued on next page)
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CPU2017 License: 3175
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Tested by: Huawei

Platform Notes (Continued)

```
VARIANT_ID="server"
VERSION_ID="7.4"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.4 (Maipo)"
redhat-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.4:ga:server
uname -a:
    Linux localhost.localdomain 3.10.0-693.11.6.el7.x86_64 #1 SMP Thu Dec 28 14:23:39 EST 2017 x86_64 x86_64 x86_64 GNU/Linux
run-level 3 Aug 6 07:05

SPEC is set to: /spec2017
    Filesystem     Type  Size  Used Avail Use% Mounted on
    /dev/sda4      xfs   700G   35G  666G   5% /
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS INSYDE Corp. 0.62 03/26/2018
Memory:
    24x Samsung M393A2K43BB1-CTD 16 GB 2 rank 2666, configured at 2133

(End of data from sysinfo program)

Compiler Version Notes

```
==============================================================================
 CC   519.lbm_r(base) 538.imagick_r(base, peak) 544.nab_r(base)
==============================================================================

icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

```
(Continued on next page)
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Compiler Version Notes (Continued)

icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
CXXC 508.namd_r(peak) 510.parest_r(peak)
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
CC 511.povray_r(base) 526.blender_r(base)
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
CC 511.povray_r(peak) 526.blender_r(peak)
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
FC 507.cactuBSSN_r(base)
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

==============================================================================
FC 507.cactuBSSN_r(peak)
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

(Continued on next page)
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**Software Availability:** Jan-2018

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**Compiler Version Notes (Continued)**

```plaintext
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
```

```plaintext
FC 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base)
```

```plaintext
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
```

```plaintext
FC 554.roms_r(peak)
```

```plaintext
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
```

```plaintext
CC 521.wrf_r(base) 527.cam4_r(base)
```

```plaintext
ifort (IFORT) 18.0.0 20170811
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icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
```

```plaintext
CC 521.wrf_r(peak) 527.cam4_r(peak)
```

```plaintext
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
```

---

**Base Compiler Invocation**

C benchmarks:  
`icc`

C++ benchmarks:  
`icpc`

Fortran benchmarks:  
`ifort`

(Continued on next page)
Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using both C and C++:
icpc icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactusBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

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</table>

Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

Benchmarks using both C and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Benchmarks using Fortran, C, and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

Base Other Flags

C benchmarks:
-m64 -std=c11

C++ benchmarks:
-m64

Fortran benchmarks:
-m64

Benchmarks using both Fortran and C:
-m64 -std=c11

Benchmarks using both C and C++:
-m64 -std=c11

Benchmarks using Fortran, C, and C++:
-m64 -std=c11

Peak Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

(Continued on next page)
Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using both C and C++:
icpc icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
519.lbm_r: basepeak = yes
538.imagick_r: basepeak = yes
544.nab_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

C++ benchmarks:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Fortran benchmarks:
503.bwaves_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3
-nostandard-realloc-lhs -align array32byte

549.fotonik3d_r: Same as 503.bwaves_r

554.roms_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only

(Continued on next page)
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Test Date: Aug-2018
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Software Availability: Jan-2018

Peak Optimization Flags (Continued)

554.roms_r (continued):
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs
-align array32byte

Benchmarks using both Fortran and C:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte

Benchmarks using both C and C++:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Benchmarks using Fortran, C, and C++:
507.cactuBSSN_r: basepeak = yes

Peak Other Flags

C benchmarks:
-m64 -std=c11

C++ benchmarks:
-m64

Fortran benchmarks:
-m64

Benchmarks using both Fortran and C:
-m64 -std=c11

Benchmarks using both C and C++:
-m64 -std=c11

Benchmarks using Fortran, C, and C++:
-m64 -std=c11

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html
<table>
<thead>
<tr>
<th>Huawei 5288 V5 (Intel Xeon Bronze 3104)</th>
<th>SPECrate2017_fp_peak = 47.6</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate2017_fp_base = 46.3</td>
<td></td>
</tr>
<tr>
<td>Huawei</td>
<td></td>
</tr>
<tr>
<td>Huawei</td>
<td></td>
</tr>
<tr>
<td>SPEC CPU2017 Floating Point Rate Result</td>
<td></td>
</tr>
<tr>
<td>Copyright 2017-2018 Standard Performance Evaluation Corporation</td>
<td></td>
</tr>
</tbody>
</table>

You can also download the XML flags sources by saving the following links:

- http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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