Dell Inc.
PowerEdge MX740c (Intel Xeon Silver 4110 CPU, 2.10GHz)

SPEC® CPU2017 Integer Rate Result

**SPECrate2017_int_base** = 73.3
**SPECrate2017_int_peak** = 76.4

<table>
<thead>
<tr>
<th>Software</th>
<th>Hardware</th>
</tr>
</thead>
<tbody>
<tr>
<td>OS: SUSE Linux Enterprise Server 12 SP3</td>
<td>CPU Name: Intel Xeon Silver 4110</td>
</tr>
<tr>
<td>Compiler: C/C++: Version 18.0.0.128 of Intel C/C++</td>
<td>Max MHz.: 3000</td>
</tr>
<tr>
<td>Compiler for Linux: Fortran: Version 18.0.0.128 of Intel Fortran</td>
<td>Nominal: 2100</td>
</tr>
<tr>
<td>File System: xfs</td>
<td>Enabled: 16 cores, 2 chips, 2 threads/core</td>
</tr>
<tr>
<td>System State: Run level 3 (multi-user)</td>
<td>Orderable: 1.2 chips</td>
</tr>
<tr>
<td>Base Pointers: 64-bit</td>
<td>Cache L1: 32 KB I+ 32 KB D on chip per core</td>
</tr>
<tr>
<td>Peak Pointers: 32/64-bit</td>
<td>L2: 1 MB I+D on chip per core</td>
</tr>
<tr>
<td>Other: jemalloc memory allocator library, version 5.0.1</td>
<td>L3: 11 MB I+D on chip per chip</td>
</tr>
</tbody>
</table>

---

<table>
<thead>
<tr>
<th>SPECrate2017_int_base</th>
<th>SPECrate2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>73.3</td>
<td>76.4</td>
</tr>
</tbody>
</table>

---

**Test Sponsor:** Dell Inc.
**Test Date:** Apr-2018
**Software Availability:** Sep-2017

**Hardware Availability:** Sep-2017

---

**Copies**

500.perlbench_r 32
502.gcc_r 32
505.mcf_r 32
520.omnetpp_r 32
523.xalancbmk_r 32
525.x264_r 32
531.deepsjeng_r 32
541.leela_r 32
548.exchange2_r 32
557.xz_r 32

---

**SPECrate2017_int_base (73.3)**
**SPECrate2017_int_peak (76.4)**
### Dell Inc.  
**PowerEdge MX740c (Intel Xeon Silver 4110 CPU, 2.10GHz)**

**SPEC CPU2017 Integer Rate Result**

<table>
<thead>
<tr>
<th>CPU2017 License: 55</th>
<th>Dell Inc.</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor: Dell Inc.</td>
<td>Dell Inc.</td>
</tr>
<tr>
<td>Tested by: Dell Inc.</td>
<td>Dell Inc.</td>
</tr>
<tr>
<td>Test Date: Apr-2018</td>
<td>Hardware Availability: Sep-2017</td>
</tr>
<tr>
<td>Software Availability: Sep-2017</td>
<td></td>
</tr>
</tbody>
</table>

**SPECrate2017_int_base = 73.3**  
**SPECrate2017_int_peak = 76.4**

#### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Base</th>
<th>Peak</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Copies</td>
<td>Seconds</td>
</tr>
<tr>
<td>500.perlbench_r</td>
<td>32</td>
<td>911</td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>32</td>
<td>702</td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>32</td>
<td>556</td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>32</td>
<td>867</td>
</tr>
<tr>
<td>523.xalancbmk_r</td>
<td>32</td>
<td>442</td>
</tr>
<tr>
<td>525.x264_r</td>
<td>32</td>
<td>404</td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
<td>32</td>
<td>576</td>
</tr>
<tr>
<td>541.leela_r</td>
<td>32</td>
<td>926</td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>32</td>
<td>642</td>
</tr>
<tr>
<td>557.z.r</td>
<td>32</td>
<td>641</td>
</tr>
</tbody>
</table>

**SPECrate2017_int_base = 73.3**  
**SPECrate2017_int_peak = 76.4**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### General Notes

Environment variables set by runcpu before the start of the run:

```
```

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc: configured and built at default for 32bit (i686) and 64bit (x86_64) targets;
jemalloc: built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5;
jemalloc: sources available via jemalloc.net

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

(Continued on next page)
**SPEC CPU2017 Integer Rate Result**

*Dell Inc.*

PowerEdge MX740c (Intel Xeon Silver 4110 CPU, 2.10GHz)

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>55</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor:</td>
<td>Dell Inc.</td>
</tr>
<tr>
<td>Tested by:</td>
<td>Dell Inc.</td>
</tr>
</tbody>
</table>

**SPECrate2017_int_base** = 73.3

**SPECrate2017_int_peak** = 76.4

---

**General Notes (Continued)**

sync; echo 3>/proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

---

**Platform Notes**

BIOS settings:
Sub NUMA Cluster Disabled
Virtualization Technology Disabled
System Profile set to Custom
CPU Performance set to Maximum Performance
C States set to Autonomous
C1E Disabled
Uncore Frequency set to Dynamic
Energy Efficiency Policy set to Performance
Memory Patrol Scrub Disabled
Logical Processor Enabled
CPU Interconnect Bus Link Power Management Disabled
PCI ASPM L1 Link Power Management Disabled
Sysinfo program /root/cpu2017/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on linux-9px5 Sat Apr 7 00:51:32 2018

SUT (System Under Test) info as seen by some common utilities. For more information on this section, see [https://www.spec.org/cpu2017/Docs/config.html#sysinfo](https://www.spec.org/cpu2017/Docs/config.html#sysinfo)

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) Silver 4110 CPU @ 2.10GHz
  2 "physical id"s (chips)
  32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 8
siblings : 16
physical 0: cores 0 1 2 3 4 5 6 7
physical 1: cores 0 1 2 3 4 5 6 7
```

From lscpu:

```
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 32
On-line CPU(s) list: 0-31
Thread(s) per core: 2
Core(s) per socket: 8
```

(Continued on next page)
### SPEC CPU2017 Integer Rate Result

**Dell Inc.**  
**PowerEdge MX740c (Intel Xeon Silver 4110 CPU, 2.10GHz)**

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>55</th>
<th>Test Date:</th>
<th>Apr-2018</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor:</td>
<td>Dell Inc.</td>
<td>Hardware Availability:</td>
<td>Sep-2017</td>
</tr>
<tr>
<td>Tested by:</td>
<td>Dell Inc.</td>
<td>Software Availability:</td>
<td>Sep-2017</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>SPECrate2017_int_base</th>
<th>73.3</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate2017_int_peak</td>
<td>76.4</td>
</tr>
</tbody>
</table>

**Platform Notes (Continued)**

- **Socket(s):** 2
- **NUMA node(s):** 2
- **Vendor ID:** GenuineIntel
- **CPU family:** 6
- **Model:** 85
- **Model name:** Intel(R) Xeon(R) Silver 4110 CPU @ 2.10GHz
- **Stepping:** 4
- **CPU MHz:** 2095.086
- **BogoMIPS:** 4190.17
- **Virtualization:** VT-x
- **L1d cache:** 32K
- **L1i cache:** 32K
- **L2 cache:** 1024K
- **L3 cache:** 11264K
- **NUMA node0 CPU(s):** 0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30
- **NUMA node1 CPU(s):** 1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31
- **Flags:** fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good ncpu nonstop_tsc aperfmperf eagerfpu pni pclmulqdq dtsc64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb invpcid_single pln pts dtherm intel_pt rsb_ctxtsw spec_ctrl retexpel kaiser tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 3dnow vpmx avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsaveprec qgetbv1 cqm_llc cqm_occup_llc pku ospke

From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a physical chip.

<table>
<thead>
<tr>
<th>available: 2 nodes (0-1)</th>
</tr>
</thead>
<tbody>
<tr>
<td>node 0 cpus: 0 2 4 6 8 10 12 14 16 18 20 22 24 26 28 30</td>
</tr>
<tr>
<td>node 0 size: 192118 MB</td>
</tr>
<tr>
<td>node 0 free: 191761 MB</td>
</tr>
<tr>
<td>node 1 cpus: 1 3 5 7 9 11 13 15 17 19 21 23 25 27 29 31</td>
</tr>
<tr>
<td>node 1 size: 193516 MB</td>
</tr>
<tr>
<td>node 1 free: 193181 MB</td>
</tr>
<tr>
<td>node distances:</td>
</tr>
<tr>
<td>node 0 1</td>
</tr>
<tr>
<td>0: 10 21</td>
</tr>
<tr>
<td>1: 21 10</td>
</tr>
</tbody>
</table>

From /proc/meminfo

<table>
<thead>
<tr>
<th>MemTotal: 394890860 kB</th>
</tr>
</thead>
<tbody>
<tr>
<td>HugePages_Total: 0</td>
</tr>
</tbody>
</table>

(Continued on next page)
### SPEC CPU2017 Integer Rate Result

**Dell Inc.**

PowerEdge MX740c (Intel Xeon Silver 4110 CPU, 2.10GHz)

<table>
<thead>
<tr>
<th>SPECrate2017_int_base</th>
<th>73.3</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate2017_int_peak</td>
<td>76.4</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Hardware Availability:** Sep-2017  
**Software Availability:** Sep-2017  
**Test Date:** Apr-2018  
**Tested by:** Dell Inc.

#### Platform Notes (Continued)

- **Hugepagesize:** 2048 kB

- **From /etc/*release* /etc/*version*:
  - **SuSE-release:**
    - SUSE Linux Enterprise Server 12 (x86_64)
    - VERSION = 12
    - PATCHLEVEL = 3
    - # This file is deprecated and will be removed in a future service pack or release.
    - # Please check /etc/os-release for details about this release.
  - **os-release:**
    - NAME="SLES"
    - VERSION="12-SP3"
    - VERSION_ID="12.3"
    - PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
    - ID="sles"
    - ANSI_COLOR="0;32"
    - CPE_NAME="cpe:/o:suse:sles:12:sp3"

- **uname -a:**
  - Linux linux-9px5 4.4.114-94.11-default #1 SMP Thu Feb 1 19:28:26 UTC 2018 (4309ff9)
  - x86_64 x86_64 x86_64 GNU/Linux

- **run-level:** 3 Apr 7 00:48

- **SPEC is set to:** /root/cpu2017
  - **Filesystem**  
    - **Type**  
    - **Size**  
    - **Used**  
    - **Avail**  
    - **Use%**  
    - **Mounted on**

- **Memory:**
  - 19x 002C00B3002C 18ASF2G72PD2-2G6D1 16 GB 2 rank 2666, configured at 2400
  - 1x 00AD00B300AD HMA82GR7AFR8N-VK 16 GB 2 rank 2666, configured at 2400
  - 4x 00CE063200CE M393A2K43BB1-CTD 16 GB 2 rank 2666, configured at 2400

(End of data from sysinfo program)

#### Compiler Version Notes

- **CC**
  - 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
  - 525.x264_r(base, peak) 557.xz_r(base, peak)

(Continued on next page)
Dell Inc.
PowerEdge MX740c (Intel Xeon Silver 4110 CPU, 2.10GHz)

**Spec CPU2017 Integer Rate Result**

<table>
<thead>
<tr>
<th>Spec CPU2017_int_base</th>
<th>73.3</th>
</tr>
</thead>
<tbody>
<tr>
<td>SpecCPU2017_int_peak</td>
<td>76.4</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 55
**Test Sponsor:** Dell Inc.
**Tested by:** Dell Inc.
**Test Date:** Apr-2018
**Hardware Availability:** Sep-2017
**Software Availability:** Sep-2017

---

### Compiler Version Notes (Continued)

```plaintext
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
```

```plaintext
CC 500.perlbench_r(peak) 502.gcc_r(peak)
```

```plaintext
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
```

```plaintext
CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base) 541.leela_r(base)
```

```plaintext
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
```

```plaintext
CXXC 520.omnetpp_r(peak) 523.xalancbmk_r(peak) 531.deepsjeng_r(peak) 541.leela_r(peak)
```

```plaintext
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
```

```plaintext
FC 548.exchange2_r(base, peak)
```

```plaintext
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
```

---

### Base Compiler Invocation

**C benchmarks:**
- icc

**C++ benchmarks:**
- icpc

**Fortran benchmarks:**
- ifort
# SPEC CPU2017 Integer Rate Result

## Dell Inc.
PowerEdge MX740c (Intel Xeon Silver 4110 CPU, 2.10GHz)

<table>
<thead>
<tr>
<th>Test Sponsor</th>
<th>Dell Inc.</th>
<th>Dell Inc.</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU2017 License</td>
<td>55</td>
<td>55</td>
</tr>
<tr>
<td>Test Date</td>
<td>Apr-2018</td>
<td>Apr-2018</td>
</tr>
<tr>
<td>Hardware Availability</td>
<td>Sep-2017</td>
<td>Sep-2017</td>
</tr>
<tr>
<td>Software Availability</td>
<td>Sep-2017</td>
<td>Sep-2017</td>
</tr>
</tbody>
</table>

### SPECrate2017_int_base = 73.3
### SPECrate2017_int_peak = 76.4

## Base Portability Flags

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Flags</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>-DSPEC_LP64 -DSPEC_LINUX_X64</td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>-DSPEC_LP64</td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>-DSPEC_LP64</td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>-DSPEC_LP64</td>
</tr>
<tr>
<td>523.xalancbmk_r</td>
<td>-DSPEC_LP64 -DSPEC_LINUX</td>
</tr>
<tr>
<td>525.x264_r</td>
<td>-DSPEC_LP64</td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
<td>-DSPEC_LP64</td>
</tr>
<tr>
<td>541.leela_r</td>
<td>-DSPEC_LP64</td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>-DSPEC_LP64</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>-DSPEC_LP64</td>
</tr>
</tbody>
</table>

## Base Optimization Flags

### C benchmarks:

- Wl, -z, muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
- qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

### C++ benchmarks:

- Wl, -z, muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
- qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

### Fortran benchmarks:

- Wl, -z, muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
- qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
- -L/usr/local/je5.0.1-64/lib -ljemalloc

## Base Other Flags

### C benchmarks:

- m64 -std=c11

### C++ benchmarks:

- m64

### Fortran benchmarks:

- m64
## SPEC CPU2017 Integer Rate Result

**Dell Inc.**  
PowerEdge MX740c (Intel Xeon Silver 4110 CPU, 2.10GHz)

<table>
<thead>
<tr>
<th>CPU2017 License</th>
<th>55</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor</td>
<td>Dell Inc.</td>
</tr>
<tr>
<td>Tested by</td>
<td>Dell Inc.</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>SPECrate2017_int_base</th>
<th>73.3</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate2017_int_peak</td>
<td>76.4</td>
</tr>
</tbody>
</table>

### Peak Compiler Invocation

- **C benchmarks:** icc
- **C++ benchmarks:** icpc
- **Fortran benchmarks:** ifort

### Peak Portability Flags

- 500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
- 502.gcc_r: -D_FILE_OFFSET_BITS=64
- 505.mcf_r: -DSPEC_LP64
- 520.omnetpp_r: -DSPEC_LP64
- 523.xalancbmk_r: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
- 525.x264_r: -DSPEC_LP64
- 531.deepsjeng_r: -DSPEC_LP64
- 541.leela_r: -DSPEC_LP64
- 548.exchange2_r: -DSPEC_LP64
- 557.xz_r: -DSPEC_LP64

### Peak Optimization Flags

- **C benchmarks:**
  - 500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
  -xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
  -fno-strict-overflow -L/usr/local/je5.0.1-64/lib
  -ljemalloc

- 502.gcc_r: -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32
  -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
  -xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
  -L/usr/local/je5.0.1-32/lib -ljemalloc

- 505.mcf_r: -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
  -qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib
  -ljemalloc

- 525.x264_r: -Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
  -qopt-mem-layout-trans=3 -fno-alias

(Continued on next page)
SPEC CPU2017 Integer Rate Result

Dell Inc.
PowerEdge MX740c (Intel Xeon Silver 4110 CPU, 2.10GHz)

SPECrate2017_int_base = 73.3
SPECrate2017_int_peak = 76.4

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Apr-2018
Hardware Availability: Sep-2017
Software Availability: Sep-2017

Peak Optimization Flags (Continued)

525.x264_r (continued):
- L/usr/local/je5.0.1-64/lib -ljemalloc

557.xz_r: Same as 505.mcf_r

C++ benchmarks:

520.omnetpp_r: -Wl,-z, muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
- xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
- L/usr/local/je5.0.1-64/lib -ljemalloc

523.xalancbmk_r: -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32
- Wl,-z, muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
- xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
- L/usr/local/je5.0.1-32/lib -ljemalloc

531.deepsjeng_r: Same as 520.omnetpp_r

541.leela_r: Same as 520.omnetpp_r

Fortran benchmarks:
- Wl,-z, muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
- qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
- L/usr/local/je5.0.1-64/lib -ljemalloc

Peak Other Flags

C benchmarks (except as noted below):
- m64 -std=c11
  502.gcc_r: -m32 -std=c11

C++ benchmarks (except as noted below):
- m64
  523.xalancbmk_r: -m32

Fortran benchmarks:
- m64

The flags files that were used to format this result can be browsed at:
<table>
<thead>
<tr>
<th>Dell Inc.</th>
<th>SPECrate2017_int_base = 73.3</th>
</tr>
</thead>
<tbody>
<tr>
<td>PowerEdge MX740c (Intel Xeon Silver 4110 CPU, 2.10GHz)</td>
<td>SPECrate2017_int_peak = 76.4</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>CPU2017 License: 55</th>
<th>Test Date: Apr-2018</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor: Dell Inc.</td>
<td>Hardware Availability: Sep-2017</td>
</tr>
<tr>
<td>Tested by: Dell Inc.</td>
<td>Software Availability: Sep-2017</td>
</tr>
</tbody>
</table>

You can also download the XML flags sources by saving the following links:


SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2018-04-07 01:51:32-0400.
Originally published on 2018-09-04.