# SPEC® CPU2017 Integer Rate Result

**Dell Inc.**  
**PowerEdge MX740c (Intel Xeon 4114 CPU, 2.20GHz)**  

**SPECrate2017_int_base** = 92.5  
**SPECrate2017_int_peak** = 97.6

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>55</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor:</td>
<td>Dell Inc.</td>
</tr>
<tr>
<td>Tested by:</td>
<td>Dell Inc.</td>
</tr>
<tr>
<td><strong>Test Date:</strong></td>
<td>May-2018</td>
</tr>
<tr>
<td><strong>Hardware Available:</strong></td>
<td>Sep-2017</td>
</tr>
<tr>
<td><strong>Software Available:</strong></td>
<td>Sep-2017</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Name</th>
<th>Copies</th>
<th>SPECrate2017_int_base</th>
<th>SPECrate2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>40</td>
<td>72.4</td>
<td>87.3</td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>40</td>
<td>80.8</td>
<td>96.1</td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>40</td>
<td>60.3</td>
<td>94.6</td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>40</td>
<td>56.1</td>
<td>116</td>
</tr>
<tr>
<td>523.xalancbmk_r</td>
<td>40</td>
<td>716</td>
<td>179</td>
</tr>
<tr>
<td>525.x264_r</td>
<td>40</td>
<td>81.1</td>
<td>188</td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
<td>40</td>
<td>74.5</td>
<td>179</td>
</tr>
<tr>
<td>541.leela_r</td>
<td>40</td>
<td>74.9</td>
<td>175</td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>40</td>
<td>63.1</td>
<td>175</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>40</td>
<td>62.7</td>
<td></td>
</tr>
</tbody>
</table>

## Hardware

- **CPU Name:** Intel Xeon Silver 4114  
- **Max MHz.:** 3000  
- **Nominal:** 2200  
- **Enabled:** 20 cores, 2 chips, 2 threads/core  
- **Orderable:** 1.2 chips  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **L2:** 1 MB I+D on chip per core  
- **L3:** 13.75 MB I+D on chip per core  
- **Other:** None  
- **Memory:** 192 GB (12 x 16 GB 2Rx8 PC4-2666V-R, running at 2400)  
- **Storage:** 960 GB SAS  
- **Other:** None

## Software

- **OS:** SUSE Linux Enterprise Server 12 SP3  
- **4.4.114-94.11-default**  
- **Compiler:** C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux;  
  Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux  
- **Parallel:** No  
- **Firmware:** Version 0.4.4 released May-2018  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 32/64-bit  
- **Other:** jemalloc memory allocator library, version 5.0.1
SPEC CPU2017 Integer Rate Result

Dell Inc.

PowerEdge MX740c (Intel Xeon Silver 4114 CPU, 2.20GHz)

SPECrate2017_int_base = 92.5
SPECrate2017_int_peak = 97.6

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>40</td>
<td>890</td>
<td>71.5</td>
<td>878</td>
<td>72.5</td>
<td>880</td>
<td>72.4</td>
<td>40</td>
<td>729</td>
<td>87.3</td>
<td>729</td>
<td>87.4</td>
<td>730</td>
<td>87.2</td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>40</td>
<td>693</td>
<td>81.8</td>
<td>701</td>
<td>80.8</td>
<td>704</td>
<td>80.5</td>
<td>40</td>
<td>589</td>
<td>96.2</td>
<td>592</td>
<td>95.7</td>
<td>589</td>
<td>96.1</td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>40</td>
<td>559</td>
<td>116</td>
<td>559</td>
<td>116</td>
<td>562</td>
<td>115</td>
<td>40</td>
<td>562</td>
<td>115</td>
<td>565</td>
<td>114</td>
<td>560</td>
<td>115</td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>40</td>
<td>873</td>
<td>60.1</td>
<td>869</td>
<td>60.4</td>
<td>870</td>
<td>60.3</td>
<td>40</td>
<td>936</td>
<td>56.1</td>
<td>943</td>
<td>55.7</td>
<td>926</td>
<td>56.7</td>
</tr>
<tr>
<td>523.xalancbmk_r</td>
<td>40</td>
<td>445</td>
<td>94.9</td>
<td>446</td>
<td>94.6</td>
<td>449</td>
<td>94.1</td>
<td>40</td>
<td>365</td>
<td>116</td>
<td>366</td>
<td>115</td>
<td>366</td>
<td>116</td>
</tr>
<tr>
<td>525.x264_r</td>
<td>40</td>
<td>392</td>
<td>179</td>
<td>391</td>
<td>179</td>
<td>391</td>
<td>179</td>
<td>40</td>
<td>372</td>
<td>188</td>
<td>374</td>
<td>187</td>
<td>372</td>
<td>188</td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
<td>40</td>
<td>559</td>
<td>82.0</td>
<td>566</td>
<td>81.0</td>
<td>565</td>
<td>81.1</td>
<td>40</td>
<td>576</td>
<td>79.6</td>
<td>575</td>
<td>79.7</td>
<td>575</td>
<td>79.8</td>
</tr>
<tr>
<td>541.leela_r</td>
<td>40</td>
<td>883</td>
<td>75.0</td>
<td>889</td>
<td>74.5</td>
<td>893</td>
<td>74.2</td>
<td>40</td>
<td>873</td>
<td>75.9</td>
<td>873</td>
<td>75.9</td>
<td>857</td>
<td>77.3</td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>40</td>
<td>598</td>
<td>175</td>
<td>598</td>
<td>175</td>
<td>599</td>
<td>175</td>
<td>40</td>
<td>598</td>
<td>175</td>
<td>599</td>
<td>175</td>
<td>599</td>
<td>175</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>40</td>
<td>681</td>
<td>63.4</td>
<td>685</td>
<td>63.1</td>
<td>686</td>
<td>62.9</td>
<td>40</td>
<td>689</td>
<td>62.7</td>
<td>689</td>
<td>62.7</td>
<td>690</td>
<td>62.6</td>
</tr>
</tbody>
</table>

SPECrate2017_int_base = 92.5
SPECrate2017_int_peak = 97.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
jemalloc: configured and built at default for 32bit (i686) and 64bit (x86_64) targets; jemalloc: built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5; jemalloc: sources available via jemalloc.net
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:

(Continued on next page)
Dell Inc.
PowerEdge MX740c (Intel Xeon Silver 4114 CPU, 2.20GHz)  

SPECrate2017_int_base = 92.5  
SPECrate2017_int_peak = 97.6

General Notes (Continued)

sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

Platform Notes

BIOS settings:
Sub NUMA Cluster Disabled
Virtualization Technology Disabled
System Profile set to Custom
CPU Performance set to Maximum Performance
C States set to Autonomous
C1E Disabled
Uncore Frequency set to Dynamic
Energy Efficiency Policy set to Performance
Memory Patrol Scrub Disabled
Logical Processor Enabled
CPU Interconnect Bus Link Power Management Disabled
PCI ASPM L1 Link Power Management Disabled
Sysinfo program /root/cpu2017/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bce091c0f
running on linux-kuth Wed May 30 11:59:19 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Silver 4114 CPU @ 2.20GHz
  2 "physical id"s (chips)
  40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 10
siblings : 20
physical 0: cores 0 1 2 3 4 8 9 10 11 12
physical 1: cores 0 1 2 3 4 8 9 10 11 12

From lscpu:
Architecture:         x86_64
CPU op-mode(s):       32-bit, 64-bit
Byte Order:           Little Endian
CPU(s):               40
On-line CPU(s) list:  0-39
Thread(s) per core:   2
Core(s) per socket:   10

(Continued on next page)
**SPEC CPU2017 Integer Rate Result**

**Dell Inc.**

PowerEdge MX740c (Intel Xeon Silver 4114 CPU, 2.20GHz)  

**SPECrate2017_int_base = 92.5**  
**SPECrate2017_int_peak = 97.6**

---

**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.

**Test Date:** May-2018  
**Hardware Availability:** Sep-2017  
**Software Availability:** Sep-2017

---

**Platform Notes (Continued)**

- Socket(s): 2
- NUMA node(s): 2
- Vendor ID: GenuineIntel
- CPU family: 6
- Model: 85
- Model name: Intel(R) Xeon(R) Silver 4114 CPU @ 2.20GHz
- Stepping: 4
- CPU MHz: 2194.946
- BogoMIPS: 4389.89
- Virtualization: VT-x
- L1d cache: 32K
- L1i cache: 32K
- L2 cache: 1024K
- L3 cache: 14080K
- NUMA node0 CPU(s): 0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30,32,34,36,38
- NUMA node1 CPU(s): 1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31,33,35,37,39
- Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bs rep_good nopl xtopology nonstop_tsc aperfmpref eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtrac pdcn pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb invpcid_single pln pts dtherm intel_pt rsb_cxsw spec_ctrl reteliner kaiser tpr_shadow vmxflex priority ept vpid fsgsbase tsc_adjust bm11 hle avx2 smep bmi2 ems invpcid rtm cqm mpx avx512f avx512dq rsseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1 cqm_llc cqm_occup_llc pku ospke

/proc/cpuinfo cache data

- cache size: 14080 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

- available: 2 nodes (0-1)
- node 0 cpus: 0 2 4 6 8 10 12 14 16 18 20 22 24 26 28 30 32 34 36 38
- node 0 size: 95353 MB
- node 0 free: 94929 MB
- node 1 cpus: 1 3 5 7 9 11 13 15 17 19 21 23 25 27 29 31 33 35 37 39
- node 1 size: 96748 MB
- node 1 free: 96419 MB
- node distances:
  - node 0 1
  - 0: 10 21
  - 1: 21 10

From /proc/meminfo

- MemTotal: 196712156 kB
- HugePages_Total: 0

(Continued on next page)
Dell Inc.
PowerEdge MX740c (Intel Xeon Silver 4114 CPU, 2.20GHz)  

<table>
<thead>
<tr>
<th>SPECrate2017_int_base = 92.5</th>
<th>Test Date:</th>
<th>May-2018</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate2017_int_peak = 97.6</td>
<td>Hardware Availability:</td>
<td>Sep-2017</td>
</tr>
</tbody>
</table>

CPU2017 License: 55  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

Platform Notes (Continued)

Hugepagesize: 2048 kB

/usr/bin/lsb_release -d  
SUSE Linux Enterprise Server 12 SP3

From /etc/*release* /etc/*version*  
SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)  
  VERSION = 12  
  PATCHLEVEL = 3  
  # This file is deprecated and will be removed in a future service pack or release.  
  # Please check /etc/os-release for details about this release.  
SuSE-release:
  NAME="SLES"  
  VERSION="12-SP3"  
  VERSION_ID="12.3"  
  PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"  
  ID="sles"  
  ANSI_COLOR="0;32"  
  CPE_NAME="cpe:/o:suse:sles:12:sp3"

uname -a:
  Linux linux-kuth 4.4.114-94.11-default #1 SMP Thu Feb 1 19:28:26 UTC 2018 (4309ff9)  
  x86_64 x86_64 x86_64 GNU/Linux

run-level 3 May 30 11:55

SPEC is set to: /root/cpu2017

Filesystem Type Size Used Avail Use% Mounted on  
/dev/sda2 xfs 890G 18G 873G 2% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 0.4.4 05/22/2018
Memory:
  12x 00AD063200AD HMA82GR7AFR8N-VK 16 GB 2 rank 2666, configured at 2400
  12x Not Specified Not Specified

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================  
CC 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)

(Continued on next page)
## SPEC CPU2017 Integer Rate Result

### Dell Inc. PowerEdge MX740c (Intel Xeon Silver 4114 CPU, 2.20GHz)

<table>
<thead>
<tr>
<th>SPECrate2017_int_base</th>
<th>SPECrate2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>92.5</td>
<td>97.6</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 55  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.  
**Test Date:** May-2018  
**Hardware Availability:** Sep-2017  
**Software Availability:** Sep-2017

### Compiler Version Notes (Continued)

- **icc (ICC) 18.0.0 20170811**  
  Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

- **CC 500.perlbench_r(peak) 502.gcc_r(peak)**

- **icc (ICC) 18.0.0 20170811**  
  Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

- **CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base) 541.leela_r(base)**

- **icpc (ICC) 18.0.0 20170811**  
  Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

- **CXXC 520.omnetpp_r(peak) 523.xalancbmk_r(peak) 531.deepsjeng_r(peak) 541.leela_r(peak)**

- **icpc (ICC) 18.0.0 20170811**  
  Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

- **FC 548.exchange2_r(base, peak)**

- **ifort (IFORT) 18.0.0 20170811**  
  Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

### Base Compiler Invocation

**C benchmarks:**  
- icc

**C++ benchmarks:**  
- icpc

**Fortran benchmarks:**  
- ifort
# SPEC CPU2017 Integer Rate Result

## Dell Inc.

PowerEdge MX740c (Intel Xeon Silver 4114 CPU, 2.20GHz)

<table>
<thead>
<tr>
<th>CPU2017 License</th>
<th>Test Sponsor</th>
<th>Tested by</th>
<th>Test Date</th>
<th>Hardware Availability</th>
<th>Software Availability</th>
</tr>
</thead>
</table>

**SPECrated2017_int_base** = 92.5

**SPECrated2017_int_peak** = 97.6

---

### Base Portability Flags

- 500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
- 502.gcc_r: -DSPEC_LP64
- 505.mcf_r: -DSPEC_LP64
- 520.omnetpp_r: -DSPEC_LP64
- 523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
- 525.x264_r: -DSPEC_LP64
- 531.deepsjeng_r: -DSPEC_LP64
- 541.leela_r: -DSPEC_LP64
- 548.exchange2_r: -DSPEC_LP64
- 557.xz_r: -DSPEC_LP64

### Base Optimization Flags

**C benchmarks:**

- `-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div`
- `-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc`

**C++ benchmarks:**

- `-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div`
- `-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc`

**Fortran benchmarks:**

- `-Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div`
- `-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte`
- `-L/usr/local/je5.0.1-64/lib -ljemalloc`

### Base Other Flags

**C benchmarks:**

- `-m64 -std=c11`

**C++ benchmarks:**

- `-m64`

**Fortran benchmarks:**

- `-m64`
Dell Inc.
PowerEdge MX740c (Intel Xeon Silver 4114 CPU, 2.20GHz)

SPECrate2017_int_base = 92.5
SPECrate2017_int_peak = 97.6

Peak Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:
500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -03 -no-prec-div -qopt-mem-layout-trans=3
-fno-strict-overflow -L/usr/local/je5.0.1-64/lib
-ljemalloc

502.gcc_r: -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32
-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -03 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-32/lib -ljemalloc

505.mcf_r: -Wl,-z,muldefs -xCORE-AVX2 -ipo -03 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib
-ljemalloc

525.x264_r: -Wl,-z,muldefs -xCORE-AVX2 -ipo -03 -no-prec-div
-qopt-mem-layout-trans=3 -fno-alias

(Continued on next page)
Dell Inc.
PowerEdge MX740c (Intel Xeon Silver 4114 CPU, 2.20GHz)  

| SPECrate2017_int_base = 92.5 |
| SPECrate2017_int_peak = 97.6 |

**Peak Optimization Flags (Continued)**

525.x264_r (continued):
- L/usr/local/je5.0.1-64/lib -ljemalloc

557.xz_r: Same as 505.mcf_r

C++ benchmarks:

520.omnetpp_r: -Wl,-z, muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-64/lib -ljemalloc

523.xalancbmk_r: -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32
-Wl,-z, muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-32/lib -ljemalloc

531.deepsjeng_r: Same as 520.omnetpp_r

541.leela_r: Same as 520.omnetpp_r

Fortran benchmarks:

-Wl,-z, muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
-L/usr/local/je5.0.1-64/lib -ljemalloc

**Peak Other Flags**

C benchmarks (except as noted below):

-m64 -std=c11

502.gcc_r: -m32 -std=c11

C++ benchmarks (except as noted below):

-m64

523.xalancbmk_r: -m32

Fortran benchmarks:

-m64

The flags files that were used to format this result can be browsed at
<table>
<thead>
<tr>
<th>Dell Inc.</th>
<th>SPECrate2017_int_base = 92.5</th>
</tr>
</thead>
<tbody>
<tr>
<td>PowerEdge MX740c (Intel Xeon Silver 4114 CPU, 2.20GHz)</td>
<td>SPECrate2017_int_peak = 97.6</td>
</tr>
</tbody>
</table>

| CPU2017 License: 55 | Test Date: May-2018 |
| Test Sponsor: Dell Inc. | Hardware Availability: Sep-2017 |
| Tested by: Dell Inc. | Software Availability: Sep-2017 |

You can also download the XML flags sources by saving the following links:


SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2018-05-29 23:59:19-0400.
Originally published on 2018-09-04.