



# SPEC® CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate2017\_fp\_base = 209

SPECrate2017\_fp\_peak = Not Run

CPU2017 License: 55

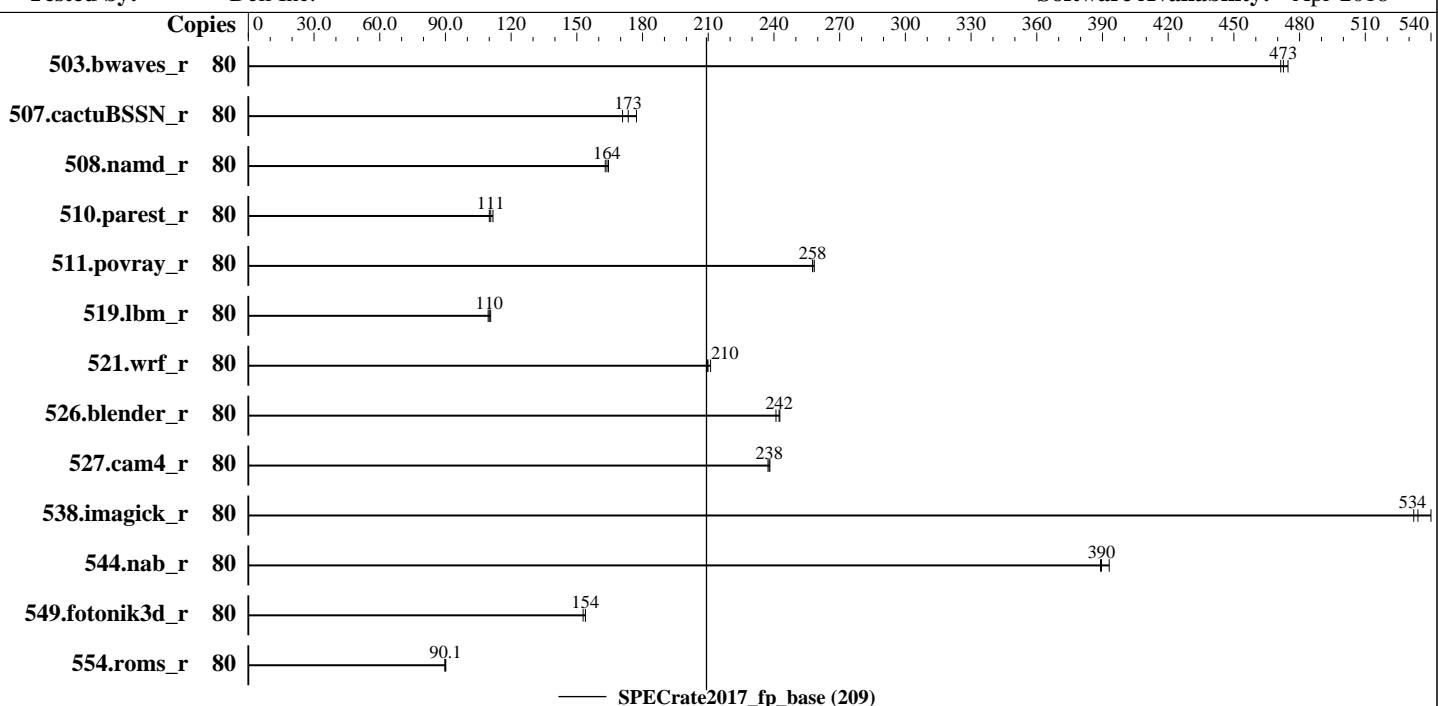
Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Aug-2018

Hardware Availability: Feb-2018

Software Availability: Apr-2018



## Hardware

CPU Name: Intel Xeon Gold 6148  
 Max MHz.: 3700  
 Nominal: 2400  
 Enabled: 40 cores, 2 chips, 2 threads/core  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 27.5 MB I+D on chip per chip  
 Other: None  
 Memory: 384 GB (24 x 16 GB 2Rx8 PC4-2666V-R)  
 Storage: 1 x 960 GB SATA SSD  
 Other: None

## Software

OS: SUSE Linux Enterprise Server 12 SP3  
 Compiler: kernel 4.4.126-94.22-default  
 C/C++: Version 18.0.2.20180210 of Intel C/C++  
 Compiler for Linux;  
 Fortran: Version 18.0.2.20180210 of Intel Fortran  
 Compiler for Linux  
 Parallel: No  
 Firmware: Version 1.3.7 released Feb-2018  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: Not Applicable  
 Other: None



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PowerEdge R740 (Intel Xeon Gold 6148, 2.40GHz)

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## Results Table

| Benchmark        | Base   |             |            |             |            |             |             | Peak   |         |       |         |       |         |       |
|------------------|--------|-------------|------------|-------------|------------|-------------|-------------|--------|---------|-------|---------|-------|---------|-------|
|                  | Copies | Seconds     | Ratio      | Seconds     | Ratio      | Seconds     | Ratio       | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 503.bwaves_r     | 80     | 1702        | 471        | <b>1697</b> | <b>473</b> | 1690        | 475         |        |         |       |         |       |         |       |
| 507.cactusBSSN_r | 80     | 593         | 171        | <b>584</b>  | <b>173</b> | 571         | 177         |        |         |       |         |       |         |       |
| 508.namd_r       | 80     | 466         | 163        | <b>464</b>  | <b>164</b> | 462         | 164         |        |         |       |         |       |         |       |
| 510.parest_r     | 80     | 1902        | 110        | 1874        | 112        | <b>1891</b> | <b>111</b>  |        |         |       |         |       |         |       |
| 511.povray_r     | 80     | 725         | 258        | <b>725</b>  | <b>258</b> | 723         | 258         |        |         |       |         |       |         |       |
| 519.lbm_r        | 80     | 770         | 110        | 762         | 111        | <b>766</b>  | <b>110</b>  |        |         |       |         |       |         |       |
| 521.wrf_r        | 80     | 855         | 210        | 849         | 211        | <b>854</b>  | <b>210</b>  |        |         |       |         |       |         |       |
| 526.blender_r    | 80     | 506         | 241        | <b>503</b>  | <b>242</b> | 502         | 243         |        |         |       |         |       |         |       |
| 527.cam4_r       | 80     | 590         | 237        | <b>588</b>  | <b>238</b> | 588         | 238         |        |         |       |         |       |         |       |
| 538.imagick_r    | 80     | <b>373</b>  | <b>534</b> | 368         | 540        | 374         | 532         |        |         |       |         |       |         |       |
| 544.nab_r        | 80     | 346         | 389        | <b>346</b>  | <b>390</b> | 343         | 393         |        |         |       |         |       |         |       |
| 549.fotonik3d_r  | 80     | <b>2025</b> | <b>154</b> | 2025        | 154        | 2040        | 153         |        |         |       |         |       |         |       |
| 554.roms_r       | 80     | 1417        | 89.7       | 1410        | 90.1       | <b>1411</b> | <b>90.1</b> |        |         |       |         |       |         |       |

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## General Notes

Environment variables set by runcpu before the start of the run:

LD\_LIBRARY\_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.4

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Transparent Huge Pages enabled by default

Prior to runcpu invocation

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## General Notes (Continued)

Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches  
runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

## Platform Notes

BIOS settings:

Sub NUMA Cluster Enabled

Virtualization Technology Disabled

System Profile set to Custom

CPU Performance set to Maximum Performance

C States set to Autonomous

C1E Disabled

Uncore Frequency set to Dynamic

Energy Efficiency Policy set to Performance

Memory Patrol Scrub Disabled

Logical Processor Enabled

CPU Interconnect Bus Link Power Management Disabled

PCI ASPM L1 Link Power Management Disabled

Sysinfo program /home/cpu2017/bin/sysinfo

Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9  
running on linux-in7q Fri Aug 17 20:44:05 2018

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) Gold 6148 CPU @ 2.40GHz
  2 "physical id"s (chips)
  80 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 20
  siblings : 40
  physical 0: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
  physical 1: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
```

From lscpu:

|                      |                |
|----------------------|----------------|
| Architecture:        | x86_64         |
| CPU op-mode(s):      | 32-bit, 64-bit |
| Byte Order:          | Little Endian  |
| CPU(s):              | 80             |
| On-line CPU(s) list: | 0-79           |
| Thread(s) per core:  | 2              |

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## Platform Notes (Continued)

```

Core(s) per socket: 20
Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6148 CPU @ 2.40GHz
Stepping: 4
CPU MHz: 2394.376
BogoMIPS: 4788.75
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 28160K
NUMA node0 CPU(s): 0,4,8,12,16,20,24,28,32,36,40,44,48,52,56,60,64,68,72,76
NUMA node1 CPU(s): 1,5,9,13,17,21,25,29,33,37,41,45,49,53,57,61,65,69,73,77
NUMA node2 CPU(s): 2,6,10,14,18,22,26,30,34,38,42,46,50,54,58,62,66,70,74,78
NUMA node3 CPU(s): 3,7,11,15,19,23,27,31,35,39,43,47,51,55,59,63,67,71,75,79
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmpfperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch ida arat epb invpcid_single pln pts
dtherm intel_pt rsb_ctxtsw spec_ctrl stibp retpoline kaiser tpr_shadow vnmi
flexpriority ept vpid fsgsbbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm
cqm mpx avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl
xsaveopt xsavec xgetbv1 cqm_llc cqm_occup_llc pkru ospke

```

```
/proc/cpuinfo cache data
cache size : 28160 KB
```

```
From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
```

```

available: 4 nodes (0-3)
node 0 cpus: 0 4 8 12 16 20 24 28 32 36 40 44 48 52 56 60 64 68 72 76
node 0 size: 95354 MB
node 0 free: 95102 MB
node 1 cpus: 1 5 9 13 17 21 25 29 33 37 41 45 49 53 57 61 65 69 73 77
node 1 size: 96756 MB
node 1 free: 96493 MB
node 2 cpus: 2 6 10 14 18 22 26 30 34 38 42 46 50 54 58 62 66 70 74 78
node 2 size: 96756 MB
node 2 free: 96542 MB
node 3 cpus: 3 7 11 15 19 23 27 31 35 39 43 47 51 55 59 63 67 71 75 79
node 3 size: 96754 MB

```

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## Platform Notes (Continued)

```
node 3 free: 96529 MB
node distances:
node  0   1   2   3
 0: 10  21  11  21
 1: 21  10  21  11
 2: 11  21  10  21
 3: 21  11  21  10

From /proc/meminfo
  MemTotal:      394877552 kB
  HugePages_Total:       0
  Hugepagesize:     2048 kB

/usr/bin/lsb_release -d
  SUSE Linux Enterprise Server 12 SP3

From /etc/*release* /etc/*version*
SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
  VERSION = 12
  PATCHLEVEL = 3
  # This file is deprecated and will be removed in a future service pack or release.
  # Please check /etc/os-release for details about this release.
os-release:
  NAME="SLES"
  VERSION="12-SP3"
  VERSION_ID="12.3"
  PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
  ID="sles"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:12:sp3"

uname -a:
  Linux linux-in7q 4.4.126-94.22-default #1 SMP Wed Apr 11 07:45:03 UTC 2018 (9649989)
  x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2017-5754 (Meltdown):           Mitigation: PTI
CVE-2017-5753 (Spectre variant 1):  Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2):  Mitigation: IBRS+IBPB

run-level 3 Aug 17 17:17 last=5

SPEC is set to: /home/cpu2017
  Filesystem  Type  Size  Used Avail Use% Mounted on
  /dev/sda4    xfs  889G  58G  832G   7%  /home
```

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## Platform Notes (Continued)

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 1.3.7 02/08/2018

Memory:

20x 00AD00B300AD HMA82GR7AFR8N-VK 16 GB 2 rank 2666  
4x 00AD063200AD HMA82GR7AFR8N-VK 16 GB 2 rank 2666

(End of data from sysinfo program)

## Compiler Version Notes

=====

CC 519.lbm\_r(base) 538.imagick\_r(base) 544.nab\_r(base)

=====

icc (ICC) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

=====

=====

CXXC 508.namd\_r(base) 510.parest\_r(base)

=====

icpc (ICC) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

=====

=====

CC 511.povray\_r(base) 526.blender\_r(base)

=====

icpc (ICC) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
icc (ICC) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

=====

=====

FC 507.cactuBSSN\_r(base)

=====

icpc (ICC) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
icc (ICC) 18.0.2 20180210  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.  
ifort (IFORT) 18.0.2 20180210  
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## Compiler Version Notes (Continued)

```
=====
FC 503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)
=====
```

```
ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
=====
```

```
=====
CC 521.wrf_r(base) 527.cam4_r(base)
=====
```

```
ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
=====
```

## Base Compiler Invocation

C benchmarks:

```
icc -m64 -std=c11
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
fort -m64
```

Benchmarks using both Fortran and C:

```
fort -m64 icc -m64 -std=c11
```

Benchmarks using both C and C++:

```
icpc -m64 icc -m64 -std=c11
```

Benchmarks using Fortran, C, and C++:

```
icpc -m64 icc -m64 -std=c11 ifort -m64
```

## Base Portability Flags

503.bwaves\_r: -DSPEC\_LP64

507.cactuBSSN\_r: -DSPEC\_LP64

508.namd\_r: -DSPEC\_LP64

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## Base Portability Flags (Continued)

510.parest\_r: -DSPEC\_LP64  
511.povray\_r: -DSPEC\_LP64  
519.lbm\_r: -DSPEC\_LP64  
521.wrf\_r: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG -convert big\_endian  
526.blender\_r: -DSPEC\_LP64 -DSPEC\_LINUX -funsigned-char  
527.cam4\_r: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG  
538.imagick\_r: -DSPEC\_LP64  
544.nab\_r: -DSPEC\_LP64  
549.fotonik3d\_r: -DSPEC\_LP64  
554.roms\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

Benchmarks using both C and C++:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3

Benchmarks using Fortran, C, and C++:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=3 -auto -nostandard-realloc-lhs

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge14G-revD.2018-07-24.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge14G-revD.2018-07-24.xml>

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