## SPEC® CPU2017 Integer Speed Result

**Huawei**

**Huawei XH628 V5 (Intel Xeon Gold 6152)**

<table>
<thead>
<tr>
<th>SPECspeed2017_int_base</th>
<th>9.07</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed2017_int_peak</td>
<td>9.39</td>
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</table>

**CPU2017 License:** 3175  
**Test Date:** Aug-2018  
**Test Sponsor:** Huawei  
**Hardware Availability:** Aug-2018  
**Tested by:** Huawei  
**Software Availability:** Mar-2018

### Threads

<table>
<thead>
<tr>
<th>Specification</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>44 threads</td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>44 threads</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>44 threads</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>44 threads</td>
</tr>
<tr>
<td>623.xalancbmk_s</td>
<td>44 threads</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>44 threads</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>44 threads</td>
</tr>
<tr>
<td>641.leela_s</td>
<td>44 threads</td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>44 threads</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>44 threads</td>
</tr>
</tbody>
</table>

### Software

**OS:** Red Hat Enterprise Linux Server release 7.4 (Maipo)  
**Compiler:** C/C++: Version 18.0.2.199 of Intel C/C++  
**Compiler for Linux:** Fortran: Version 18.0.2.199 of Intel Fortran  
**Compiler for Linux:**  
**Parallel:** Yes  
**Firmware:** Version 0.86 Released Aug-2018  
**File System:** xfs  
**System State:** Run level 3 (multi-user)  
**Base Pointers:** 64-bit  
**Peak Pointers:** 32/64-bit  
**Other:** jemalloc memory allocator V5.0.1

### Hardware

**CPU Name:** Intel Xeon Gold 6152  
**Max MHz.:** 3700  
**Nominal:** 2100  
**Enabled:** 44 cores, 2 chips  
**Orderable:** 1,2 chips  
**Cache L1:** 32 KB I + 32 KB D on chip per core  
**L2:** 1 MB I+D on chip per core  
**L3:** 30.25 MB I+D on chip per chip  
**Other:** None  
**Memory:** 384 GB (12 x 32 GB 2Rx4 PC4-2666V-R)  
**Storage:** 1 x 1800 GB SAS, 10000 RPM  
**Other:** None
Huawei
Huawei XH628 V5 (Intel Xeon Gold 6152)

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SPECspeed2017_int_base = 9.07
SPECspeed2017_int_peak = 9.39

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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<th>Ratio</th>
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<tbody>
<tr>
<td>600.perlbench_s</td>
<td>44</td>
<td>286</td>
<td>6.22</td>
<td>284</td>
<td>6.26</td>
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<td>44</td>
<td>235</td>
<td>7.55</td>
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<td>7.57</td>
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<tr>
<td>602.gcc_s</td>
<td>44</td>
<td>408</td>
<td>9.75</td>
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<td>605.mcf_s</td>
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<td>227</td>
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<td>220</td>
<td>7.41</td>
<td>219</td>
<td>7.45</td>
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<td>623.xalanchmk_s</td>
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<td>148</td>
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<td>148</td>
<td>9.56</td>
<td>149</td>
<td>9.50</td>
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<td>139</td>
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<td>139</td>
<td>10.2</td>
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<tr>
<td>625.x264_s</td>
<td>44</td>
<td>158</td>
<td>11.2</td>
<td>158</td>
<td>11.2</td>
<td>158</td>
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<td>631.deepsjeng_s</td>
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<td>271</td>
<td>5.29</td>
<td>271</td>
<td>5.29</td>
<td>271</td>
<td>5.29</td>
<td>44</td>
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<td>4.32</td>
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<td>4.32</td>
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<tr>
<td>648.exchange2_s</td>
<td>44</td>
<td>218</td>
<td>13.5</td>
<td>218</td>
<td>13.5</td>
<td>218</td>
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<td>13.5</td>
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<tr>
<td>657.xz_s</td>
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<td>23.0</td>
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<td>23.0</td>
<td>44</td>
<td>266</td>
<td>23.2</td>
<td>264</td>
<td>23.4</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
OMP_STACKSIZE = "192M" 

Binaries compiled on a system with 1x Intel Core i7-6700K CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>/proc/sys/vm/drop_caches
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
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**SPECspeed2017_int_base** = 9.07  
**SPECspeed2017_int_peak** = 9.39

---

### Platform Notes

- BIOS configuration:
- Power Policy Set to Load Balance
- Hyper-Threading Set to Disable
- XPT Prefetch Set to Enabled
- Sysinfo program /spec2017/bin/sysinfo
- Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
  running on localhost.localdomain Thu Aug 9 19:27:26 2018

**SUT (System Under Test) info as seen by some common utilities.**

For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo:

- model name: Intel(R) Xeon(R) Gold 6152 CPU @ 2.10GHz
- 2 "physical id"s (chips)
- 44 "processors"

- cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  - cpu cores: 22
  - siblings: 22
  - physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28
  - physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28

From lscpu:

- Architecture: x86_64
- CPU op-mode(s): 32-bit, 64-bit
- Byte Order: Little Endian
- CPU(s): 44
- On-line CPU(s) list: 0-43
- Core(s) per socket: 22
- Socket(s): 2
- NUMA node(s): 2
- Vendor ID: GenuineIntel
- CPU family: 6
- Model: 85
- Model name: Intel(R) Xeon(R) Gold 6152 CPU @ 2.10GHz
- Stepping: 4
- CPU MHz: 2101.000
- CPU max MHz: 2101.0000
- CPU min MHz: 1000.0000
- BogoMIPS: 4200.00
- Virtualization: VT-x
- L1d cache: 32K
- L1i cache: 32K
- L2 cache: 1024K
- L3 cache: 30976K

(Continued on next page)
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Platform Notes (Continued)

NUMA node0 CPU(s): 0-21
NUMA node1 CPU(s): 22-43
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dtc acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good ntop nonstop_tsc
aperfmpref perf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 fma
cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3nowprefetch epb cat_13 cdp_13 invpcid_single
intel_pt spec_ctrl ibpb_support tpr_shadow vmni flexpriority ept vpid fsgsbase
tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a avx512f avx512dq
rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsaves xgetbv1
cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local dtherm ida arat pln pts

From /proc/cpuinfo cache data
  cache size : 30976 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
  available: 2 nodes (0-1)
  node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21
  node 0 size: 194741 MB
  node 0 free: 189949 MB
  node 1 cpus: 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43
  node 1 size: 196608 MB
  node 1 free: 191586 MB
  node distances:
    node 0 1
    0: 10 21
    1: 21 10

From /proc/meminfo
  MemTotal: 394174880 kB
  HugePages_Total: 0
  Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
  os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.4 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VARIANT="Server"
    VARIANT_ID="server"
    VERSION_ID="7.4"
    PRETTY_NAME="Red Hat Enterprise Linux Server 7.4 (Maipo)"
  redhat-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)
  system-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)

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Platform Notes (Continued)

system-release-cpe: cpe:/o:redhat:enterprise_linux:7.4:ga:server

uname -a:
    Linux localhost.localdomain 3.10.0-693.11.6.el7.x86_64 #1 SMP Thu Dec 28 14:23:39 EST 2017 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Aug 9 19:24

SPEC is set to: /spec2017

Filesystem     Type  Size  Used Avail Use% Mounted on
/dev/sda4      xfs   553G  5.6G  547G   2% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS INSYDE Corp. 0.86 08/06/2018
Memory:
    4x NO DIMM NO DIMM
    12x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
| CC  600.perlbench_s(base) 602.gcc_s(base) 605.mcf_s(base) 625.x264_s(base, peak) 657.xz_s(base) |
==============================================================================
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
| CC  600.perlbench_s(peak) 602.gcc_s(peak) 605.mcf_s(peak) 657.xz_s(peak) |
==============================================================================
icc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
| CXXC 620.omnetpp_s(base) 623.xalancbmk_s(base) 631.deepsjeng_s(base) 641.leela_s(base) |
==============================================================================
icpc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

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Compiler Version Notes (Continued)

CXXC 620.omnetpp_s(peak) 623.xalancbmk_s(peak) 631.deepsjeng_s(peak)
641.leela_s(peak)

icpc (ICC) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

FC 648.exchange2_s(base, peak)

ifort (IFORT) 18.0.2 20180210
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64
Huawei

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</table>

**Base Optimization Flags**

C benchmarks:
- Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
- qopt-mem-layout-trans=3 -gopenmp -DSPEC_OPENMP
- L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:
- Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
- qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

Fortran benchmarks:
- Wl,-z,muldefs -xCORE-AVX2 -ipo -O3 -no-prec-div
- qopt-mem-layout-trans=3 -nostandard-realloc-lhs
- L/usr/local/je5.0.1-64/lib -ljemalloc

**Peak Compiler Invocation**

C benchmarks:
icc -m64 -std=c11

C++ benchmarks (except as noted below):
icpc -m64

623.xalancbmk_s: icpc -m32 -L/home/prasadj/specdev/IC18u2_Internal/lin_18_0_20180210/compiler/lib/ia32_lin

Fortran benchmarks:
ifort -m64

**Peak Portability Flags**

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
505.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64
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Peak Optimization Flags

C benchmarks:

600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX2 -qopt-prefetch -ipo -O3
-qopt-mem-layout-trans=3 -no-prec-div
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-fno-strict-overflow -L/usr/local/je5.0.1-64/lib
-ljemalloc

602.gcc_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX2 -qopt-prefetch -ipo -O3
-qopt-mem-layout-trans=3 -no-prec-div
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

605.mcf_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc

625.x264_s: -Wl,-z,muldefs -xCORE-AVX2 -ipo -03 -no-prec-div
-qopt-prefetch -qopt-mem-layout-trans=3 -qopenmp
-DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc

657.xz_s: Same as 602.gcc_s

C++ benchmarks:

620.omnetpp_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc

623.xalancbmk_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX2 -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3 -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -L/usr/local/je5.0.1-32/lib -ljemalloc

631.deepsjeng_s: basepeak = yes
641.leela_s: basepeak = yes

Fortran benchmarks:

-Wl,-z,muldefs -xCORE-AVX2 -ipo -03 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs
-L/usr/local/je5.0.1-64/lib -ljemalloc
Huawei

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The flags files that were used to format this result can be browsed at:


You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-12-21.xml
http://www.spec.org/cpu2017/flags/Huawei-Platform-Settings-SKL-V1.9-revC.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2018-08-09 15:27:25-0400.
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