### Lenovo Global Technology

**ThinkSystem SN550 (3.80 GHz, Intel Xeon Gold 5222)**

<table>
<thead>
<tr>
<th>SPECrate2017_fp_base =</th>
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</tr>
</thead>
<tbody>
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<td>Not Run</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 9017

**Test Date:** May-2019

**Test Sponsor:** Lenovo Global Technology

**Hardware Availability:** Apr-2019

**Tested by:** Lenovo Global Technology

**Software Availability:** Nov-2018

### Copies

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>16</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>16</td>
</tr>
<tr>
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<td>16</td>
</tr>
<tr>
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<td>16</td>
</tr>
<tr>
<td>511.povray_r</td>
<td>16</td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>16</td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>16</td>
</tr>
<tr>
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</tr>
<tr>
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<td>16</td>
</tr>
<tr>
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<td>16</td>
</tr>
<tr>
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<td>16</td>
</tr>
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<td>16</td>
</tr>
<tr>
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<td>16</td>
</tr>
</tbody>
</table>

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</tr>
</thead>
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</tr>
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</tr>
<tr>
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</tbody>
</table>

### Hardware

**CPU Name:** Intel Xeon Gold 5222

**Max MHz.:** 3900

**Nominal:** 3800

**Enabled:** 8 cores, 2 chips, 2 threads/core

**Orderable:** 1.2 chips

**Cache L1:** 32 KB I + 32 KB D on chip per core

**L2:** 1 MB I+D on chip per core

**L3:** 16.5 MB I+D on chip per chip

**Other:** None

**Memory:** 768 GB (24 x 32 GB 2Rx4 PC4-2933Y-R)

**Storage:** 1 x 960 GB SATA SSD

**Other:** None

### Software

**OS:** SUSE Linux Enterprise Server 15 (x86_64)

**Kernel:** 4.12.14-25.13-default

**Compiler:** C/C++: Version 19.0.1.144 of Intel C/C++

**Compiler Build:** 20181018 for Linux;

**Fortran:** Version 19.0.1.144 of Intel Fortran

**Compiler Build:** 20181018 for Linux

**Parallel:** No

**Firmware:** Lenovo BIOS Version IVE135M 2.10 released Jan-2019

**File System:** xfs

**System State:** Run level 3 (multi-user)

**Base Pointers:** 64-bit

**Peak Pointers:** Not Applicable

**Other:** None
SPEC CPU2017 Floating Point Rate Result

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Lenovo Global Technology
ThinkSystem SN550
(3.80 GHz, Intel Xeon Gold 5222)

SPECrater2017_fp_base = 77.6
SPECrater2017_fp_peak = Not Run

Test Date: May-2019
Hardware Availability: Apr-2019
Software Availability: Nov-2018

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>16</td>
<td>557</td>
<td>288</td>
<td>558</td>
<td>287</td>
<td>557</td>
<td>288</td>
<td></td>
<td></td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>16</td>
<td>412</td>
<td>49.2</td>
<td>410</td>
<td>49.4</td>
<td>411</td>
<td>49.3</td>
<td></td>
<td></td>
</tr>
<tr>
<td>508.namd_r</td>
<td>16</td>
<td>331</td>
<td>46.0</td>
<td>332</td>
<td>45.8</td>
<td>328</td>
<td>46.3</td>
<td></td>
<td></td>
</tr>
<tr>
<td>510.parest_r</td>
<td>16</td>
<td>704</td>
<td>59.4</td>
<td>709</td>
<td>59.0</td>
<td>709</td>
<td>59.0</td>
<td></td>
<td></td>
</tr>
<tr>
<td>511.povray_r</td>
<td>16</td>
<td>512</td>
<td>73.0</td>
<td>515</td>
<td>72.5</td>
<td>511</td>
<td>73.1</td>
<td></td>
<td></td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>16</td>
<td>374</td>
<td>45.1</td>
<td>374</td>
<td>45.1</td>
<td>374</td>
<td>45.1</td>
<td></td>
<td></td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>16</td>
<td>397</td>
<td>90.4</td>
<td>408</td>
<td>87.8</td>
<td>403</td>
<td>88.9</td>
<td></td>
<td></td>
</tr>
<tr>
<td>526.blender_r</td>
<td>16</td>
<td>359</td>
<td>67.9</td>
<td>359</td>
<td>67.9</td>
<td>359</td>
<td>67.9</td>
<td></td>
<td></td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>16</td>
<td>362</td>
<td>77.4</td>
<td>372</td>
<td>75.3</td>
<td>375</td>
<td>74.6</td>
<td></td>
<td></td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>16</td>
<td>264</td>
<td>151</td>
<td>262</td>
<td>152</td>
<td>263</td>
<td>151</td>
<td></td>
<td></td>
</tr>
<tr>
<td>544.nab_r</td>
<td>16</td>
<td>263</td>
<td>102</td>
<td>267</td>
<td>101</td>
<td>261</td>
<td>103</td>
<td></td>
<td></td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>16</td>
<td>757</td>
<td>82.4</td>
<td>753</td>
<td>82.8</td>
<td>758</td>
<td>82.3</td>
<td></td>
<td></td>
</tr>
<tr>
<td>554.roms_r</td>
<td>16</td>
<td>503</td>
<td>50.5</td>
<td>506</td>
<td>50.3</td>
<td>503</td>
<td>50.5</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017-1.0.5-ic19.0u1/lib/intel64"

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Lenovo Global Technology
ThinkSystem SN550
(3.80 GHz, Intel Xeon Gold 5222)

SPECrate2017_fp_base = 77.6
SPECrate2017_fp_peak = Not Run

General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3640 (Spectre variant 3a) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3639 (Spectre variant 4) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance
Trusted Execution Technology set to Enable
SNC set to Enable
CPU Frequency Limits set to Restrict Maximum Frequency
Workload Configuration set to I/O Sensitive
Sysinfo program /home/cpu2017-1.0.5-ic19.0u1/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on linux-4brr Fri May 24 15:06:59 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 5222 CPU @ 3.80GHz
2 "physical id"s (chips)
16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 4
siblings : 8
physical 0: cores 1 2 12 13
physical 1: cores 2 5 10 13

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 16
On-line CPU(s) list: 0-15
Thread(s) per core: 2
Core(s) per socket: 4
Socket(s): 2
NUMA node(s): 4

(Continued on next page)
Lenovo Global Technology
ThinkSystem SN550
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Platform Notes (Continued)

Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 5222 CPU @ 3.80GHz
Stepping: 6
CPU MHz: 3800.000
CPU max MHz: 3900.0000
CPU min MHz: 1200.0000
BogoMIPS: 7600.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 16896K
NUMA node0 CPU(s): 0,1,8,9
NUMA node1 CPU(s): 2,3,10,11
NUMA node2 CPU(s): 4,6,12,14
NUMA node3 CPU(s): 5,7,13,15
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpref perf pni pclmulqdq dtes64 msr vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm
pcid dca lahf_lm abm 3dnowprefetch cpuid_fault epb cat_13 cdpl3 invpcid_single ssbd
mba ibrs stibp tpr_shadown vnmi flexpriority ept vpid fsgsbase tsc_adjust bmid
hle avx2 smep bmi2 erms invpcid rtm cmq mpx rdt_a avx512f avx512dq rdseed adx smap
ciflushopt clwb intel_pt avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves
cmq_llc cmq_occup_llc cmq_mbb_total cmq_mbb_local dtherm ida arat pln pts pku ospke
avx512_vnni flush_lld arch_capabilities

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0 1 8 9
node 0 size: 193137 MB
node 0 free: 187056 MB
node 1 cpus: 2 3 10 11
node 1 size: 193524 MB
node 1 free: 193206 MB
node 2 cpus: 4 6 12 14
node 2 size: 193524 MB
node 2 free: 193269 MB
node 3 cpus: 5 7 13 15
node 3 size: 193493 MB

SPECrate2017_fp_base = 77.6
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CPU2017 License: 9017
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Lenovo Global Technology
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**SpecSystem SN550**  
**(3.80 GHz, Intel Xeon Gold 5222)**

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**SPECrate2017_fp_base** = 77.6

**SPECrate2017_fp_peak** = Not Run

### Platform Notes (Continued)

24x Samsung M393A4K40CB2-CVF 32 GB 2 rank 2933

(End of data from sysinfo program)

### Compiler Version Notes

```plaintext
==============================================================================
CC  519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
CXXC 508.namd_r(base) 510.parest_r(base)
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
CC  511.povray_r(base) 526.blender_r(base)
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
FC  507.cactuBSSN_r(base)
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
```

(Continued on next page)
## Lenovo Global Technology

**ThinkSystem SN550**
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**SPEC CPU2017 Floating Point Rate Result**

### Compiler Version Notes (Continued)

```plaintext
FC  503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)

------------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

------------------------------------------------------------------------------
CC  521.wrf_r(base) 527.cam4_r(base)

------------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
```

### Base Compiler Invocation

**C benchmarks:**

```plaintext
icc -m64 -std=c11
```

**C++ benchmarks:**

```plaintext
icpc -m64
```

**Fortran benchmarks:**

```plaintext
ifort -m64
```

**Benchmarks using both Fortran and C:**

```plaintext
ifort -m64 icc -m64 -std=c11
```

**Benchmarks using both C and C++:**

```plaintext
icpc -m64 icc -m64 -std=c11
```

**Benchmarks using Fortran, C, and C++:**

```plaintext
icpc -m64 icc -m64 -std=c11 ifort -m64
```

### Base Portability Flags

503.bwaves_r: -DSPEC_LP64

(Continued on next page)
SPEC CPU2017 Floating Point Rate Result

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```
Base Portability Flags (Continued)

507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64
```

```
Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Benchmarks using both C and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

Benchmarks using Fortran, C, and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte
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The flags files that were used to format this result can be browsed at:
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-A.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-A.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.