ASUSTeK Computer Inc.

ASUS ESC8000 G4(Z11PG-D24) Server System
(3.60 GHz, Intel Xeon Gold 6244)

SPECrate®2017_int_base = 144
SPECrate®2017_int_peak = 148

CPU2017 License: 9016
Test Sponsor: ASUSTeK Computer Inc.
Test Date: May-2019
Hardware Availability: Apr-2019

Tested by: ASUSTeK Computer Inc.
Software Availability: Nov-2018

Hardware

CPU Name: Intel Xeon Gold 6244
Max MHz: 4400
Nominal: 3600
Enabled: 16 cores, 2 chips, 2 threads/core
Orderable: 1.2 chips
Cache L1: 32 KB I + 32 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 24.75 MB I+D on chip per chip
Other: None

Memory: 768 GB (24 x 32 GB 2Rx4 PC4-2933Y-R)
Storage: 1 x 1 TB SATA SSD
Other: None

Software

OS: SUSE Linux Enterprise Server 15
Compiler: C/C++: Version 19.0.1.144 of Intel C/C++
Compiler Build 20181018 for Linux;
Fortran: Version 19.0.1.144 of Intel Fortran
Compiler Build 20181018 for Linux

Parallel: No
Firmware: Version 5102 released Feb-2019
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other:jemalloc: jemalloc memory allocator library
V5.0.1

Power Management: --
ASUSTeK Computer Inc.

ASUS ESC8000 G4(Z11PG-D24) Server System
(3.60 GHz, Intel Xeon Gold 6244)

**SPECrate®2017_int_base = 144**

**SPECrate®2017_int_peak = 148**

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**Results Table**

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>32</td>
<td>461</td>
<td>110</td>
<td>463</td>
<td>110</td>
<td>468</td>
<td>109</td>
<td>32</td>
<td>408</td>
<td>125</td>
<td>407</td>
<td>125</td>
<td>408</td>
<td>125</td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>32</td>
<td>375</td>
<td>121</td>
<td>370</td>
<td>122</td>
<td>370</td>
<td>123</td>
<td>32</td>
<td>335</td>
<td>135</td>
<td>334</td>
<td>136</td>
<td>334</td>
<td>136</td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>32</td>
<td>261</td>
<td>198</td>
<td>261</td>
<td>198</td>
<td>260</td>
<td>199</td>
<td>32</td>
<td>261</td>
<td>198</td>
<td>261</td>
<td>198</td>
<td>262</td>
<td>198</td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>32</td>
<td>460</td>
<td>91.2</td>
<td>459</td>
<td>91.5</td>
<td>459</td>
<td>91.4</td>
<td>32</td>
<td>460</td>
<td>91.2</td>
<td>459</td>
<td>91.5</td>
<td>459</td>
<td>91.4</td>
</tr>
<tr>
<td>523.xalancbmk_r</td>
<td>32</td>
<td>182</td>
<td>186</td>
<td>183</td>
<td>185</td>
<td>180</td>
<td>188</td>
<td>32</td>
<td>177</td>
<td>191</td>
<td>177</td>
<td>191</td>
<td>177</td>
<td>191</td>
</tr>
<tr>
<td>525.x264_r</td>
<td>32</td>
<td>196</td>
<td>286</td>
<td>197</td>
<td>284</td>
<td>199</td>
<td>282</td>
<td>32</td>
<td>188</td>
<td>298</td>
<td>188</td>
<td>298</td>
<td>189</td>
<td>297</td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
<td>32</td>
<td>312</td>
<td>118</td>
<td>312</td>
<td>118</td>
<td>312</td>
<td>118</td>
<td>32</td>
<td>312</td>
<td>117</td>
<td>312</td>
<td>117</td>
<td>312</td>
<td>118</td>
</tr>
<tr>
<td>541.leela_r</td>
<td>32</td>
<td>485</td>
<td>109</td>
<td>486</td>
<td>109</td>
<td>483</td>
<td>110</td>
<td>32</td>
<td>485</td>
<td>109</td>
<td>486</td>
<td>109</td>
<td>483</td>
<td>110</td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>32</td>
<td>333</td>
<td>252</td>
<td>333</td>
<td>252</td>
<td>333</td>
<td>252</td>
<td>32</td>
<td>333</td>
<td>252</td>
<td>333</td>
<td>252</td>
<td>333</td>
<td>252</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>32</td>
<td>375</td>
<td>92.2</td>
<td>374</td>
<td>92.3</td>
<td>375</td>
<td>92.2</td>
<td>32</td>
<td>375</td>
<td>92.2</td>
<td>375</td>
<td>92.2</td>
<td>375</td>
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</tr>
</tbody>
</table>

---

**Submit Notes**

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

---

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"

---

**General Notes**

Environment variables set by runcpu before the start of the run:
```
LD_LIBRARY_PATH = "/spec2017_2019u1/lib/ia32:/spec2017_2019u1/lib/intel64:
/spec2017_2019u1/je5.0.1-32:/spec2017_2019u1/je5.0.1-64"
```

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:
```
sync; echo 3>/proc/sys/vm/drop_caches
```

runcpu command invoked through numactl i.e.:
```
numactl --interleave=all runcpu <etc>
```

jemalloc: configured and built at default for 32bit (i686) and 64bit (x86_64) targets;
jemalloc: built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5;

---

(Continued on next page)
General Notes (Continued)

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS Configuration:
VT-d = Disabled
Patrol Scrub = Disabled
ENERGY_PERF_BIAS_CFG mode = performance
SNC = Enabled
IMC interleaving = 1-way
Engine Boost = Level3(Max)
Enforce POR = Disable
Memory Frequency = 2933
LLC dead line alloc = Disabled
SR-IOV Support = Disabled
CSM Support = Disabled
Sysinfo program /spec2017/bin/sysinfo
Rev: r5974 of 2018-05-19 9bce8f2999c33d61f64985e45859ea9
running on linux-gh78 Mon May 13 09:54:07 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6244 CPU @ 3.60GHz
  2 "physical id"s (chips)
  32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 8
  siblings : 16
  physical 0: cores 2 3 4 16 17 20 24 27
  physical 1: cores 2 3 11 17 20 24 25 27

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 32
On-line CPU(s) list: 0-31
SPEC CPU®2017 Integer Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

ASUSTeK Computer Inc.

ASUS ESC8000 G4(Z11PG-D24) Server System
(3.60 GHz, Intel Xeon Gold 6244)

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CPU2017 License: 9016
Test Sponsor: ASUSTeK Computer Inc.
Tested by: ASUSTeK Computer Inc.
Test Date: May-2019
Hardware Availability: Apr-2019
Software Availability: Nov-2018

Platform Notes (Continued)

Thread(s) per core: 2
Core(s) per socket: 8
Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6244 CPU @ 3.60GHz
Stepping: 7
CPU MHz: 3600.000
CPU max MHz: 4400.0000
CPU min MHz: 1200.0000
BogoMIPS: 7200.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 25344K
NUMA node0 CPU(s): 0,3,4,6,16,19,20,22
NUMA node1 CPU(s): 1,2,5,7,17,18,21,23
NUMA node2 CPU(s): 8,11,13,14,24,27,29,30
NUMA node3 CPU(s): 9,10,12,15,25,26,28,31
Flags: fpu vme de pse tsc msr pae mce cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni pmlns qdts64 monitor ds_cpl vmx smx est tm2 ssse3 sse4_1 sse4_2 x2apic movbe popcnt tread_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3 invpcid_single mba tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bts hle avx2 smep bmi2 erms invpcid rtm cqm mxm rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local ibpb ibrs stibp dtlbld ida arat pln pts hwp hwp_act_window hwp_epp hwp_klg req pku ospke avx512_vnni arch_capabilities ssbd

/platform/cpuintel data
cache size : 25344 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0 3 4 6 16 19 20 22
node 0 size: 192025 MB
node 0 free: 191706 MB
node 1 cpus: 1 2 5 7 17 18 21 23
node 1 size: 193513 MB
node 1 free: 193253 MB

(Continued on next page)
Platform Notes (Continued)

node 2 cpus: 8 11 13 14 24 27 29 30
node 2 size: 193513 MB
node 2 free: 193209 MB
node 3 cpus: 9 10 12 15 25 26 28 31
node 3 size: 193509 MB
node 3 free: 193227 MB
node distances:
  node 0 1 2 3
  0: 10 11 21 21
  1: 11 10 21 21
  2: 21 21 10 11
  3: 21 21 11 10

From /proc/meminfo
  MemTotal:  791103716 kB
  HugePages_Total: 0
  Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
  os-release:
    NAME="SLES"
    VERSION="15"
    VERSION_ID="15"
    PRETTY_NAME="SUSE Linux Enterprise Server 15"
    ID="sles"
    ID_LIKE="suse"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:15"

  uname -a:
    Linux linux-gh78 4.12.14-23-default #1 SMP Tue May 29 21:04:44 UTC 2018 (cd0437b)
    x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

  CVE-2017-5754 (Meltdown): Not affected
  CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
  CVE-2017-5715 (Spectre variant 2): Mitigation: Indirect Branch Restricted Speculation, IBPB, IBRS_FW

run-level 3 May 13 09:51

SPEC is set to: /spec2017

  Filesystem      Type      Size  Used  Avail Use% Mounted on
  /dev/sda4       xfs       929G  10G   919G   2%   /

Additional information from dmidecode follows. WARNING: Use caution when you interpret
ASUSTeK Computer Inc.
ASUS ESC8000 G4(Z11PG-D24) Server System
(3.60 GHz, Intel Xeon Gold 6244)

SPECrate®2017_int_base = 144
SPECrate®2017_int_peak = 148

Platform Notes (Continued)

this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. 5102 02/11/2019
Memory:
24x Samsung M393A4K40CB2-CVF 32 GB 2 rank 2933, configured at 2934

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C     | 502.gcc_r(peak)
==============================================================================
 Intel(R) C Intel(R) 64 Compiler for applications running on IA-32, Version
 19.0.1.144 Build 20181018
 Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

==============================================================================
C     | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak)
    | 525.x264_r(base, peak) 557.xz_r(base, peak)
==============================================================================
 Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
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==============================================================================
C     | 502.gcc_r(peak)
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==============================================================================
C     | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak)
    | 525.x264_r(base, peak) 557.xz_r(base, peak)
==============================================================================
 Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
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(Continued on next page)
ASUSTeK Computer Inc.  
ASUS ESC8000 G4(Z11PG-D24) Server System  
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SPEC CPU®2017 Integer Rate Result  

Copyright 2017-2020 Standard Performance Evaluation Corporation  

SPEC CPU®2017 _int_base = 144  
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Test Sponsor: ASUSTeK Computer Inc.  
Test Date: May-2019  
Hardware Availability: Apr-2019  
Tested by: ASUSTeK Computer Inc.  
Software Availability: Nov-2018  

Compiler Version Notes (Continued)  

C++ | 523.xalancbmk_r(peak)  

Intel(R) C++ Intel(R) 64 Compiler for applications running on IA-32, Version 19.0.1.144 Build 20181018 
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

C++ | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base)  
| 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)  

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.1.144 Build 20181018  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

C++ | 523.xalancbmk_r(peak)  

Intel(R) C++ Intel(R) 64 Compiler for applications running on IA-32, Version 19.0.1.144 Build 20181018 
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

C++ | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base)  
| 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)  

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.1.144 Build 20181018  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

Fortran | 548.exchange2_r(base, peak)  

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.1.144 Build 20181018  
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

Base Compiler Invocation  

C benchmarks:  
icc -m64 -std=c11  

(Continued on next page)
**SPEC CPU®2017 Integer Rate Result**

**ASUSTeK Computer Inc.**

ASUS ESC8000 G4(Z11PG-D24) Server System
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<table>
<thead>
<tr>
<th><strong>CPU2017 License:</strong> 9016</th>
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</tr>
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<tbody>
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**SPECrate®2017_int_base = 144**

**SPECrate®2017_int_peak = 148**

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Base Compiler Invocation (Continued)

C++ benchmarks:
```c
icpc -m64
```

Fortran benchmarks:
```fortran
ifort -m64
```

---

**Base Portability Flags**

- 500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
- 502.gcc_r: -DSPEC_LP64
- 505.mcf_r: -DSPEC_LP64
- 520.omnetpp_r: -DSPEC_LP64
- 523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
- 525.x264_r: -DSPEC_LP64
- 531.deepsjeng_r: -DSPEC_LP64
- 541.leela_r: -DSPEC_LP64
- 548.exchange2_r: -DSPEC_LP64
- 557.xz_r: -DSPEC_LP64

---

**Base Optimization Flags**

C benchmarks:
```c
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-lqkmalloc
```

C++ benchmarks:
```c
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-lqkmalloc
```

Fortran benchmarks:
```fortran
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-lqkmalloc
```
ASUSTeK Computer Inc.
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Software Availability: Nov-2018

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64 -std=c11


C++ benchmarks (except as noted below):
icpc -m64

523.xalancbmk_r: icpc -m32 -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/ia32_lin

Fortran benchmarks:
ifort -m64

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:
500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4
-fno-strict-overflow
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-lqkmalloc

502.gcc_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4
-L/usr/local/je5.0.1-32/lib -ljemalloc

505.mcf_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4

(Continued on next page)
Peak Optimization Flags (Continued)

505.mcf_r (continued):
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-lqkmalloc

525.x264_r -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-alias
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-lqkmalloc

557.xz_r: Same as 505.mcf_r

C++ benchmarks:

520.omnetpp_r: basepeak = yes

523.xalancbmk_r -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4
-L/usr/local/je5.0.1-32/lib -ljemalloc

531.deepsjeng_r -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-lqkmalloc

541.leela_r: basepeak = yes

Fortran benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.1.144/linux/compiler/lib/intel64
-lqkmalloc

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/ASUSTeKPlatform-Settings-z11-V2.0-revD.xml
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<tr>
<td>Test Sponsor: ASUSTeK Computer Inc.</td>
<td>Hardware Availability: Apr-2019</td>
</tr>
<tr>
<td>Tested by: ASUSTeK Computer Inc.</td>
<td>Software Availability: Nov-2018</td>
</tr>
</tbody>
</table>

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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