Dell Inc.

PowerEdge M640 (Intel Xeon Gold 6230, 2.10GHz)

SPEC® CPU2017 Integer Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate2017_int_base = 219

SPECrate2017_int_peak = 227

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Mar-2019
Hardware Availability: Apr-2019
Software Availability: May-2019

Hardware

CPU Name: Intel Xeon Gold 6230
Max MHz.: 3900
Nominal: 2100
Enabled: 40 cores, 2 chips, 2 threads/core
Orderable: 1.2 chips
Cache L1: 32 KB I + 32 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 27.5 MB I+D on chip per chip
Other: None
Memory: 384 GB (12 x 32 GB 2Rx4 PC4-2933Y-R)
Storage: 1 x 480 GB SATA SSD
Other: None

Software

OS: Ubuntu 18.04.2 LTS
Compiler: C/C++: Version 19.0.4.227 of Intel
Compiler Build 20190416 for Linux;
Fortran: Version 19.0.4.227 of Intel Fortran
Compiler Build 20190416 for Linux
Parallel: No
Firmware: Version 2.3.1 released May-2019
File System: ext4
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other: jemalloc memory allocator V5.0.1
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**SPECrate2017_int_peak = 227**

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Copies</th>
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<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>80</td>
<td>768</td>
<td>166</td>
<td>768</td>
<td>166</td>
<td>769</td>
<td>166</td>
<td>80</td>
<td>666</td>
<td>191</td>
<td>666</td>
<td>191</td>
<td>668</td>
<td>191</td>
<td></td>
<td></td>
</tr>
<tr>
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<td>80</td>
<td>635</td>
<td>179</td>
<td>637</td>
<td>178</td>
<td>639</td>
<td>177</td>
<td>80</td>
<td>555</td>
<td>204</td>
<td>553</td>
<td>205</td>
<td>555</td>
<td>204</td>
<td></td>
<td></td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>80</td>
<td><strong>458</strong></td>
<td><strong>282</strong></td>
<td>459</td>
<td>282</td>
<td>457</td>
<td>283</td>
<td>80</td>
<td><strong>459</strong></td>
<td><strong>282</strong></td>
<td>459</td>
<td>282</td>
<td>459</td>
<td>282</td>
<td></td>
<td></td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>80</td>
<td>736</td>
<td>143</td>
<td>737</td>
<td>142</td>
<td><strong>736</strong></td>
<td><strong>143</strong></td>
<td>80</td>
<td><strong>736</strong></td>
<td><strong>143</strong></td>
<td>736</td>
<td>143</td>
<td>737</td>
<td>142</td>
<td></td>
<td></td>
</tr>
<tr>
<td>523.xalanbmk_r</td>
<td>80</td>
<td>346</td>
<td>244</td>
<td>346</td>
<td>244</td>
<td>346</td>
<td>244</td>
<td>80</td>
<td>327</td>
<td>258</td>
<td>327</td>
<td>258</td>
<td>327</td>
<td>258</td>
<td></td>
<td></td>
</tr>
<tr>
<td>525.x264_r</td>
<td>80</td>
<td>323</td>
<td>433</td>
<td>322</td>
<td>435</td>
<td><strong>323</strong></td>
<td><strong>434</strong></td>
<td>80</td>
<td><strong>310</strong></td>
<td><strong>452</strong></td>
<td>310</td>
<td>452</td>
<td>310</td>
<td>452</td>
<td></td>
<td></td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
<td>80</td>
<td>507</td>
<td>181</td>
<td><strong>508</strong></td>
<td><strong>181</strong></td>
<td>508</td>
<td>181</td>
<td>80</td>
<td>509</td>
<td>180</td>
<td><strong>508</strong></td>
<td><strong>180</strong></td>
<td>508</td>
<td>181</td>
<td></td>
<td></td>
</tr>
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<td>80</td>
<td>761</td>
<td>174</td>
<td>776</td>
<td>171</td>
<td><strong>765</strong></td>
<td><strong>173</strong></td>
<td>80</td>
<td>761</td>
<td>174</td>
<td>760</td>
<td>174</td>
<td><strong>760</strong></td>
<td><strong>174</strong></td>
<td></td>
<td></td>
</tr>
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<td>80</td>
<td><strong>473</strong></td>
<td><strong>443</strong></td>
<td>473</td>
<td>443</td>
<td>475</td>
<td>442</td>
<td>80</td>
<td>474</td>
<td>442</td>
<td>474</td>
<td>442</td>
<td><strong>474</strong></td>
<td><strong>442</strong></td>
<td></td>
<td></td>
</tr>
<tr>
<td>557.xz_r</td>
<td>80</td>
<td><strong>597</strong></td>
<td><strong>145</strong></td>
<td>597</td>
<td>145</td>
<td>598</td>
<td>144</td>
<td>80</td>
<td>596</td>
<td>145</td>
<td><strong>596</strong></td>
<td><strong>145</strong></td>
<td>597</td>
<td>145</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"
```

Binaries compiled on a system with 1x Intel Core i9–7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Transparent Huge Pages enabled by default

Prior to runcpu invocation

```
Filesystem page cache synced and cleared with:
sync; echo 3>/proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
```

(Continued on next page)
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#### General Notes (Continued)

- numactl --interleave=all runcpu <etc>
- jemalloc, a general purpose malloc implementation
- built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

#### Platform Notes

- BIOS settings:
  - ADDDC setting disabled
  - Sub NUMA Cluster enabled
  - Virtualization Technology disabled
  - DCU Streamer Prefetcher enabled
  - System Profile set to Custom
  - CPU Performance set to Maximum Performance
  - C States set to Autonomous
  - C1E disabled
  - Uncore Frequency set to Dynamic
  - Energy Efficiency Policy set to Performance
  - Memory Patrol Scrub disabled
  - Logical Processor enabled
  - CPU Interconnect Bus Link Power Management disabled
  - PCI ASPM L1 Link Power Management disabled
  - Sysinfo program /home/cpu2017/bin/sysinfo
  - Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
  - running on intel-sut Fri Jul 5 16:29:11 2019

SUT (System Under Test) info as seen by some common utilities. For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
  
  - model name: Intel(R) Xeon(R) Gold 6230 CPU @ 2.10GHz
  - 2 "physical id"s (chips)
  - 80 "processors"
  - cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  - cpu cores: 20
  - siblings: 40
  - physical 0: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28
  - physical 1: cores 0 1 2 3 4 8 9 10 11 12 16 17 18 19 20 24 25 26 27 28

From lscpu:
  
  - Architecture: x86_64
  - CPU op-mode(s): 32-bit, 64-bit
  - Byte Order: Little Endian
  - CPU(s): 80

(Continued on next page)
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Platform Notes (Continued)

On-line CPU(s) list: 0-79
Thread(s) per core: 2
Core(s) per socket: 20
Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6230 CPU @ 2.10GHz
Stepping: 6
CPU MHz: 1993.565
BogoMIPS: 4200.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 28160K
NUMA node0 CPU(s): 0,4,8,12,16,20,24,28,32,36,40,44,48,52,56,60,64,68,72,76
NUMA node1 CPU(s): 1,5,9,13,17,21,25,29,33,37,41,45,49,53,57,61,65,69,73,77
NUMA node2 CPU(s): 2,6,10,14,18,22,26,30,34,38,42,46,50,54,58,62,66,70,74,78
NUMA node3 CPU(s): 3,7,11,15,19,23,27,31,35,39,43,47,51,55,59,63,67,71,75,79
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3 invpcid_single intel_pinn ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mxp rdt_a avx512f avx512dq rdseed adx clflushopt clwb intel_pt avx512cd avx512bw avx512vl xsaveopt xsaves xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbb_total cqm_mbb_local dtherm ida arat pln pts pku ospke avx512_vnni flush_l1d arch_capabilities

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

node 0 cpus: 0 4 8 12 16 20 24 28 32 36 40 44 48 52 56 60 64 68 72 76
node 0 size: 95123 MB
node 0 free: 94750 MB
node 1 cpus: 1 5 9 13 17 21 25 29 33 37 41 45 49 53 57 61 65 69 73 77
node 1 size: 96764 MB
node 1 free: 96448 MB
node 2 cpus: 2 6 10 14 18 22 26 30 34 38 42 46 50 54 58 62 66 70 74 78
node 2 size: 96764 MB

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Platform Notes (Continued)

node 2 free: 96545 MB  
node 3 cpus: 3 7 11 15 19 23 27 31 35 39 43 47 51 55 59 63 67 71 75 79  
node 3 size: 96762 MB  
node 3 free: 96531 MB  
node distances:

node  0   1   2   3
0:  10  21  11  21
1:  21  10  21  11
2:  11  21  10  21
3:  21  11  21  10

From /proc/meminfo

MemTotal:       394664916 kB  
HugePages_Total:       0  
Hugepagesize:       2048 kB

From /usr/bin/lsb_release -d

Ubuntu 18.04.2 LTS

debian_version: buster/sid

uname -a:

Linux intel-sut 4.15.0-45-generic #48-Ubuntu SMP Tue Jan 29 16:28:13 UTC 2019 x86_64  
x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2017-5754 (Meltdown): Not affected  
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization  
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB

run-level 3 Jul 5 16:22

SPEC is set to: /home/cpu2017

Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 ext4 439G 31G 386G 8% /

(Continued on next page)
SPEC CPU2017 Integer Rate Result

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Platform Notes (Continued)

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 2.3.1 05/02/2019
Memory:
   6x 00AD00B300AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933
   6x 00AD069D00AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933
   4x Not Specified Not Specified

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
CC  502.gcc_r(peak)
Intel(R) C Intel(R) 64 Compiler for applications running on IA-32, Version
19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
==============================================================================
CC  500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
   525.x264_r(base, peak) 557.xz_r(base, peak)
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
==============================================================================
CC  500.perlbench_r(peak)
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
==============================================================================
CXXC 523.xalancbmk_r(peak)
Intel(R) C++ Intel(R) 64 Compiler for applications running on IA-32, Version
19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

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---

**Compiler Version Notes (Continued)**

```plaintext
CXXC 520.omnetpp_r(base, peak) 523.xalancbmk_r(base) 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

FC 548.exchange2_r(base, peak)

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
```

---

**Base Compiler Invocation**

C benchmarks:
```plaintext
icc -m64 -std=c11
```

C++ benchmarks:
```plaintext
icpc -m64
```

Fortran benchmarks:
```plaintext
ifort -m64
```

---

**Base Portability Flags**

```plaintext
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
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Base Optimization Flags

C benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

C++ benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

Fortran benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64 -std=c11
502.gcc_r icc -m32 -std=c11 -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/ia32_lin

C++ benchmarks (except as noted below):
icpc -m64
523.xalancbmk_r icpc -m32 -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/ia32_lin

Fortran benchmarks:
ifort -m64

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64

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Peak Portability Flags (Continued)

531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:

500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4
-fno-strict-overflow
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

502.gcc_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4
-L/usr/local/je5.0.1-32/lib -ljemalloc

505.mcf_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

525.x264_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-alias
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

557.xz_r: Same as 505.mcf_r

C++ benchmarks:

520.omnetpp_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

523.xalancbmk_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4
-L/usr/local/je5.0.1-32/lib -ljemalloc

531.deepsjeng_r: Same as 520.omnetpp_r

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Tested by: Dell Inc.  Software Availability: May-2019

Peak Optimization Flags (Continued)

541.leela_r: Same as 520.omnetpp_r

Fortran benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.5 on 2019-07-05 12:29:10-0400.
Originally published on 2019-07-23.