Lenovo Global Technology
ThinkSystem SR550
(2.50 GHz, Intel Xeon Gold 5215)

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: May-2019
Hardware Availability: Apr-2019
Software Availability: Dec-2018

SPECrate2017_fp_base = 124
SPECrate2017_fp_peak = Not Run

### Hardware
- CPU Name: Intel Xeon Gold 5215
- Max MHz.: 3400
- Nominal: 2500
- Enabled: 20 cores, 2 chips, 2 threads/core
- Orderable: 1.2 chips
- Cache L1: 32 KB I + 32 KB D on chip per core
- Cache L2: 1 MB I+D on chip per core
- Cache L3: 13.75 MB I+D on chip per chip
- Other: None
- Memory: 384 GB (12 x 32 GB 2Rx4 PC4-2666V-R)
- Storage: 1 x 960 GB SATA SSD
- Other: None

### Software
- OS: SUSE Linux Enterprise Server 12 SP4 (x86_64)
- Compiler: C/C++: Version 19.0.1.144 of Intel C/C++
- Fortran: Version 19.0.1.144 of Intel Fortran
- Compiler Build 20181018 for Linux;
- Compiler Build 20181018 for Linux
- Parallel: No
- Firmware: Lenovo BIOS Version TEE142E 2.30 released Aug-2019 tested as TEE135L 2.10 Jan-2019
- File System: xfs
- System State: Run level 3 (multi-user)
- Base Pointers: 64-bit
- Peak Pointers: Not Applicable
- Other: None

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<table>
<thead>
<tr>
<th>Software Availability</th>
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</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td>May-2019</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Apr-2019</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Dec-2018</td>
</tr>
</tbody>
</table>

---

**Copies**

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>SPECrate2017_fp_base</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>40</td>
<td>89.8</td>
</tr>
<tr>
<td>507.cacluBSSN_r</td>
<td>40</td>
<td>87.3</td>
</tr>
<tr>
<td>508.namd_r</td>
<td>40</td>
<td>72.0</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>40</td>
<td>127</td>
</tr>
<tr>
<td>511.povray_r</td>
<td>40</td>
<td>85.8</td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>40</td>
<td>144</td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>40</td>
<td>114</td>
</tr>
<tr>
<td>526.blender_r</td>
<td>40</td>
<td>127</td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>40</td>
<td>261</td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>40</td>
<td>118</td>
</tr>
<tr>
<td>544.nab_r</td>
<td>40</td>
<td>65.0</td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>40</td>
<td>191</td>
</tr>
<tr>
<td>554.roms_r</td>
<td>40</td>
<td>191</td>
</tr>
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---

**SPECrate2017_fp_base (124)**
## Lenovo Global Technology

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**Results Table**

<table>
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<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>40</td>
<td>1163</td>
<td>345</td>
<td><strong>1163</strong></td>
<td><strong>345</strong></td>
<td>1160</td>
<td>346</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>40</td>
<td>565</td>
<td>89.6</td>
<td>564</td>
<td>89.8</td>
<td><strong>564</strong></td>
<td><strong>89.8</strong></td>
</tr>
<tr>
<td>508.namd_r</td>
<td>40</td>
<td>435</td>
<td><strong>87.3</strong></td>
<td>432</td>
<td>88.0</td>
<td>439</td>
<td>86.5</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>40</td>
<td>1450</td>
<td>72.2</td>
<td>1455</td>
<td>71.9</td>
<td><strong>1454</strong></td>
<td><strong>72.0</strong></td>
</tr>
<tr>
<td>511.povray_r</td>
<td>40</td>
<td>737</td>
<td>127</td>
<td>738</td>
<td>127</td>
<td><strong>737</strong></td>
<td><strong>127</strong></td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>40</td>
<td>488</td>
<td>86.5</td>
<td>492</td>
<td>85.7</td>
<td><strong>491</strong></td>
<td><strong>85.8</strong></td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>40</td>
<td>628</td>
<td>143</td>
<td><strong>634</strong></td>
<td><strong>141</strong></td>
<td>635</td>
<td>141</td>
</tr>
<tr>
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<td>40</td>
<td>535</td>
<td>114</td>
<td>535</td>
<td>114</td>
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<td>40</td>
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<td>127</td>
<td><strong>550</strong></td>
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<td>127</td>
</tr>
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<td>40</td>
<td>381</td>
<td>261</td>
<td>380</td>
<td>262</td>
<td><strong>381</strong></td>
<td><strong>261</strong></td>
</tr>
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<td>40</td>
<td>348</td>
<td>193</td>
<td><strong>353</strong></td>
<td><strong>191</strong></td>
<td>353</td>
<td>191</td>
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<td>40</td>
<td>1316</td>
<td>118</td>
<td>1319</td>
<td>118</td>
<td>1313</td>
<td>119</td>
</tr>
<tr>
<td>554.roms_r</td>
<td>40</td>
<td>979</td>
<td>64.9</td>
<td><strong>978</strong></td>
<td><strong>65.0</strong></td>
<td>974</td>
<td>65.2</td>
</tr>
</tbody>
</table>

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**Submit Notes**

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

---

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"

---

**General Notes**

Environment variables set by runcpu before the start of the run:

LD_LIBRARY_PATH = ":/home/cpu2017-1.0.5-ic19.0u1/lib/intel64"

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.5  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:

```bash
sync; echo 3>/proc/sys/vm/drop_caches
```

runcpu command invoked through numactl i.e.:

```bash
numactl --interleave=all runcpu &etc
```

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

(Continued on next page)
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Tested by: Lenovo Global Technology

Test Date: May-2019
Hardware Availability: Apr-2019
Software Availability: Dec-2018

General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3640 (Spectre variant 3a) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3639 (Spectre variant 4) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance
Choose Operating Mode set to Custom Mode
MONITOR/MWAIT set to Enable
LLC dead line alloc set to Disable
Sysinfo program /home/cpu2017-1.0.5-ic19.0u1/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on linux-h2e9 Tue May 7 17:19:26 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 5215 CPU @ 2.50GHz
  2 "physical id"s (chips)
  40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 10
siblings : 20
physical 0: cores 0 1 2 3 4 8 9 10 11 12
physical 1: cores 0 1 2 3 4 8 9 10 11 12

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 40
On-line CPU(s) list: 0-39
Thread(s) per core: 2
Core(s) per socket: 10
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel

(Continued on next page)
### Lenovo Global Technology

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#### Platform Notes (Continued)

- **CPU family:** 6  
- **Model:** 85  
- **Model name:** Intel(R) Xeon(R) Gold 5215 CPU @ 2.50GHz  
- **Stepping:** 6  
- **CPU MHz:** 2500.000  
- **CPU max MHz:** 3400.0000  
- **CPU min MHz:** 1000.0000  
- **BogoMIPS:** 5000.00  
- **Virtualization:** VT-x  
- **L1d cache:** 32K  
- **L1i cache:** 32K  
- **L2 cache:** 1024K  
- **L3 cache:** 14080K  
- **NUMA node0 CPU(s):** 0-9,20-29  
- **NUMA node1 CPU(s):** 10-19,30-39  
- **Flags:** fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts  
  - tsc acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abcm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3 invpcid_single ssbd mba ibrs ibpb stibp tpr_shadow vmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl xsaveopt xsaves xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local dtherm ida arat pln pts pku ospke avx512_vnni flush_l1d arch_capabilities

- **/proc/cpuinfo cache data**  
  - cache size : 14080 KB

- From `numactl --hardware`  
  - WARNING: a numactl 'node' might or might not correspond to a physical chip.
  - available: 2 nodes (0-1)
  - node 0 cpus: 0 1 2 3 4 5 6 7 8 9 20 21 22 23 24 25 26 27 28 29
  - node 0 size: 193095 MB
  - node 0 free: 192598 MB
  - node 1 cpus: 10 11 12 13 14 15 16 17 18 19 30 31 32 33 34 35 36 37 38 39
  - node 1 size: 193508 MB
  - node 1 free: 192977 MB
  - node distances:
    - node 0 1
    - 0: 10 21
    - 1: 21 10

- From `/proc/meminfo`  
  - MemTotal: 395882912 KB
  - HugePages_Total: 0

(Continued on next page)
Platform Notes (Continued)

Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
  VERSION = 12
  PATCHLEVEL = 4
  # This file is deprecated and will be removed in a future service pack or release.
  # Please check /etc/os-release for details about this release.
os-release:
  NAME="SLES"
  VERSION="12-SP4"
  VERSION_ID="12.4"
  PRETTY_NAME="SUSE Linux Enterprise Server 12 SP4"
  ID="sles"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:12:sp4"

uname -a:
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2017-5754 (Meltdown): Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Indirect Branch Restricted Speculation, IBPB, IBRS_PW

run-level 3 May 7 17:17

SPEC is set to: /home/cpu2017-1.0.5-ic19.0u1
  Filesystem Type Size Used Avail Use% Mounted on
/dev/sda3 xfs 892G 31G 861G 4% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
  BIOS Lenovo -[TEE135L-2.10]- 01/10/2019
  Memory:
    12x SK Hynix HMA84GR7AFR4N-VK 32 GB 2 rank 2666

(End of data from sysinfo program)
**SPEC CPU2017 Floating Point Rate Result**

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</tr>
</tbody>
</table>

**SPECrate2017_fp_base = 124**  
**SPECrate2017_fp_peak = Not Run**

**Compiler Version Notes**

```
==============================================================================
CC  519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)
------------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
CXXC 508.namd_r(base) 510.parest_r(base)
------------------------------------------------------------------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
CC  511.povray_r(base) 526.blender_r(base)
------------------------------------------------------------------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

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FC  507.cactuBSSN_r(base)
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Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,  
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Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
FC  503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)
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Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
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```

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### Compiler Version Notes (Continued)

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Intel (R) Fortran Intel (R) 64 Compiler for applications running on Intel (R) 64, Version 19.0.1.144 Build 20181018  
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---

**Base Compiler Invocation**

---

C benchmarks:  
```
icc -m64 -std=c11
```  
C++ benchmarks:  
```
icpc -m64
```  
Fortran benchmarks:  
```
ifort -m64
```  
Benchmarks using both Fortran and C:  
```
ifort -m64 icc -m64 -std=c11
```  
Benchmarks using both C and C++:  
```
icpc -m64 icc -m64 -std=c11
```  
Benchmarks using Fortran, C, and C++:  
```
icpc -m64 icc -m64 -std=c11 ifort -m64
```  
---

**Base Portability Flags**

---

503.bwaves_r: -DSPEC_LP64  
507.cactuBSSN_r: -DSPEC_LP64  
508.namd_r: -DSPEC_LP64  
510.parest_r: -DSPEC_LP64  
511.povray_r: -DSPEC_LP64  
519.lbm_r: -DSPEC_LP64  
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian  
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funssigned-char

(Continued on next page)
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### Base Portability Flags (Continued)

- 527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
- 538.imagick_r: -DSPEC_LP64
- 544.nab_r: -DSPEC_LP64
- 549.fotonik3d_r: -DSPEC_LP64
- 554.roms_r: -DSPEC_LP64

### Base Optimization Flags

**C benchmarks**:  
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=4

**C++ benchmarks**:  
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=4

**Fortran benchmarks**:  
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=4 -auto  
-nostandard-realloc-lhs -align array32byte

**Benchmarks using both Fortran and C**:  
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=4 -auto  
-nostandard-realloc-lhs -align array32byte

**Benchmarks using both C and C++**:  
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=4

**Benchmarks using Fortran, C, and C++**:  
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=4 -auto  
-nostandard-realloc-lhs -align array32byte

The flags files that were used to format this result can be browsed at  
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-D.html

You can also download the XML flags sources by saving the following links:  
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-D.xml
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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.5 on 2019-05-07 05:19:25-0400.
Report generated on 2019-08-08 14:58:58 by CPU2017 PDF formatter v6067.
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