SPEC CPU®2017 Integer Rate Result

ASUSTeK Computer Inc.
ASUS ESC8000-G4(Z11PG-D24) Server System (2.20 GHz, Intel Xeon Silver 4214)

SPECrate®2017_int_base = 142
SPECrate®2017_int_peak = 147

CPU2017 License: 9016
Test Sponsor: ASUSTeK Computer Inc.
Tested by: ASUSTeK Computer Inc.
Test Date: Jul-2019
Hardware Availability: Apr-2019
Software Availability: May-2019

ASUSTeK Computer Inc.
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CPU Name: Intel Xeon Silver 4214
Max MHz: 3200
Nominal: 2200
Enabled: 24 cores, 2 chips, 2 threads/core
Orderable: 1.2 chips
Cache L1: 32 KB I + 32 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 16.5 MB I+D on chip per chip
Other: None
Memory: 768 GB (24 x 32 GB 2Rx4 PC4-2933Y-R, running at 2400)
Storage: 1 x 1 TB SATA SSD
Other: None

OS: SUSE Linux Enterprise Server 15
Kernel 4.12.14-23-default
Compiler: C/C++: Version 19.0.4.227 of Intel C/C++ Compiler Build 20190416 for Linux;
Fortran: Version 19.0.4.227 of Intel Fortran Compiler Build 20190416 for Linux
Parallel: No
Firmware: Version 5102 released Feb-2019
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other: jemalloc: jemalloc memory allocator library V5.0.1
Power Management: --
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Results Table

Benchmark | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
--- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
500.perlbench_r | 48 | 728 | 105 | 734 | 104 | 726 | 105 | 48 | 632 | 121 | 632 | 121 | 633 | 121 |
502.gcc_r | 48 | 564 | 121 | 565 | 120 | 561 | 121 | 48 | 511 | 133 | 510 | 133 | 509 | 133 |
505.mcf_r | 48 | 400 | 194 | 397 | 195 | 400 | 194 | 48 | 400 | 194 | 399 | 195 | 400 | 194 |
520.omnetpp_r | 48 | 626 | 101 | 626 | 101 | 626 | 101 | 48 | 626 | 101 | 625 | 101 | 625 | 101 |
523.xalancbmk_r | 48 | 301 | 169 | 301 | 169 | 301 | 169 | 48 | 284 | 178 | 285 | 178 | 285 | 178 |
525.x264_r | 48 | 315 | 267 | 316 | 266 | 316 | 266 | 48 | 301 | 279 | 301 | 279 | 301 | 279 |
531.deepsjeng_r | 48 | 487 | 113 | 486 | 113 | 486 | 113 | 48 | 486 | 113 | 486 | 113 | 487 | 113 |
541.leea_r | 48 | 754 | 105 | 761 | 104 | 773 | 103 | 48 | 754 | 105 | 763 | 104 | 774 | 103 |
548.exchange2_r | 48 | 550 | 94.3 | 48 | 550 | 94.3 | 48 | 550 | 94.3 | 48 | 550 | 94.3 | 48 | 550 | 94.3 |
557.xz_r | 48 | 550 | 94.3 | 548 | 94.6 | 548 | 94.6 | 48 | 550 | 94.2 | 547 | 94.7 | 547 | 94.8 |

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/spec2017_19u4/lib/intel64:/spec2017_19u4/lib/ia32:
/spec2017_19u4/je5.0.1-32"

Binaries compiled on a system with 1x Intel Core i9-799X CPU + 32GB RAM
Memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with: sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.: numactl --interleave=all runcpu <etc>
jemalloc: configured and built at default for 32bit (i686) and 64bit (x86_64) targets;
jemalloc: built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5;

(Continued on next page)
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General Notes (Continued)
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS Configuration:
VT-d = Disabled
Patrol Scrub = Disabled
ENERGY_PERF_BIAS_CFG mode = performance
SNC = Enabled
IMC interleaving = 1-way
Engine Boost = Level3(Max)
LLC dead line alloc = Disabled
SR-IOV Support = Disabled
CSM Support = Disabled
Sysinfo program /spec2017_19u4/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on linux-gh78 Sat Jul 27 10:31:17 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Silver 4214 CPU @ 2.20GHz
  2 "physical id"s (chips)
  48 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 12
siblings : 24
physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 48
On-line CPU(s) list: 0-47
Thread(s) per core: 2
Core(s) per socket: 12

(Continued on next page)
SPEC CPU®2017 Integer Rate Result

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Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Silver 4214 CPU @ 2.20GHz
Stepping: 6
CPU MHz: 2200.000
CPU max MHz: 3200.0000
CPU min MHz: 1000.0000
BogoMIPS: 4400.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 16896K
NUMA node0 CPU(s): 0-2, 6-8, 24-26, 30-32
NUMA node1 CPU(s): 3-5, 9-11, 27-29, 33-35
NUMA node2 CPU(s): 12-14, 18-20, 36-38, 42-44
NUMA node3 CPU(s): 15-17, 21-23, 39-41, 45-47
Flags: fpu vme de pse tsc msr pae mce cmovpat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperf perfctr tsc_known_freq pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer_timer肟 tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mp xdt_a avx512f avx512dq rdseed adx snap clflushopt clwb intel_pt avx512cd avx512bw avx512vl xsaveopt xsaves xgetbv1 xsaves cqm l1c cqm_occ 11c cqm_mbm total cqm_mbm local ibpb ibrs stibp dtherm ida arat pln pts hwp act_window hwp_epp hwp_pkg req pku ospke avx512_vnni arch capabilities ssbd

/platform/cpulinfo cache data
 cache size: 16896 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
 available: 4 nodes (0-3)
 node 0 cpus: 0 1 2 6 7 8 24 25 26 30 31 32
 node 0 size: 192034 MB
 node 0 free: 191326 MB
 node 1 cpus: 3 4 5 9 10 11 27 28 29 33 34 35
 node 1 size: 193512 MB
 node 1 free: 192811 MB
 node 2 cpus: 12 13 14 18 19 20 36 37 38 42 43 44
 node 2 size: 193512 MB

(Continued on next page)
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Platform Notes (Continued)

node 2 free: 192793 MB
node 3 cpus: 15 16 17 21 22 23 39 40 41 45 46 47
node 3 size: 193508 MB
node 3 free: 192815 MB
node distances:
node 0 1 2 3
0: 10 11 21 21
1: 11 10 21 21
2: 21 21 10 11
3: 21 21 11 10

From /proc/meminfo
MemTotal: 791110448 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
   os-release:
   NAME="SLES"
   VERSION="15"
   VERSION_ID="15"
   PRETTY_NAME="SUSE Linux Enterprise Server 15"
   ID="sles"
   ID_LIKE="suse"
   ANSI_COLOR="0;32"
   CPE_NAME="cpe:/o:suse:sles:15"

uname -a:
Linux linux-gh78 4.12.14-23-default #1 SMP Tue May 29 21:04:44 UTC 2018 (cd0437b)
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2017-5754 (Meltdown): Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Indirect Branch Restricted Speculation, IBPB, IBRS_FW

run-level 3 Jul 26 11:12

SPEC is set to: /spec2017_19u4
   Filesystem Type Size Used Avail Use% Mounted on
   /dev/sda4 xfs 929G 15G 914G 2% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are
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Platform Notes (Continued)

frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
  BIOS American Megatrends Inc. 5102 02/11/2019
  Memory:
  24x Samsung M393A4K40CB2-CVF 32 GB 2 rank 2933, configured at 2400

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C       | 502.gcc_r(peak)
Intel(R) C Intel(R) 64 Compiler for applications running on IA-32, Version
  19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

C       | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak)
  525.x264_r(base, peak) 557.xz_r(base, peak)
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.4.227 Build 20190416
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C       | 502.gcc_r(peak)
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C       | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak)
  525.x264_r(base, peak) 557.xz_r(base, peak)
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

C++     | 523.xalancbmk_r(peak)

(Continued on next page)
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Compiler Version Notes (Continued)

Intel(R) C++ Intel(R) 64 Compiler for applications running on IA-32, Version
19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

==============================================================================
C++ | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base)
    | 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)
------------------------------------------------------------------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

==============================================================================
C++ | 523.xalancbmk_r(peak)
------------------------------------------------------------------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on IA-32, Version
19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

==============================================================================
C++ | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base)
    | 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)
------------------------------------------------------------------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

==============================================================================
Fortran | 548.exchange2_r(base, peak)
------------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

(Continued on next page)
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Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort -m64

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

C++ benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

Fortran benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc
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Peak Compiler Invocation
C benchmarks (except as noted below):
  icc -m64 -std=c11
502.gcc_r: icc -m32 -std=c11 -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/ia32

C++ benchmarks (except as noted below):
  icpc -m64
523.xalancbmk_r: icpc -m32 -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/ia32

Fortran benchmarks:
  ifort -m64

Peak Portability Flags
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalanchbmk_r: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags
C benchmarks:
  500.perlbench_r: -Wl, -z, muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
  -xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4
  -fno-strict-overflow
  -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
  -lqkmalloc
  502.gcc_r: -Wl, -z, muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
  -xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4
  -L/usr/local/jemalloc

(Continued on next page)
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### Peak Optimization Flags (Continued)

505.mcf_r (continued):
- `-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64`  
- `-lqkmalloc`

525.x264_r: `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`  
- `-qopt-mem-layout-trans=4`  
- `-fno-alias`  
- `-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64`  
- `-lqkmalloc`

557.xz_r: Same as 505.mcf_r

C++ benchmarks:

520.omnetpp_r: `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`  
- `-qopt-mem-layout-trans=4`  
- `-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64`  
- `-lqkmalloc`

523.xalancbmk_r: `-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo`  
- `-xCORE-AVX512`  
- `-O3`  
- `-no-prec-div`  
- `-qopt-mem-layout-trans=4`  
- `-L/usr/local/je5.0.1-32/lib`  
- `-ljemalloc`

531.deepsjeng_r: Same as 520.omnetpp_r

541.leela_r: Same as 520.omnetpp_r

Fortran benchmarks:

- `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`  
- `-qopt-mem-layout-trans=4`  
- `-nostandard-realloc-lhs`  
- `-align array32byte`  
- `-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64`  
- `-lqkmalloc`

The flags files that were used to format this result can be browsed at:


You can also download the XML flags sources by saving the following links:

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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