Lenovo Global Technology
ThinkSystem SR650
(2.30 GHz, Intel Xeon Gold 6252N)

SPECrater®2017_fp_base = 242
SPECrater®2017_fp_peak = Not Run

<table>
<thead>
<tr>
<th>Copies</th>
<th>SPECrate®2017_fp_base (242)</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>96</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>96</td>
</tr>
<tr>
<td>508.namd_r</td>
<td>96</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>96</td>
</tr>
<tr>
<td>511.povray_r</td>
<td>96</td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>96</td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>96</td>
</tr>
<tr>
<td>526.blender_r</td>
<td>96</td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>96</td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>96</td>
</tr>
<tr>
<td>544.nab_r</td>
<td>96</td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>96</td>
</tr>
<tr>
<td>554.roms_r</td>
<td>96</td>
</tr>
</tbody>
</table>

Hardware
- CPU Name: Intel Xeon Gold 6252N
- Max MHz: 3600
- Nominal: 2300
- Enabled: 48 cores, 2 chips, 2 threads/core
- Orderable: 1,2 chips
- Cache L1: 32 KB I + 32 KB D on chip per core
- L2: 1 MB I+D on chip per core
- L3: 35.75 MB I+D on chip per chip
- Other: None
- Memory: 768 GB (24 x 32 GB 2Rx4 PC4-2933Y-R)
- Storage: 1 x 800 GB SATA SSD
- Other: None

Software
- OS: Red Hat Enterprise Linux Server release 7.6 (Maipo)
- Kernel 3.10.0-957.el7.x86_64
- Compiler: C/C++: Version 19.0.4.227 of Intel
- C/C++ Compiler for Linux;
- Fortran: Version 19.0.4.227 of Intel Fortran
- Compiler for Linux
- Parallel: No
- Firmware: Lenovo BIOS Version IVE142E 2.30 released Aug-2019 tested as IVE141E 2.30 Jul-2019
- File System: xfs
- System State: Run level 3 (multi-user)
- Base Pointers: 64-bit
- Peak Pointers: Not Applicable
- Other: None
- Power Management: --
## Lenovo Global Technology

**ThinkSystem SR650**  
(2.30 GHz, Intel Xeon Gold 6252N)

**SPEC CPU®2017 Floating Point Rate Result**  
Copyright 2017-2019 Standard Performance Evaluation Corporation

### CPU2017 License: 9017  
Test Sponsors: Lenovo Global Technology  
Tested by: Lenovo Global Technology

### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>96</td>
<td>1805</td>
<td>533</td>
<td>1803</td>
<td>534</td>
<td>1803</td>
<td>534</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>96</td>
<td>580</td>
<td>210</td>
<td>580</td>
<td>210</td>
<td>580</td>
<td>209</td>
</tr>
<tr>
<td>508.namd_r</td>
<td>96</td>
<td>488</td>
<td>187</td>
<td>489</td>
<td>186</td>
<td>489</td>
<td>186</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>96</td>
<td>1905</td>
<td>132</td>
<td>1882</td>
<td>133</td>
<td>1890</td>
<td>133</td>
</tr>
<tr>
<td>511.povray_r</td>
<td>96</td>
<td>783</td>
<td>286</td>
<td>783</td>
<td>286</td>
<td>784</td>
<td>286</td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>96</td>
<td>824</td>
<td>123</td>
<td>824</td>
<td>123</td>
<td>825</td>
<td>123</td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>96</td>
<td>931</td>
<td>231</td>
<td>956</td>
<td>225</td>
<td>921</td>
<td>234</td>
</tr>
<tr>
<td>526.blender_r</td>
<td>96</td>
<td>514</td>
<td>285</td>
<td>512</td>
<td>285</td>
<td>514</td>
<td>284</td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>96</td>
<td>573</td>
<td>293</td>
<td>571</td>
<td>294</td>
<td>572</td>
<td>293</td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>96</td>
<td>379</td>
<td>630</td>
<td>379</td>
<td>629</td>
<td>379</td>
<td>630</td>
</tr>
<tr>
<td>544.nab_r</td>
<td>96</td>
<td>356</td>
<td>453</td>
<td>357</td>
<td>452</td>
<td>356</td>
<td>454</td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>96</td>
<td>2155</td>
<td>174</td>
<td>2157</td>
<td>173</td>
<td>2159</td>
<td>173</td>
</tr>
<tr>
<td>554.roms_r</td>
<td>96</td>
<td>1481</td>
<td>103</td>
<td>1478</td>
<td>103</td>
<td>1472</td>
<td>104</td>
</tr>
</tbody>
</table>

**SPECrate®2017_fp_base = 242**  
**SPECrate®2017_fp_peak = Not Run**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017-1.0.5-ic19.0u4/lib/intel64"

Binaries compiled on a system with 1x Intel Core i9-799X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Files system page cache synced and cleared with:
sync; echo 3>/proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)

(Continued on next page)
General Notes (Continued)

is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3640 (Spectre variant 3a) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3639 (Spectre variant 4) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance
Choose Operating Mode set to Custom Mode
C-States set to Legacy
SNC set to Enable
DCU Streamer Prefetcher set to Disable
Trusted Execution Technology set to Enable
Stale AtoS set to Enable
LLC dead line alloc set to Disable
Patrol Scrub set to Disable
Sysinfo program /home/cpu2017-1.0.5-ic19.0u4/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on localhost.localdomain Fri Sep 13 04:57:20 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6252N CPU @ 2.30GHz
  2 "physical id"'s (chips)
  96 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 24
siblings : 48
physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 25 26 27 28 29
physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 25 26 27 28 29

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 96
Platform Notes (Continued)

On-line CPU(s) list: 0-95
Thread(s) per core: 2
Core(s) per socket: 24
Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6252N CPU @ 2.30GHz
Stepping: 7
CPU MHz: 2300.000
BogoMIPS: 4600.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 36608K
NUMA node0 CPU(s): 0-3,7-9,13-15,19,20,48-51,55-57,61-63,67,68
NUMA node1 CPU(s): 4-6,10-12,16-18,21-23,52-54,58-60,64-66,69-71
NUMA node2 CPU(s): 24-27,31-33,37-39,43,44,72-75,79-81,85-87,91,92
NUMA node3 CPU(s): 28-30,34-36,40-42,45-47,76-78,82-84,88-90,93-95
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtsscp
lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmprefl eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
fma cx16 xtop pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3nowprefetch ebp cat _13 cdp _13 intel_pppin
intel_pt ssbd mba ibrs ibbp stibp ibrs_enhanced tpr_shadow vmmi flexpriority ept
vpid fsgsbase tsc_adjust bml1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a
avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt
xsavec xgetbv1 cmp lcc cmp_lcm cmp_mmcm_total cmp_mmcm_local dtherm ida arat pln
pts pku ospke avx512_vnni spec_ctrl intel_stibp flush_lld arch_capabilities

/proc/cpuinfo cache data
  cache size: 36608 KB

From numacl --hardware WARNING: a numacl 'node' might or might not correspond to a
physical chip.
  available: 4 nodes (0-3)
  node 0 cpus: 0 1 2 3 7 8 9 13 14 15 19 20 48 49 50 51 55 56 57 61 62 63 67 68
  node 0 size: 196280 MB
  node 0 free: 191373 MB
  node 1 cpus: 4 5 6 10 11 12 16 17 18 21 22 23 52 53 54 58 59 60 64 65 66 69 70 71
  node 1 size: 196608 MB
  node 1 free: 191452 MB
  node 2 cpus: 24 25 26 27 31 32 33 37 38 39 43 44 47 72 73 74 75 79 80 81 85 86 87 91 92
  node 2 size: 196608 MB

(Continued on next page)

Platform Notes (Continued)

node 2 free: 191940 MB
node 3 cpus: 28 29 30 34 35 40 41 42 45 46 47 76 77 78 82 83 84 88 89 90 93 94 95
node 3 size: 196608 MB
node 3 free: 191901 MB
node distances:
node 0 1 2 3
0:  10  11  21  21
1:  11  10  21  21
2:  21  21  10  11
3:  21  21  11  10

From /proc/meminfo
MemTotal:       792239644 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
  os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.6 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VARIANT="Server"
    VARIANT_ID="server"
    VERSION_ID="7.6"
    PRETTY_NAME="Red Hat Enterprise Linux Server 7.6 (Maipo)"
  redhat-release: Red Hat Enterprise Linux Server release 7.6 (Maipo)
  system-release: Red Hat Enterprise Linux Server release 7.6 (Maipo)

uname -a:
    Linux localhost.localdomain 3.10.0-957.el7.x86_64 #1 SMP Thu Oct 4 20:48:51 UTC 2018
    x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2017-5754 (Meltdown): Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: Load fences, __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS

run-level 3 Sep 12 18:04

SPEC is set to: /home/cpu2017-1.0.5-ic19.0u4
  Filesystem   Type  Size  Used Avail Use% Mounted on
  /dev/sdb2    xfs   689G   36G   653G   6%  /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret
Lenovo Global Technology
ThinkSystem SR650
(2.30 GHz, Intel Xeon Gold 6252N)

SPECrates®

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Sep-2019
Hardware Availability: Jul-2019
Software Availability: May-2019

SPECrates®

SPECrate®2017_fp_base = 242
SPECrate®2017_fp_peak = Not Run

Platform Notes (Continued)
this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
BIOS Lenovo -[IVE141E-2.30]– 07/02/2019
Memory:
24x Samsung M393A4K40CB2-CVF 32 GB 2 rank 2933

(End of data from sysinfo program)

Compiler Version Notes
==============================================================================
C               | 519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
==============================================================================

C++             | 508.namd_r(base) 510.parest_r(base)
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
==============================================================================

C++, C          | 511.povray_r(base) 526.blender_r(base)
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

(Continued on next page)
**Lenovo Global Technology**

ThinkSystem SR650  
(2.30 GHz, Intel Xeon Gold 6252N)  

<table>
<thead>
<tr>
<th>SPECrate®2017_fp_base = 242</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate®2017_fp_peak = Not Run</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology  

<table>
<thead>
<tr>
<th>Test Date: Sep-2019</th>
</tr>
</thead>
<tbody>
<tr>
<td>Hardware Availability: Jul-2019</td>
</tr>
<tr>
<td>Software Availability: May-2019</td>
</tr>
</tbody>
</table>

---

**Compiler Version Notes (Continued)**

Copyright (C) 1985-2019 Intel Corporation. All rights reserved.  
Intel (R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
64, Version 19.0.4.227 Build 20190416  
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

---

**Base Compiler Invocation**

C benchmarks:  
```  
icc -m64 -std=c11  
```

C++ benchmarks:  
```  
icpc -m64  
```

Fortran benchmarks:  
```  
ifort -m64  
```

Benchmarks using both Fortran and C:  
```  
ifort -m64 icc -m64 -std=c11  
```

Benchmarks using both C and C++:  
```  
icpc -m64 icc -m64 -std=c11  
```

Benchmarks using Fortran, C, and C++:  
```  
icpc -m64 icc -m64 -std=c11 ifort -m64  
```
Lenovo Global Technology
ThinkSystem SR650
(2.30 GHz, Intel Xeon Gold 6252N)

SPECrates\textsuperscript{2017}_fp_base = 242
SPECrates\textsuperscript{2017}_fp_peak = Not Run

CPU\textsuperscript{2017} License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Base Portability Flags

503.bwaves\textsubscript{r}: -DSPEC\_LP64
507.cactuBSSN\textsubscript{r}: -DSPEC\_LP64
508.namd\textsubscript{r}: -DSPEC\_LP64
510.parest\textsubscript{r}: -DSPEC\_LP64
511.povray\textsubscript{r}: -DSPEC\_LP64
519.lbm\textsubscript{r}: -DSPEC\_LP64
521.wrf\textsubscript{r}: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG -convert big_endian
526.blender\textsubscript{r}: -DSPEC\_LP64 -DSPEC\_LINUX -funsigned-char
527.cam4\textsubscript{r}: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG
538.imagick\textsubscript{r}: -DSPEC\_LP64
544.nab\textsubscript{r}: -DSPEC\_LP64
549.fotonik3d\textsubscript{r}: -DSPEC\_LP64
554.roms\textsubscript{r}: -DSPEC\_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout=trans=4

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout=trans=4

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout=trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout=trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Benchmarks using both C and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout=trans=4

Benchmarks using Fortran, C, and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout=trans=4 -auto -nostandard-realloc-lhs
-align array32byte
# Lenovo Global Technology

## ThinkSystem SR650
(2.30 GHz, Intel Xeon Gold 6252N)

<table>
<thead>
<tr>
<th>SPECrate®2017_fp_base =</th>
<th>242</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate®2017_fp_peak =</td>
<td>Not Run</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology  
**Test Date:** Sep-2019  
**Hardware Availability:** Jul-2019  
**Software Availability:** May-2019

The flags files that were used to format this result can be browsed at:
- [http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-D.html](http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-D.html)

You can also download the XML flags sources by saving the following links:
- [http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-D.xml](http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-D.xml)

---

**SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.**

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.0.5 on 2019-09-12 16:57:19-0400.  
Originally published on 2019-10-01.