## Lenovo Global Technology

ThinkSystem SR650  
(1.90 GHz, Intel Xeon Gold 6262V)

<table>
<thead>
<tr>
<th>SPECrate®2017_fp_base = 210</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate®2017_fp_peak = Not Run</td>
</tr>
</tbody>
</table>

### Hardware

- **CPU Name:** Intel Xeon Gold 6262V  
  - Max MHz: 3600  
  - Nominal: 1900  
  - Enabled: 48 cores, 2 chips, 2 threads/core  
  - Orderable: 1,2 chips  
  - Cache L1: 32 KB I + 32 KB D on chip per core  
  - L2: 1 MB I+D on chip per core  
  - L3: 33 MB I+D on chip per chip  
  - Other: None  
  - Memory: 768 GB (24 x 32 GB 2Rx4 PC4-2933Y-R, running at 2400)  
  - Storage: 1 x 800 GB SATA SSD  
  - Other: None  

### Software

- **OS:** Red Hat Enterprise Linux Server release 7.6 (Maipo)  
  - Kernel 3.10.0-957.el7.x86_64  
- **Compiler:**  
  - C/C++: Version 19.0.4.227 of Intel  
  - C/C++ Compiler for Linux;  
  - Fortran: Version 19.0.4.227 of Intel Fortran  
  - Compiler for Linux  
- **Parallel:** No  
- **Firmware:**  
  - Lenovo BIOS Version IVE142E 2.30 released Aug-2019  
  - Tested as IVE141E 2.30 Jul-2019  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** Not Applicable  
- **Other:** None  
- **Power Management:** --

---

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>SPECrate®2017_fp_base (210)</th>
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<tr>
<td>503.bwaves_r</td>
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<tr>
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<td>527.cam4_r</td>
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<td>549.fotonik3d_r</td>
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<tr>
<td>554.roms_r</td>
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Lenovo Global Technology

ThinkSystem SR650
(1.90 GHz, Intel Xeon Gold 6262V)

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

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<td>1816</td>
<td>84.0</td>
<td>1809</td>
<td>84.3</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017-1.0.5-ic19.0u4/lib/intel64"

Binaries compiled on a system with 1x Intel Core i9-799X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR650
(1.90 GHz, Intel Xeon Gold 6262V)

SPECrate®2017_fp_base = 210
SPECrate®2017_fp_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

General Notes (Continued)
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3640 (Spectre variant 3a) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3639 (Spectre variant 4) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance
Choose Operating Mode set to Custom Mode
C-States set to Legacy
SNC set to Enable
DCU Streamer Prefetcher set to Disable
Trusted Execution Technology set to Enable
Stale AtoS set to Enable
LLC dead line alloc set to Disable
Patrol Scrub set to Disable
Sysinfo program /home/cpu2017-1.0.5-ic19.0u4/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on localhost.localdomain Thu Sep 19 06:22:48 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6262V CPU @ 1.90GHz
  2 "physical id"s (chips)
  96 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 24
siblings : 48
  physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26 27 28 29
  physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13 16 17 18 19 20 21 24 25 26 27 28 29

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 96
On-line CPU(s) list: 0-95

(Continued on next page)
### Lenovo Global Technology

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<td>Test Date: Sep-2019</td>
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<td>Hardware Availability: Jul-2019</td>
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<td>Software Availability: May-2019</td>
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**SPECrate®2017_fp_base = 210**  
**SPECrate®2017_fp_peak = Not Run**

#### CPU2017 License:
9017

#### Test Sponsor:
Lenovo Global Technology

#### Tested by:
Lenovo Global Technology

#### Platform Notes (Continued)

| Thread(s) per core: | 2 |
| Core(s) per socket:  | 24 |
| Socket(s):          | 2 |
| NUMA node(s):       | 4 |
| Vendor ID:          | GenuineIntel |
| CPU family:         | 6 |
| Model:              | 85 |
| Model name:         | Intel(R) Xeon(R) Gold 6262V CPU @ 1.90GHz |
| Stepping:           | 7 |
| CPU MHz:            | 1900.000 |
| BogoMIPS:           | 3800.00 |
| Virtualization:     | VT-x |
| L1d cache:          | 32K |
| L1i cache:          | 32K |
| L2 cache:           | 1024K |
| L3 cache:           | 33792K |
| NUMA node0 CPU(s):  | 0-2,6-8,12-14,18-20,48-50,54-56,60-62,66-68 |
| NUMA node1 CPU(s):  | 3-5,9-11,15-17,21-23,51-53,57-59,63-65,69-71 |
| NUMA node2 CPU(s):  | 24-26,30-32,36-38,42-44,72-74,78-80,84-86,90-92 |
| NUMA node3 CPU(s):  | 27-29,33-35,39-41,45-47,75-77,81-83,87-89,93-95 |
| Flags:              | fpu vme de pse tsc msr pae mce cmov cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc aperfmperf eagerfpu nni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abtm 3nowprefetch eb x86ms mpx rdart_a avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl vombrob xsaveopt xsaves xsaveopt xsavec xgetbv1 cqm_llc cqm_occupt_l1c cqm_mbb_total cqm_mbb_local dtherm ida arat plc nospec avx512_vnni spec_ctrl intel_stibp flush_l1d arch_capabilities |

From `numactl --hardware`:

```
WARNING: a numactl 'node' might or might not correspond to a physical chip.
```

| node 0 cpus: 0 1 2 6 7 8 12 13 14 18 19 20 48 49 50 54 55 56 60 61 62 66 67 68 |
| node 0 size: 196280 MB |
| node 0 free: 191415 MB |
| node 1 cpus: 3 4 5 9 10 11 15 16 17 21 22 23 51 52 53 57 58 59 63 64 65 69 70 71 |
| node 1 size: 196608 MB |
| node 1 free: 191861 MB |
| node 2 cpus: 24 25 26 30 31 32 36 37 38 42 43 44 72 73 74 78 79 80 84 85 86 90 91 92 |
| node 2 size: 196608 MB |
| node 2 free: 191497 MB |

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Lenovo Global Technology
ThinkSystem SR650
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LENNOVO GLOBAL TECHNOLOGY

SPECrater®2017_fp_base = 210
SPECrater®2017_fp_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Spec CPU®2017 Floating Point Rate Result

Test Date: Sep-2019
Hardware Availability: Jul-2019
Software Availability: May-2019

Platform Notes (Continued)

node 3 cpus: 27 28 29 33 34 35 39 40 41 45 46 47 75 76 77 81 82 83 87 88 89 93 94 95
node 3 size: 196608 MB
node 3 free: 191909 MB
node distances:
node 0 1 2 3
0: 10 11 21 21
1: 11 10 21 21
2: 21 21 10 11
3: 21 21 11 10

From /proc/meminfo
MemTotal: 792239648 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
NAME="Red Hat Enterprise Linux Server"
VERSION="7.6 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VARIANT="Server"
VARIANT_ID="server"
VERSION_ID="7.6"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.6 (Maipo)"
redhat-release: Red Hat Enterprise Linux Server release 7.6 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.6 (Maipo)

uname -a:
Linux localhost.localdomain 3.10.0-957.el7.x86_64 #1 SMP Thu Oct 4 20:48:51 UTC 2018
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2017-5754 (Meltdown): Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: Load fences, __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS

run-level 3 Sep 18 17:17

SPEC is set to: /home/cpu2017-1.0.5-ic19.0u4
Filesystem Type Size Used Avail Use% Mounted on
/dev/sdb2 xfs 689G 36G 653G 6% /home

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow

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SPECrate®2017_fp_base = 210
SPECrate®2017_fp_peak = Not Run

Platform Notes (Continued)

硬件 to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Lenovo -[IVE141E-2.30]- 07/02/2019
Memory:
24x Samsung M393A4K40CB2-CVF 32 GB 2 rank 2933, configured at 2400

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C               | 519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)
-----------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
-----------------------------------------------------------------------------

==============================================================================
C++             | 508.namd_r(base) 510.parest_r(base)
-----------------------------------------------------------------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
-----------------------------------------------------------------------------

==============================================================================
C++, C          | 511.povray_r(base) 526.blender_r(base)
-----------------------------------------------------------------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
-----------------------------------------------------------------------------

==============================================================================
C++, C, Fortran | 507.cactuBSSN_r(base)
-----------------------------------------------------------------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
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Compiler Version Notes (Continued)

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

==============================================================================
Fortran         | 503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)
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Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.4.227 Build 20190416
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Fortran, C      | 521.wrf_r(base) 527.cam4_r(base)
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Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using both C and C++:
icpc -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64
SPEC CPU®2017 Floating Point Rate Result
Copyright 2017-2019 Standard Performance Evaluation Corporation

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CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
CPUTest by: Lenovo Global Technology

Test Date: Sep-2019
Hardware Availability: Jul-2019
Software Availability: May-2019

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Benchmarks using both C and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

Benchmarks using Fortran, C, and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte
# Lenovo Global Technology

**ThinkSystem SR650**  
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| SPECrate®2017_fp_base | 210 |
| SPECrate®2017_fp_peak | Not Run |

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**Tested by:** Lenovo Global Technology  

| Test Date: | Sep-2019 |
| Hardware Availability: | Jul-2019 |
| Software Availability: | May-2019 |

The flags files that were used to format this result can be browsed at

- [http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-F.html](http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-F.html)

You can also download the XML flags sources by saving the following links:

- [http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-F.xml](http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-F.xml)

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Tested with SPEC CPU®2017 v1.0.5 on 2019-09-18 18:22:48-0400.  