**SPEC CPU®2017 Floating Point Rate Result**

**NEC Corporation**

Express5800/T110j-S (Intel Core i3-9300)

<table>
<thead>
<tr>
<th>SPECrate®2017 fp_base = 29.0</th>
<th>SPECrate®2017 fp_peak = 29.5</th>
</tr>
</thead>
</table>

**CPU2017 License:** 9006  
**Test Sponsor:** NEC Corporation  
**Tested by:** NEC Corporation  
**Test Date:** Oct-2019  
**Hardware Availability:** Nov-2019  
**Software Availability:** Aug-2019

### Hardware

- **CPU Name:** Intel Core i3-9300  
  - **Max MHz:** 4300  
  - **Nominal:** 3700  
  - **Enabled:** 4 cores, 1 chip  
  - **Orderable:** 1 chip  
  - **Cache L1:** 32 KB I + 32 KB D on chip per core  
  - **L2:** 256 KB I+D on chip per core  
  - **L3:** 8 MB I+D on chip per chip  
  - **Other:** None  
- **Memory:** 64 GB (4 x 16 GB 2Rx8 PC4-2666V-E, running at 2400)  
- **Storage:** 1 x 1 TB SATA, 7200 RPM  
- **Other:** None

### Software

- **OS:** Red Hat Enterprise Linux Server release 7.7 (Maipo)  
  - **Kernel:** 3.10.0-1062.el7.x86_64  
- **Compiler:** C/C++: Version 19.0.4.227 of Intel C/C++ Compiler Build 20190416 for Linux; Fortran: Version 19.0.4.227 of Intel Fortran Compiler Build 20190416 for Linux  
- **Parallel:** No  
- **Firmware:** NEC BIOS Version F01 08/21/2019 released Nov-2019  
- **File System:** ext4  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 64-bit  
- **Other:** None  
- **Power Management:** --

---

Copyright 2017-2019 Standard Performance Evaluation Corporation
NEC Corporation
Express5800/T110j-S (Intel Core i3-9300)

**CPU2017 License:** 9006  
**Test Sponsor:** NEC Corporation  
**Tested by:** NEC Corporation  
**Test Date:** Oct-2019  
**Hardware Availability:** Nov-2019  
**Software Availability:** Aug-2019

### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>4</td>
<td>598</td>
<td>67.1</td>
<td>598</td>
<td>67.1</td>
<td>598</td>
<td>67.1</td>
<td>4</td>
<td>598</td>
<td>67.1</td>
<td>597</td>
<td>67.1</td>
<td></td>
<td></td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>4</td>
<td>202</td>
<td>25.0</td>
<td>205</td>
<td>24.7</td>
<td>202</td>
<td>25.1</td>
<td>4</td>
<td>202</td>
<td>25.0</td>
<td>203</td>
<td>24.9</td>
<td>202</td>
<td>25.1</td>
</tr>
<tr>
<td>508.namd_r</td>
<td>4</td>
<td>174</td>
<td>21.8</td>
<td>180</td>
<td>21.1</td>
<td><strong>176</strong></td>
<td><strong>21.6</strong></td>
<td>4</td>
<td>179</td>
<td>21.3</td>
<td><strong>175</strong></td>
<td><strong>21.7</strong></td>
<td>175</td>
<td>21.7</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>4</td>
<td>589</td>
<td>17.8</td>
<td>588</td>
<td>17.8</td>
<td>591</td>
<td>17.7</td>
<td>4</td>
<td>579</td>
<td>18.1</td>
<td><strong>582</strong></td>
<td><strong>18.0</strong></td>
<td>583</td>
<td>18.0</td>
</tr>
<tr>
<td>511.povray_r</td>
<td>4</td>
<td>275</td>
<td>34.0</td>
<td><strong>276</strong></td>
<td><strong>33.8</strong></td>
<td>277</td>
<td>33.7</td>
<td>4</td>
<td>236</td>
<td>39.6</td>
<td><strong>237</strong></td>
<td><strong>39.5</strong></td>
<td>239</td>
<td>39.1</td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>4</td>
<td>256</td>
<td>16.5</td>
<td>256</td>
<td>16.5</td>
<td><strong>256</strong></td>
<td><strong>16.5</strong></td>
<td>4</td>
<td><strong>255</strong></td>
<td><strong>16.5</strong></td>
<td>255</td>
<td>16.5</td>
<td>255</td>
<td>16.5</td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>4</td>
<td>270</td>
<td>33.2</td>
<td><strong>271</strong></td>
<td><strong>33.1</strong></td>
<td>271</td>
<td>33.1</td>
<td>4</td>
<td><strong>267</strong></td>
<td><strong>33.5</strong></td>
<td>266</td>
<td>33.6</td>
<td>269</td>
<td>33.3</td>
</tr>
<tr>
<td>526.blender_r</td>
<td>4</td>
<td>214</td>
<td>28.5</td>
<td><strong>214</strong></td>
<td><strong>28.5</strong></td>
<td>214</td>
<td>28.5</td>
<td>4</td>
<td><strong>214</strong></td>
<td><strong>28.5</strong></td>
<td>214</td>
<td>28.5</td>
<td>214</td>
<td>28.5</td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>4</td>
<td>214</td>
<td>32.8</td>
<td>212</td>
<td>32.9</td>
<td>215</td>
<td>32.6</td>
<td>4</td>
<td><strong>207</strong></td>
<td><strong>33.8</strong></td>
<td>208</td>
<td>33.6</td>
<td>206</td>
<td>33.9</td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>4</td>
<td><strong>133</strong></td>
<td>74.6</td>
<td>134</td>
<td>74.5</td>
<td>133</td>
<td>74.6</td>
<td>4</td>
<td><strong>133</strong></td>
<td>74.6</td>
<td>134</td>
<td>74.5</td>
<td>133</td>
<td>74.6</td>
</tr>
<tr>
<td>544.nab_r</td>
<td>4</td>
<td>159</td>
<td>42.3</td>
<td><strong>159</strong></td>
<td><strong>42.3</strong></td>
<td>159</td>
<td>42.3</td>
<td>4</td>
<td><strong>159</strong></td>
<td><strong>42.4</strong></td>
<td>159</td>
<td>42.4</td>
<td>159</td>
<td>42.4</td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>4</td>
<td>757</td>
<td>20.6</td>
<td>757</td>
<td>20.6</td>
<td>755</td>
<td>20.6</td>
<td>4</td>
<td><strong>756</strong></td>
<td><strong>20.6</strong></td>
<td>755</td>
<td>20.6</td>
<td>757</td>
<td>20.6</td>
</tr>
<tr>
<td>554.roms_r</td>
<td>4</td>
<td><strong>455</strong></td>
<td>14.0</td>
<td>463</td>
<td>13.7</td>
<td>452</td>
<td>14.1</td>
<td>4</td>
<td><strong>445</strong></td>
<td><strong>14.3</strong></td>
<td>448</td>
<td>14.2</td>
<td>445</td>
<td>14.3</td>
</tr>
</tbody>
</table>

**SPECrate®2017_fp_base = 29.0**  
**SPECrate®2017_fp_peak = 29.5**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
IRQ balance service was stopped using "systemctl stop irqbalance.service"

### Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
`LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64"`

### General Notes

Binaries compiled on a system with 1x Intel Core i9-799X CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.5  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:

(Continued on next page)
NEC Corporation
Express5800/T110j-S (Intel Core i3-9300)

SPEC CPU®2017 Floating Point Rate Result
Copyright 2017-2019 Standard Performance Evaluation Corporation

SPECrate®2017_fp_base = 29.0
SPECrate®2017_fp_peak = 29.5

CPU2017 License: 9006
Test Sponsor: NEC Corporation
Tested by: NEC Corporation

Test Date: Oct-2019
Hardware Availability: Nov-2019
Software Availability: Aug-2019

General Notes (Continued)

sync; echo 3 > /proc/sys/vm/drop_caches

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS Settings:
VT-x: Disabled
Energy Efficient P-state: Disabled
Energy Efficient Turbo: Disabled

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7ed6e6e46a485a0011
running on t110jst Tue Oct 29 16:16:08 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
    model name : Intel(R) Core(TM) i3-9300 CPU @ 3.70GHz
    1 "physical id"s (chips)
        4 "processors"
    cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
        cpu cores : 4
        siblings : 4
        physical 0: cores 0 1 2 3

From lscpu:
    Architecture:       x86_64
    CPU op-mode(s):     32-bit, 64-bit
    Byte Order:         Little Endian
    CPU(s):             4
    On-line CPU(s) list: 0-3
    Thread(s) per core: 1
    Core(s) per socket: 4
    Socket(s):          1
    NUMA node(s):       1
    Vendor ID:          GenuineIntel

(Continued on next page)
NEC Corporation

Express5800/T110j-S (Intel Core i3-9300)

SPECRate® 2017_fp_base = 29.0
SPECRate® 2017_fp_peak = 29.5

CPU2017 License: 9006
Test Sponsor: NEC Corporation
Test Date: Oct-2019
Tested by: NEC Corporation
Hardware Availability: Nov-2019
Software Availability: Aug-2019

Platform Notes (Continued)

CPU family: 6
Model: 158
Model name: Intel(R) Core(TM) i3-9300 CPU @ 3.70GHz
Stepping: 11
CPU MHz: 4199.987
CPU max MHz: 4300.0000
CPU min MHz: 800.0000
BogoMIPS: 7392.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 256K
L3 cache: 8192K
NUMA node0 CPU(s): 0-3

Flags:
 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
 pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
 lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
 aperfmperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx est tm2 ssse3 sdbg fma
 cx16 xtpr pdcm pcid sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
 avx f16c rdrand lahf_lm abm 3dnowprefetch intel_pt ssbd ibrs ibpb stibp tpr_shadow
 vmvi flexpriority ept vpid fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms invpcid mxp
 rdseed adx smap clflushopt xsaveopt xsave xsetbv1 dtherm ida arat pln pts hwp
 hwp_notify hwp_act_window hwp_epp md_clear spec_ctrl intel_stibp flush_l1d

/proc/cpuinfo cache data
 cache size : 8192 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
 available: 1 nodes (0)
 node 0 cpus: 0 1 2 3
 node 0 size: 65284 MB
 node 0 free: 63400 MB
 node distances:
 node 0
 0: 10

From /proc/meminfo
 MemTotal: 65719160 kB
 HugePages_Total: 0
 Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
 os-release:
 NAME="Red Hat Enterprise Linux Server"
 VERSION="7.7 (Maipo)"
 ID=rhel

(Continued on next page)
NEC Corporation

Express5800/T110j-S (Intel Core i3-9300)

**SPEC CPU®2017 Floating Point Rate Result**

Copyright 2017-2019 Standard Performance Evaluation Corporation

<table>
<thead>
<tr>
<th>SPECrate®2017 fp_base</th>
<th>SPECrate®2017 fp_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>= 29.0</td>
<td>= 29.5</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 9006  
**Test Sponsor:** NEC Corporation  
**Test Date:** Oct-2019  
**Hardware Availability:** Nov-2019  
**Tested by:** NEC Corporation  
**Software Availability:** Aug-2019

### Platform Notes (Continued)

```
ID_LIKE="fedora"  
VARIANT="Server"  
VARIANT_ID="server"  
VERSION_ID="7.7"  
PRETTY_NAME="Red Hat Enterprise Linux Server 7.7 (Maipo)"  
redhat-release: Red Hat Enterprise Linux Server release 7.7 (Maipo)  
system-release: Red Hat Enterprise Linux Server release 7.7 (Maipo)  
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.7:ga:server
```

```
uname -a:  
Linux t110js 3.10.0-1062.el7.x86_64 #1 SMP Thu Jul 18 20:25:13 UTC 2019 x86_64 x86_64  
ox86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

- **CVE-2018-3620 (L1 Terminal Fault):** Mitigation: PTE Inversion
- **Microarchitectural Data Sampling:** Mitigation: Clear CPU buffers; SMT disabled
- **CVE-2017-5754 (Meltdown):** Mitigation: PTI
- **CVE-2018-3639 (Speculative Store Bypass):** Mitigation: Speculative Store Bypass disabled via prctl and seccomp
- **CVE-2017-5753 (Spectre variant 1):** Mitigation: Load fences, __user pointer sanitation
- **CVE-2017-5715 (Spectre variant 2):** Mitigation: Full retpoline, IBPB

```
run-level 3 Oct 29 16:10
SPEC is set to: /home/cpu2017  
```

```
Filesystem     Type  Size  Used Avail Use% Mounted on  
/dev/sda3      ext4  908G  72G  790G   9%  /
```

From /sys/devices/virtual/dmi/id

- **BIOS:** American Megatrends Inc. F01 08/21/2019
- **Vendor:** NEC
- **Product:** Express5800/T110j-S [N8100-2804Y]
- **Serial:** 0000002

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMIOS" standard.

- **Memory:**
  - 4x Samsung M391A2K43BB1-CTD 16 GB 2 rank 2667

(End of data from sysinfo program)
NEC Corporation

Express5800/T110j-S (Intel Core i3-9300)

SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

SPECrate®2017_fp_base = 29.0
SPECrate®2017_fp_peak = 29.5

CPU2017 License: 9006
Test Date: Oct-2019
Test Sponsor: NEC Corporation
Tested by: NEC Corporation
Hardware Availability: Nov-2019
Software Availability: Aug-2019

Compiler Version Notes

C
|-- 519.1bm_r(base, peak) 538.imagick_r(base, peak)
   | 544.nab_r(base, peak)

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

C++
|-- 508.namd_r(base, peak) 510.parest_r(base, peak)

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

C++, C
|-- 511.povray_r(base, peak) 526.blender_r(base, peak)

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

C++, C, Fortran
|-- 507.cactuBSSN_r(base, peak)

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

Fortran
|-- 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak)
   | 554.roms_r(base, peak)

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
NEC Corporation

NEC Corporation

Express5800/T110j-S (Intel Core i3-9300)

SPECTop®2017 fp_base = 29.0
SPECTop®2017 fp_peak = 29.5

Compiler Version Notes (Continued)

Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

------------------------------------------------------------------------

Fortran, C      | 521.wrf_r(base, peak) 527.cam4_r(base, peak)
------------------------------------------------------------------------

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using both C and C++:
icpc -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64

(Continued on next page)
SPEC CPU®2017 Floating Point Rate Result
Copyright 2017-2019 Standard Performance Evaluation Corporation

NEC Corporation
Express5800/T110j-S (Intel Core i3-9300)

SPECrate®2017_fp_base = 29.0
SPECrate®2017_fp_peak = 29.5

CPU2017 License: 9006
Test Sponsor: NEC Corporation
Tested by: NEC Corporation
Test Date: Oct-2019
Hardware Availability: Nov-2019
Software Availability: Aug-2019

Base Portability Flags (Continued)

521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Benchmarks using both C and C++:
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

Peak Compiler Invocation

C benchmarks:
icc -m64 -std=cl11

(Continued on next page)
Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
ifort -m64 icc -m64 -std=c11

Benchmarks using both C and C++:
icpc -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:
icpc -m64 icc -m64 -std=c11 ifort -m64

Peak Portability Flags
Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
519.lbm_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

538.imagick_r: basepeak = yes

544.nab_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4

C++ benchmarks:
508.namd_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

510.parest_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4

(Continued on next page)
SPEC CPU®2017 Floating Point Rate Result
Copyright 2017-2019 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/T110j-S (Intel Core i3-9300)

SPECrate®2017_fp_base = 29.0
SPECrate®2017_fp_peak = 29.5

CPU2017 License: 9006
Test Sponsor: NEC Corporation
Test Date: Oct-2019
Tested by: NEC Corporation
Hardware Availability: Nov-2019
Software Availability: Aug-2019

Peak Optimization Flags (Continued)

Fortran benchmarks:

503.bwaves_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -auto
-nostandard-realloc-lhs -align array32byte

549.fotonik3d_r: Same as 503.bwaves_r

554.roms_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Benchmarks using both Fortran and C:

-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

Benchmarks using both C and C++:

511.povray_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4

526.blender_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4

Benchmarks using Fortran, C, and C++:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs
-align array32byte

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
<table>
<thead>
<tr>
<th>NEC Corporation</th>
<th>SPECrate®2017_fp_base = 29.0</th>
</tr>
</thead>
<tbody>
<tr>
<td>Express5800/T110j-S (Intel Core i3-9300)</td>
<td>SPECrate®2017_fp_peak = 29.5</td>
</tr>
</tbody>
</table>

| CPU2017 License:       | 9006                        |
| Test Sponsor:          | NEC Corporation             |
| Tested by:             | NEC Corporation             |
| Test Date:             | Oct-2019                    |
| Hardware Availability: | Nov-2019                    |
| Software Availability: | Aug-2019                    |

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.