### Dell Inc.

**PowerEdge MX740c (Intel Xeon Gold 6226, 2.70GHz)**

<table>
<thead>
<tr>
<th>SPECrate®2017_int_base</th>
<th>Dell Inc.</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate®2017_int_peak</td>
<td>Dell Inc.</td>
</tr>
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</table>

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>55</th>
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<tbody>
<tr>
<td>Test Sponsor:</td>
<td>Dell Inc.</td>
</tr>
<tr>
<td>Tested by:</td>
<td>Dell Inc.</td>
</tr>
<tr>
<td>Test Date:</td>
<td>Jul-2019</td>
</tr>
<tr>
<td>Hardware Availability:</td>
<td>Jun-2019</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>Aug-2019</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Copies</th>
<th>SPECrate®2017_int_base (165)</th>
<th>SPECrate®2017_int_peak (171)</th>
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<tr>
<td>500.perlbench_r</td>
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<td>142</td>
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<tr>
<td>502.gcc_r</td>
<td>48</td>
<td>135</td>
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<td>505.mcf_r</td>
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<td>520.omnetpp_r</td>
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<tr>
<td>523.xalancbmk_r</td>
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<td>103</td>
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<tr>
<td>525.x264_r</td>
<td>48</td>
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<tr>
<td>531.deepsjeng_r</td>
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<tr>
<td>541.leela_r</td>
<td>48</td>
<td>222</td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>48</td>
<td>332</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>48</td>
<td>332</td>
</tr>
</tbody>
</table>

### Hardware

- **CPU Name:** Intel Xeon Gold 6226
- **Max MHz:** 3700
- **Nominal:** 2700
- **Enabled:** 24 cores, 2 chips, 2 threads/core
- **Orderable:** 1.2 chips
- **Cache L1:** 32 KB I + 32 KB D on chip per core
- **L2:** 1 MB I+D on chip per core
- **L3:** 19.25 MB I+D on chip per chip
- **Other:** None
- **Memory:** 384 GB (12 x 32 GB 2Rx4 PC4-2933Y-R)
- **Storage:** 1 x 480 GB SATA SSD
- **Other:** None

### Software

- **OS:** Ubuntu 18.04.2 LTS
- **Compiler:** C/C++: Version 19.0.4.227 of Intel C/C++ Compiler Build 20190416 for Linux;
- **Fortran:** Version 19.0.4.227 of Intel Fortran Compiler Build 20190416 for Linux
- **Parallel:** No
- **Firmware:** Version 2.3.10 released Aug-2019
- **File System:** ext4
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 32/64-bit
- **Other:** jemalloc memory allocator V5.0.1
- **Power Management:** --
Dell Inc.

PowerEdge MX740c (Intel Xeon Gold 6226, 2.70GHz)

SPECrate®2017_int_base = 165
SPECrate®2017_int_peak = 171

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<tr>
<td>502.gcc_r</td>
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<td>503</td>
<td>135</td>
<td>502</td>
<td>135</td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>48</td>
<td>350</td>
<td>222</td>
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<td>221</td>
<td>350</td>
<td>222</td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>48</td>
<td>610</td>
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<td>611</td>
<td>103</td>
<td>608</td>
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<td>523.xalancbmk_r</td>
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<tr>
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<td>255</td>
<td>329</td>
<td>253</td>
<td>332</td>
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<tr>
<td>531.deepsjeng_r</td>
<td>48</td>
<td>403</td>
<td>137</td>
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<td>136</td>
<td>404</td>
<td>136</td>
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<tr>
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<td>378</td>
<td>332</td>
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<td>331</td>
<td>379</td>
<td>332</td>
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<tr>
<td>557.xz_r</td>
<td>48</td>
<td>491</td>
<td>106</td>
<td>489</td>
<td>106</td>
<td>490</td>
<td>106</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/lib/ia32:/home/cpu2017/je5.0.1-32"
Binary compiled on a system with 1x Intel Core i9-799X CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.5
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
  sync; echo 3 > /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
umactl --interleave=all runcpu <etc>

(Continued on next page)
SPEC CPU®2017 Integer Rate Result

**Dell Inc.**

PowerEdge MX740c (Intel Xeon Gold 6226, 2.70GHz)

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</tr>
</tbody>
</table>

**SPECrate®2017_int_base = 165**

**SPECrate®2017_int_peak = 171**

---

**General Notes (Continued)**

jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

**Platform Notes**

BIOS settings:
- ADDDC setting disabled
- Sub NUMA Cluster enabled
- Virtualization Technology disabled
- DCU Streamer Prefetcher disabled
- System Profile set to Custom
- CPU Performance set to Maximum Performance
- C States set to Autonomous
- C1E disabled
- Uncore Frequency set to Dynamic
- Energy Efficiency Policy set to Performance
- Memory Patrol Scrub disabled
- Logical Processor enabled
- CPU Interconnect Bus Link Power Management enabled
- PCI ASPM L1 Link Power Management enabled
- Sysinfo program /home/cpu2017/bin/sysinfo
- Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
- running on intel-sut Mon Nov 18 20:55:00 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
- model name : Intel(R) Xeon(R) Gold 6226 CPU @ 2.70GHz
  2 "physical id"s (chips)
  48 "processors"
- cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 12
  siblings : 24
  physical 0: cores 1 2 3 5 6 8 9 10 11 12 13 14
  physical 1: cores 0 2 3 4 5 8 9 10 11 12 13 14

From lscpu:
- Architecture: x86_64
- CPU op-mode(s): 32-bit, 64-bit
- Byte Order: Little Endian
- CPU(s): 48
- On-line CPU(s) list: 0-47

(Continued on next page)
## Platform Notes (Continued)

<table>
<thead>
<tr>
<th>Thread(s) per core:</th>
<th>2</th>
</tr>
</thead>
<tbody>
<tr>
<td>Core(s) per socket:</td>
<td>12</td>
</tr>
<tr>
<td>Socket(s):</td>
<td>2</td>
</tr>
<tr>
<td>NUMA node(s):</td>
<td>4</td>
</tr>
<tr>
<td>Vendor ID:</td>
<td>GenuineIntel</td>
</tr>
<tr>
<td>CPU family:</td>
<td>6</td>
</tr>
<tr>
<td>Model:</td>
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</tr>
<tr>
<td>Model name:</td>
<td>Intel(R) Xeon(R) Gold 6226 CPU @ 2.70GHz</td>
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<tr>
<td>Stepping:</td>
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<tr>
<td>CPU MHz:</td>
<td>3411.179</td>
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<td>BogoMIPS:</td>
<td>5400.00</td>
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<tr>
<td>Virtualization:</td>
<td>VT-x</td>
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<tr>
<td>L1d cache:</td>
<td>32K</td>
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<tr>
<td>L1i cache:</td>
<td>32K</td>
</tr>
<tr>
<td>L2 cache:</td>
<td>1024K</td>
</tr>
<tr>
<td>L3 cache:</td>
<td>19712K</td>
</tr>
<tr>
<td>NUMA node0 CPU(s):</td>
<td>0,4,8,12,16,20,24,28,32,36,40,44</td>
</tr>
<tr>
<td>NUMA node1 CPU(s):</td>
<td>1,5,9,13,17,21,25,29,33,37,41,45</td>
</tr>
<tr>
<td>NUMA node2 CPU(s):</td>
<td>2,6,10,14,18,22,26,30,34,38,42,46</td>
</tr>
<tr>
<td>NUMA node3 CPU(s):</td>
<td>3,7,11,15,19,23,27,31,35,39,43,47</td>
</tr>
<tr>
<td>Flags:</td>
<td>fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abml 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3 invpcid_single intel_pinn ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi flexpriority vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 ibrms invpcid rtm cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl xsaveopt xsaves cqm_llc cqm_ocrop_llc cqm_mbm_total cqm_mbm_local dtherm ida arat pln pts pku ospke avx512_vnni md_clear flush_lld arch_capabilities</td>
</tr>
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</table>

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

<table>
<thead>
<tr>
<th>available:</th>
<th>4 nodes (0-3)</th>
</tr>
</thead>
<tbody>
<tr>
<td>node 0 cpus:</td>
<td>0 4 8 12 16 20 24 28 32 36 40 44</td>
</tr>
<tr>
<td>node 0 size:</td>
<td>95148 MB</td>
</tr>
<tr>
<td>node 0 free:</td>
<td>94905 MB</td>
</tr>
<tr>
<td>node 1 cpus:</td>
<td>1 5 9 13 17 21 25 29 33 37 41 45</td>
</tr>
<tr>
<td>node 1 size:</td>
<td>96765 MB</td>
</tr>
<tr>
<td>node 1 free:</td>
<td>96482 MB</td>
</tr>
<tr>
<td>node 2 cpus:</td>
<td>2 6 10 14 18 22 26 30 34 38 42 46</td>
</tr>
<tr>
<td>node 2 size:</td>
<td>96744 MB</td>
</tr>
</tbody>
</table>

(Continued on next page)
Dell Inc.

PowerEdge MX740c (Intel Xeon Gold 6226, 2.70GHz)

SPEC CPU®2017 Integer Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge MX740c (Intel Xeon Gold 6226, 2.70GHz)

SPECrate®2017_int_base = 165
SPECrate®2017_int_peak = 171

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Jul-2019
Hardware Availability: Jun-2019
Software Availability: Aug-2019

Platform Notes (Continued)

node 2 free: 96377 MB
node 3 cpus: 3 7 11 15 19 23 27 31 35 39 43 47
node 3 size: 96764 MB
node 3 free: 96566 MB
node distances:
node 0 1 2 3
0: 10 21 11 21
1: 21 10 21 11
2: 11 21 10 21
3: 21 11 21 10

From /proc/meminfo
MemTotal: 394673364 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
Ubuntu 18.04.2 LTS

From /etc/*release* /etc/*version*
debian_version: buster/sid
os-release:
  NAME="Ubuntu"
  VERSION="18.04.2 LTS (Bionic Beaver)"
  ID=ubuntu
  ID_LIKE=debian
  PRETTY_NAME="Ubuntu 18.04.2 LTS"
  VERSION_ID="18.04"
  HOME_URL="https://www.ubuntu.com/"
  SUPPORT_URL="https://help.ubuntu.com/"

uname -a:
  Linux intel-sut 4.15.0-58-generic #64-Ubuntu SMP Tue Aug 6 11:12:41 UTC 2019 x86_64
  x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2017-5754 (Meltdown): Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swapgs barriers and __user
pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB
filling

run-level 3 Nov 18 20:54

SPEC is set to: /home/cpu2017

Filesystem  Type  Size  Used  Avail  Use%  Mounted on

(Continued on next page)
SPEC CPU®2017 Integer Rate Result

Dell Inc. PowerEdge MX740c (Intel Xeon Gold 6226, 2.70GHz)

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Platform Notes (Continued)
/dev/sda2   ext4  439G  32G  385G  8% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 2.3.10 08/15/2019
Memory:
1x 002C0632002C 36ASF4G72PZ-3G2E2 32 GB 2 rank 3200, configured at 2933
10x 00AD00B300AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933
1x 00AD063200AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933
12x Not Specified Not Specified

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C       | 502.gcc_r(peak)
------------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler for applications running on IA-32, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
C       | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak) 557.xz_r(base, peak)
------------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
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C       | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak) 557.xz_r(base, peak)
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SPECrater®2017_int_base = 165
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Compiler Version Notes (Continued)

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

------------------------------------------------------------------------------
C++ | 523.xalancbmk_r (peak)
------------------------------------------------------------------------------

Intel(R) C++ Intel(R) 64 Compiler for applications running on IA-32, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
C++ | 520.omnetpp_r (base, peak) 523.xalancbmk_r (base) 531.deepsjeng_r (base, peak) 541.leela_r (base, peak)
------------------------------------------------------------------------------

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
C++ | 523.xalancbmk_r (peak)
------------------------------------------------------------------------------

Intel(R) C++ Intel(R) 64 Compiler for applications running on IA-32, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
C++ | 520.omnetpp_r (base, peak) 523.xalancbmk_r (base) 531.deepsjeng_r (base, peak) 541.leela_r (base, peak)
------------------------------------------------------------------------------

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

Fortran | 548.exchange2_r (base, peak)
------------------------------------------------------------------------------

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
SPEC CPU®2017 Integer Rate Result

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CPU2017 License: 55  
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Hardware Availability: Jun-2019

Software Availability: Aug-2019

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

C++ benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

Fortran benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc
## SPEC CPU®2017 Integer Rate Result

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<td>Software Availability: Aug-2019</td>
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</table>

### Peak Compiler Invocation

C benchmarks (except as noted below):

```bash
icc -m64 -std=c11
```

$02.gcc_r:icc -m32 -std=c11 -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/ia32_lin

C++ benchmarks (except as noted below):

```bash
icpc -m64
```

$23.xalancbmk_r:icpc -m32 -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/ia32_lin

Fortran benchmarks:

```bash
ifort -m64
```

### Peak Portability Flags

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Flags</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>-DSPEC_LP64 -DSPEC_LINUX_X64</td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>-D_FILE_OFFSET_BITS=64</td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>-DSPEC_LP64</td>
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<td>520.omnetpp_r</td>
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<td>523.xalancbmk_r</td>
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<td>525.x264_r</td>
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<td>531.deepsjeng_r</td>
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<tr>
<td>541.leela_r</td>
<td>-DSPEC_LP64</td>
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<td>548.exchange2_r</td>
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<tr>
<td>557.xz_r</td>
<td>-DSPEC_LP64</td>
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### Peak Optimization Flags

C benchmarks:

```bash
500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo  
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4  
-fno-strict-overflow  
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64  
-lqkmalloc
```

```bash
502.gcc_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo  
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4  
-L/usr/local/je5.0.1-32/lib -ljemalloc
```

```bash
505.mcf_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=4
```

(Continued on next page)
Dell Inc.

PowerEdge MX740c (Intel Xeon Gold 6226, 2.70GHz)

{## SPEC CPU®2017 Integer Rate Result

**Dell Inc.**

**PowerEdge MX740c (Intel Xeon Gold 6226, 2.70GHz)**

**SPECrate®2017 Int Base = 165**

**SPECrate®2017 Int Peak = 171**

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**CPU2017 License:** 55
**Test Sponsor:** Dell Inc.
**Tested by:** Dell Inc.
**Test Date:** Jul-2019
**Hardware Availability:** Jun-2019
**Software Availability:** Aug-2019

---

**Peak Optimization Flags (Continued)**

505.mcf_r (continued):
- `-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64`
- `-lqkmalloc`

525.x264_r: `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
- `-qopt-mem-layout-trans=4 -fno-alias`
- `-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64`
- `-lqkmalloc`

557.xz_r: Same as 505.mcf_r

C++ benchmarks:

520.omnetpp_r: `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
- `-qopt-mem-layout-trans=4`
- `-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64`
- `-lqkmalloc`

523.xalancbmk_r: `-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo`
- `-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4`
- `-L/usr/local/je5.0.1-32/lib -ljemalloc`

531.deepsjeng_r: Same as 520.omnetpp_r

541.leela_r: Same as 520.omnetpp_r

Fortran benchmarks:

- `-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
- `-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte`
- `-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64`
- `-lqkmalloc`

The flags files that were used to format this result can be browsed at:


You can also download the XML flags sources by saving the following links:


Dell Inc.  
PowerEdge MX740c (Intel Xeon Gold 6226, 2.70GHz)  

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.0.5 on 2019-11-18 15:54:59-0500.  
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