SPEC CPU®2017 Floating Point Rate Result

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Dell Inc.

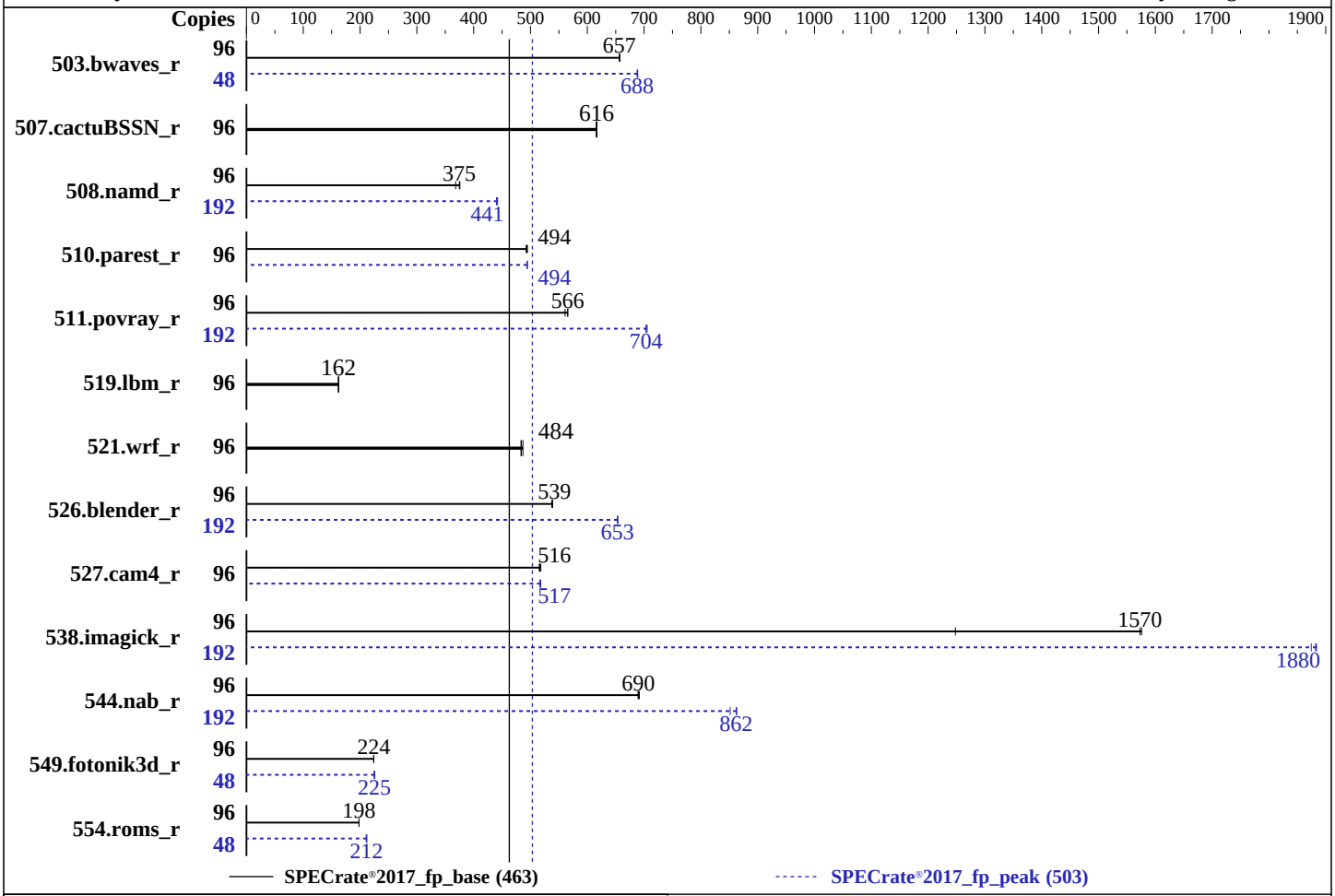
PowerEdge C6525 (AMD EPYC 7642, 2.30 GHz)

SPECrate®2017_fp_base = 463

SPECrate®2017_fp_peak = 503

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Oct-2019
Hardware Availability: Feb-2020
Software Availability: Aug-2019



Hardware

CPU Name:	AMD EPYC 7642
Max MHz:	3300
Nominal:	2300
Enabled:	96 cores, 2 chips, 2 threads/core
Orderable:	1,2 chips
Cache L1:	32 KB I + 32 KB D on chip per core
L2:	512 KB I+D on chip per core
L3:	256 MB I+D on chip per chip, 16 MB shared / 3 cores
Other:	None
Memory:	512 GB (16 x 32 GB 2Rx4 PC4-3200AA-R)
Storage:	1 x 480 GB SAS SSD
Other:	None

Software

OS:	SUSE Linux Enterprise Server 15 SP1 kernel 4.12.14-195-default
Compiler:	C/C++/Fortran: Version 2.0.0 of AOCC
Parallel:	No
Firmware:	Version 1.0.1 released Sep-2019
File System:	xfs
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	64-bit
Other:	jemalloc: jemalloc memory allocator library v5.2.0
Power Management:	BIOS set to prefer performance at the cost of additional power usage.

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	96	1465	657	1467	656	**1466**	**657**	48	700	688	**699**	**688**	699	689
507.cactuBSSN_r	96	**197**	**616**	197	617	197	616	96	**197**	**616**	197	617	197	616
508.namd_r	96	248	368	243	376	**243**	**375**	192	**414**	**441**	414	440	413	442
510.parest_r	96	508	494	**509**	**494**	510	492	96	508	494	**508**	**494**	508	494
511.povray_r	96	396	566	**396**	**566**	400	561	192	637	703	**637**	**704**	636	705
519.lbm_r	96	625	162	624	162	**625**	**162**	96	625	162	624	162	**625**	**162**
521.wrf_r	96	442	487	**445**	**484**	445	483	96	442	487	**445**	**484**	445	483
526.blender_r	96	272	537	**271**	**539**	271	539	192	447	654	448	653	**447**	**653**
527.cam4_r	96	324	518	**325**	**516**	326	515	96	324	518	325	517	**325**	**517**
538.imagick_r	96	191	1250	**152**	**1570**	152	1580	192	255	1870	**254**	**1880**	253	1880
544.nab_r	96	234	692	234	689	**234**	**690**	192	379	852	374	863	**375**	**862**
549.fotonik3d_r	96	**1669**	**224**	1669	224	1670	224	48	829	226	**831**	**225**	832	225
554.roms_r	96	**770**	**198**	769	198	770	198	48	**361**	**212**	362	211	360	212

SPECrate®2017_fp_base = 463

SPECrate®2017_fp_peak = 503

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

```
The AMD64 AOCC Compiler Suite is available at
http://developer.amd.com/amd-aocc/
```

Submit Notes

```
The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

Set dirty_ratio=8 to limit dirty cache to 8% of memory
Set swappiness=1 to swap only if necessary
Set zone_reclaim_mode=1 to free local node memory and avoid remote memory
sync then drop_caches=3 to reset caches before invoking runcpu
```

(Continued on next page)

Operating System Notes (Continued)

```
dirty_ratio, swappiness, zone_reclaim_mode and drop_caches were
all set using privileged echo (e.g. echo 1 > /proc/sys/vm/swappiness).

Transparent huge pages set to 'always' for this run (OS default)
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH =
     "/root/cpu2017-1.0.5/cpu2017-1.1.0/amd_rate_aocc200_rome_C_lib/64;/root/
     cpu2017-1.0.5/cpu2017-1.1.0/amd_rate_aocc200_rome_C_lib/32:"
MALLOC_CONF = "retain:true"
```

General Notes

```
Binaries were compiled on a system with 2x AMD EPYC 7601 CPU + 512GB Memory using Fedora 26

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

jemalloc: configured and built with GCC v9.1.0 in Ubuntu 19.04 with -O3 -znver2 -flto
jemalloc 5.2.0 is available here:
https://github.com/jemalloc/jemalloc/releases/download/5.2.0/jemalloc-5.2.0.tar.bz2
```

Platform Notes

```
 BIOS settings:
  NUMA Nodes Per Socket set to 4
  CCX as NUMA Domain set to Enabled
  System Profile set to Custom
  CPU Power Management set to Maximum Performance
  Memory Frequency set to Maximum Performance
  Turbo Boost Enabled
  Cstates set to Enabled
  Memory Patrol Scrub Disabled
  Memory Refresh Rate set to 1x
  PCI ASPM L1 Link Power Management Disabled
  Determinism Slider set to Power Determinism
  Efficiency Optimized Mode Disabled
```

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Platform Notes (Continued)

```
 Memory Interleaving set to Disabled

Sysinfo program /root/cpu2017-1.0.5/cpu2017-1.1.0/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7edb1e6e46a485a0011
running on linux-g3ob Wed Oct 23 12:01:01 2019

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
   https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : AMD EPYC 7642 48-Core Processor
      2  "physical id"s (chips)
      192 "processors"
   cores, siblings (Caution: counting these is hw and system dependent. The following
   excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
      cpu cores : 48
      siblings  : 96
      physical 0: cores 0 1 2 4 5 6 8 9 10 12 13 14 16 17 18 20 21 22 24 25 26 28 29 30
      32 33 34 36 37 38 40 41 42 44 45 46 48 49 50 52 53 54 56 57 58 60 61 62
      physical 1: cores 0 1 2 4 5 6 8 9 10 12 13 14 16 17 18 20 21 22 24 25 26 28 29 30
      32 33 34 36 37 38 40 41 42 44 45 46 48 49 50 52 53 54 56 57 58 60 61 62

From lscpu:
     Architecture:        x86_64
     CPU op-mode(s):      32-bit, 64-bit
     Byte Order:          Little Endian
     Address sizes:       43 bits physical, 48 bits virtual
     CPU(s):              192
     On-line CPU(s) list: 0-191
     Thread(s) per core:  2
     Core(s) per socket:  48
     Socket(s):           2
     NUMA node(s):        32
     Vendor ID:           AuthenticAMD
     CPU family:          23
     Model:               49
     Model name:          AMD EPYC 7642 48-Core Processor
     Stepping:            0
     CPU MHz:             2295.672
     BogoMIPS:            4591.34
     Virtualization:      AMD-V
     L1d cache:           32K
     L1i cache:           32K
     L2 cache:            512K
     L3 cache:            16384K
     NUMA node0 CPU(s):   0-2,96-98
```

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Platform Notes (Continued)

```
      NUMA node1 CPU(s):   3-5,99-101
      NUMA node2 CPU(s):   6-8,102-104
      NUMA node3 CPU(s):   9-11,105-107
      NUMA node4 CPU(s):   12-14,108-110
      NUMA node5 CPU(s):   15-17,111-113
      NUMA node6 CPU(s):   18-20,114-116
      NUMA node7 CPU(s):   21-23,117-119
      NUMA node8 CPU(s):   24-26,120-122
      NUMA node9 CPU(s):   27-29,123-125
      NUMA node10 CPU(s):  30-32,126-128
      NUMA node11 CPU(s):  33-35,129-131
      NUMA node12 CPU(s):  36-38,132-134
      NUMA node13 CPU(s):  39-41,135-137
      NUMA node14 CPU(s):  42-44,138-140
      NUMA node15 CPU(s):  45-47,141-143
      NUMA node16 CPU(s):  48-50,144-146
      NUMA node17 CPU(s):  51-53,147-149
      NUMA node18 CPU(s):  54-56,150-152
      NUMA node19 CPU(s):  57-59,153-155
      NUMA node20 CPU(s):  60-62,156-158
      NUMA node21 CPU(s):  63-65,159-161
      NUMA node22 CPU(s):  66-68,162-164
      NUMA node23 CPU(s):  69-71,165-167
      NUMA node24 CPU(s):  72-74,168-170
      NUMA node25 CPU(s):  75-77,171-173
      NUMA node26 CPU(s):  78-80,174-176
      NUMA node27 CPU(s):  81-83,177-179
      NUMA node28 CPU(s):  84-86,180-182
      NUMA node29 CPU(s):  87-89,183-185
      NUMA node30 CPU(s):  90-92,186-188
      NUMA node31 CPU(s):  93-95,189-191
      Flags:               fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
     pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm
     constant_tsc rep_good nopl xtopology nonstop_tsc cpuid extd_apicid aperfmperf pni
     pclmulqdq monitor ssse3 fma cx16 sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx
     f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse
     3dnowprefetch osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext
     perfctr_l2 mwaitx cpb cat_l3 cdp_l3 hw_pstate sme ssbd sev ibrs ibpb stibp vmmcall
     fsgsbase bmi1 avx2 smep bmi2 cqm rdt_a rdseed adx smap clflushopt clwb sha_ni
     xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
     clzero irperf xsaveerptr arat npt lbrv svm_lock nrip_save tsc_scale vmcb_clean
     flushbyasid decodeassists pausefilter pfthreshold avic v_vmsave_vmload vgif umip
     rdpid overflow_recov succor smca

  /proc/cpuinfo cache data
     cache size : 512 KB
```

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Platform Notes (Continued)

```
From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a
physical chip.
  available: 32 nodes (0-31)
  node 0 cpus: 0 1 2 96 97 98
  node 0 size: 15676 MB
  node 0 free: 15582 MB
  node 1 cpus: 3 4 5 99 100 101
  node 1 size: 16126 MB
  node 1 free: 16032 MB
  node 2 cpus: 6 7 8 102 103 104
  node 2 size: 16126 MB
  node 2 free: 16038 MB
  node 3 cpus: 9 10 11 105 106 107
  node 3 size: 16125 MB
  node 3 free: 15766 MB
  node 4 cpus: 12 13 14 108 109 110
  node 4 size: 16126 MB
  node 4 free: 16038 MB
  node 5 cpus: 15 16 17 111 112 113
  node 5 size: 16126 MB
  node 5 free: 16058 MB
  node 6 cpus: 18 19 20 114 115 116
  node 6 size: 16126 MB
  node 6 free: 16068 MB
  node 7 cpus: 21 22 23 117 118 119
  node 7 size: 16125 MB
  node 7 free: 16064 MB
  node 8 cpus: 24 25 26 120 121 122
  node 8 size: 16126 MB
  node 8 free: 16027 MB
  node 9 cpus: 27 28 29 123 124 125
  node 9 size: 16126 MB
  node 9 free: 16050 MB
  node 10 cpus: 30 31 32 126 127 128
  node 10 size: 16126 MB
  node 10 free: 16054 MB
  node 11 cpus: 33 34 35 129 130 131
  node 11 size: 16125 MB
  node 11 free: 16066 MB
  node 12 cpus: 36 37 38 132 133 134
  node 12 size: 16126 MB
  node 12 free: 16059 MB
  node 13 cpus: 39 40 41 135 136 137
  node 13 size: 16126 MB
  node 13 free: 16061 MB
  node 14 cpus: 42 43 44 138 139 140
  node 14 size: 16126 MB
```

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Platform Notes (Continued)

```
node 14 free: 16067 MB
node 15 cpus: 45 46 47 141 142 143
node 15 size: 16113 MB
node 15 free: 16059 MB
node 16 cpus: 48 49 50 144 145 146
node 16 size: 16097 MB
node 16 free: 16051 MB
node 17 cpus: 51 52 53 147 148 149
node 17 size: 16126 MB
node 17 free: 16078 MB
node 18 cpus: 54 55 56 150 151 152
node 18 size: 16126 MB
node 18 free: 16085 MB
node 19 cpus: 57 58 59 153 154 155
node 19 size: 16125 MB
node 19 free: 16074 MB
node 20 cpus: 60 61 62 156 157 158
node 20 size: 16126 MB
node 20 free: 16085 MB
node 21 cpus: 63 64 65 159 160 161
node 21 size: 16126 MB
node 21 free: 16082 MB
node 22 cpus: 66 67 68 162 163 164
node 22 size: 16126 MB
node 22 free: 16076 MB
node 23 cpus: 69 70 71 165 166 167
node 23 size: 16125 MB
node 23 free: 16081 MB
node 24 cpus: 72 73 74 168 169 170
node 24 size: 16126 MB
node 24 free: 16074 MB
node 25 cpus: 75 76 77 171 172 173
node 25 size: 16126 MB
node 25 free: 16076 MB
node 26 cpus: 78 79 80 174 175 176
node 26 size: 16126 MB
node 26 free: 16086 MB
node 27 cpus: 81 82 83 177 178 179
node 27 size: 16125 MB
node 27 free: 16080 MB
node 28 cpus: 84 85 86 180 181 182
node 28 size: 16126 MB
node 28 free: 16067 MB
node 29 cpus: 87 88 89 183 184 185
node 29 size: 16126 MB
node 29 free: 16078 MB
node 30 cpus: 90 91 92 186 187 188
```

(Continued on next page)

Platform Notes (Continued)

```
node 30 size: 16126 MB
node 30 free: 16075 MB
node 31 cpus: 93 94 95 189 190 191
node 31 size: 16124 MB
node 31 free: 16070 MB
node distances:
node   0   1   2   3   4   5   6   7   8   9  10  11  12  13  14  15  16  17  18  19
20  21  22  23  24  25  26  27  28  29  30  31
  0:  10  11  11  11  12  12  12  12  12  12  12  12  12  12  12  12  32  32  32  32
  32  32  32  32  32  32  32  32  32  32  32  32
  1:  11  10  11  11  12  12  12  12  12  12  12  12  12  12  12  12  32  32  32  32
  32  32  32  32  32  32  32  32  32  32  32  32
  2:  11  11  10  11  12  12  12  12  12  12  12  12  12  12  12  12  32  32  32  32
  32  32  32  32  32  32  32  32  32  32  32  32
  3:  11  11  11  10  12  12  12  12  12  12  12  12  12  12  12  12  32  32  32  32
  32  32  32  32  32  32  32  32  32  32  32  32
  4:  12  12  12  12  10  11  11  11  12  12  12  12  12  12  12  12  32  32  32  32
  32  32  32  32  32  32  32  32  32  32  32  32
  5:  12  12  12  12  11  10  11  11  12  12  12  12  12  12  12  12  32  32  32  32
  32  32  32  32  32  32  32  32  32  32  32  32
  6:  12  12  12  12  11  11  10  11  12  12  12  12  12  12  12  12  32  32  32  32
  32  32  32  32  32  32  32  32  32  32  32  32
  7:  12  12  12  12  11  11  11  10  12  12  12  12  12  12  12  12  32  32  32  32
  32  32  32  32  32  32  32  32  32  32  32  32
  8:  12  12  12  12  12  12  12  12  10  11  11  11  12  12  12  12  32  32  32  32
  32  32  32  32  32  32  32  32  32  32  32  32
  9:  12  12  12  12  12  12  12  12  11  10  11  11  12  12  12  12  32  32  32  32
  32  32  32  32  32  32  32  32  32  32  32  32
 10:  12  12  12  12  12  12  12  12  11  11  10  11  12  12  12  12  32  32  32  32
 32  32  32  32  32  32  32  32  32  32  32  32
 11:  12  12  12  12  12  12  12  12  11  11  11  10  12  12  12  12  32  32  32  32
 32  32  32  32  32  32  32  32  32  32  32  32
 12:  12  12  12  12  12  12  12  12  12  12  12  12  10  11  11  11  32  32  32  32
 32  32  32  32  32  32  32  32  32  32  32  32
 13:  12  12  12  12  12  12  12  12  12  12  12  12  11  10  11  11  32  32  32  32
 32  32  32  32  32  32  32  32  32  32  32  32
 14:  12  12  12  12  12  12  12  12  12  12  12  12  11  11  10  11  32  32  32  32
 32  32  32  32  32  32  32  32  32  32  32  32
 15:  12  12  12  12  12  12  12  12  12  12  12  12  11  11  11  10  32  32  32  32
 32  32  32  32  32  32  32  32  32  32  32  32
 16:  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  10  11  11  11
 12  12  12  12  12  12  12  12  12  12  12  12
 17:  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  11  10  11  11
 12  12  12  12  12  12  12  12  12  12  12  12
 18:  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  11  11  10  11
 12  12  12  12  12  12  12  12  12  12  12  12
 19:  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  11  11  11  10
```


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Platform Notes (Continued)

```
    12  12  12  12  12  12  12  12  12  12  12  12
    20:  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  12  12  12  12
    10  11  11  11  12  12  12  12  12  12  12  12
    21:  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  12  12  12  12
    11  10  11  11  12  12  12  12  12  12  12  12
    22:  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  12  12  12  12
    11  11  10  11  12  12  12  12  12  12  12  12
    23:  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  12  12  12  12
    11  11  11  10  12  12  12  12  12  12  12  12
    24:  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  12  12  12  12
    12  12  12  12  10  11  11  11  12  12  12  12
    25:  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  12  12  12  12
    12  12  12  12  11  10  11  11  12  12  12  12
    26:  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  12  12  12  12
    12  12  12  12  11  11  10  11  12  12  12  12
    27:  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  12  12  12  12
    12  12  12  12  11  11  11  10  12  12  12  12
    28:  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  12  12  12  12
    12  12  12  12  12  12  12  12  10  11  11  11
    29:  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  12  12  12  12
    12  12  12  12  12  12  12  12  11  10  11  11
    30:  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  12  12  12  12
    12  12  12  12  12  12  12  12  11  11  10  11
    31:  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  32  12  12  12  12
    12  12  12  12  12  12  12  12  11  11  11  10

  From /proc/meminfo
     MemTotal:       527927004 kB
     HugePages_Total:       0
     Hugepagesize:       2048 kB

  From /etc/*release* /etc/*version*
     os-release:
        NAME="SLES"
        VERSION="15-SP1"
        VERSION_ID="15.1"
        PRETTY_NAME="SUSE Linux Enterprise Server 15 SP1"
        ID="sles"
        ID_LIKE="suse"
        ANSI_COLOR="0;32"
        CPE_NAME="cpe:/o:suse:sles:15:sp1"

  uname -a:
     Linux linux-g3ob 4.12.14-195-default #1 SMP Tue May 7 10:55:11 UTC 2019 (8fba516)
     x86_64 x86_64 x86_64 GNU/Linux

  Kernel self-reported vulnerability status:
```


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Platform Notes (Continued)

```
 CVE-2018-3620 (L1 Terminal Fault):        Not affected
 Microarchitectural Data Sampling:         Not affected
 CVE-2017-5754 (Meltdown):                 Not affected
 CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled
                                           via prctl and seccomp
 CVE-2017-5753 (Spectre variant 1):        Mitigation: __user pointer sanitization
 CVE-2017-5715 (Spectre variant 2):        Mitigation: Full AMD retpoline, IBPB:
                                          conditional, IBRS_FW, STIBP: conditional, RSB
                                           filling

 run-level 3 Oct 23 11:30

 SPEC is set to: /root/cpu2017-1.0.5/cpu2017-1.1.0
    Filesystem     Type  Size  Used Avail Use% Mounted on
    /dev/sda2      xfs   440G   29G  412G   7% /

 From /sys/devices/virtual/dmi/id
     BIOS:    Dell Inc. 1.0.1 09/21/2019
     Vendor:  Dell Inc.
     Product: PowerEdge C6525
     Product Family: PowerEdge

 Additional information from dmidecode follows.  WARNING: Use caution when you interpret
 this section. The 'dmidecode' program reads system data which is "intended to allow
 hardware to be accurately determined", but the intent may not be met, as there are
 frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
   Memory:
     4x 802C80B3802C 36ASF4G72PZ-3G2E2 32 GB 2 rank 3200
     4x 802C869D802C 36ASF4G72PZ-3G2E2 32 GB 2 rank 3200
     8x 80AD863280AD HMA84GR7CJR4N-XN 32 GB 2 rank 3200

 (End of data from sysinfo program)
```

Compiler Version Notes

```
==============================================================================
C               | 519.lbm_r(base, peak) 538.imagick_r(base, peak)
                | 544.nab_r(base, peak)
------------------------------------------------------------------------------
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
  AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin
------------------------------------------------------------------------------
```

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Compiler Version Notes (Continued)

```
==============================================================================
C++             | 508.namd_r(base, peak) 510.parest_r(base, peak)
------------------------------------------------------------------------------
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
  AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin
------------------------------------------------------------------------------

==============================================================================
C++, C          | 511.povray_r(base, peak) 526.blender_r(base, peak)
------------------------------------------------------------------------------
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
  AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
  AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin
------------------------------------------------------------------------------

==============================================================================
C++, C, Fortran | 507.cactuBSSN_r(base, peak)
------------------------------------------------------------------------------
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
  AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
  AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
  AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin
------------------------------------------------------------------------------

==============================================================================
```


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Compiler Version Notes (Continued)

```
Fortran         | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak)
                | 554.roms_r(base, peak)
------------------------------------------------------------------------------
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
  AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin
------------------------------------------------------------------------------

==============================================================================
Fortran, C      | 521.wrf_r(base, peak) 527.cam4_r(base, peak)
------------------------------------------------------------------------------
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
  AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
  AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin
------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
`clang`

C++ benchmarks:
`clang++`

Fortran benchmarks:
`flang`

Benchmarks using both Fortran and C:
`flang clang`

Benchmarks using both C and C++:
`clang++ clang`

Benchmarks using Fortran, C, and C++:
`clang++ clang flang`

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
526.blender_r: -funsigned-char -D__BOOL_DEFINED -DSPEC_LP64
527.cam4_r: -DSPEC_CASE_FLAG -DSPEC_LP64
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -ffast-math
-march=znver2 -fstruct-layout=3 -mllvm -unroll-threshold=50
-fremap-arrays -mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -z muldefs -lmvec -lamdlibm -ljemalloc
-lflang

C++ benchmarks:
-std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-suppress-fmas -O3 -ffast-math -march=znver2
-mllvm -loop-unswitch-threshold=200000 -mllvm -vector-library=LIBMVEC
-mllvm -unroll-threshold=100 -flv-function-specialization
-mllvm -enable-partial-unswitch -z muldefs -lmvec -lamdlibm
-ljemalloc -lflang

Fortran benchmarks:
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver2
-funroll-loops -Mrecursive -mllvm -vector-library=LIBMVEC -z muldefs
-Kieee -fno-finite-math-only -lmvec -lamdlibm -ljemalloc -lflang

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Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -ffast-math
-march=znver2 -fstruct-layout=3 -mllvm -unroll-threshold=50
-fremap-arrays -mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -funroll-loops -Mrecursive -z muldefs
-Kieee -fno-finite-math-only -lmvec -lamdlibm -ljemalloc -lflang

Benchmarks using both C and C++:
-std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-suppress-fmas -O3 -ffast-math -march=znver2
-fstruct-layout=3 -mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -mllvm -loop-unswitch-threshold=200000
-mllvm -unroll-threshold=100 -mllvm -enable-partial-unswitch -z muldefs
-lmvec -lamdlibm -ljemalloc -lflang

Benchmarks using Fortran, C, and C++:
-std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-suppress-fmas -O3 -ffast-math -march=znver2
-fstruct-layout=3 -mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -mllvm -loop-unswitch-threshold=200000
-mllvm -unroll-threshold=100 -mllvm -enable-partial-unswitch
-funroll-loops -Mrecursive -z muldefs -Kieee -fno-finite-math-only
-lmvec -lamdlibm -ljemalloc -lflang

Peak Compiler Invocation

C benchmarks:
clang

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Peak Compiler Invocation (Continued)

C++ benchmarks:
clang++

Fortran benchmarks:
flang

Benchmarks using both Fortran and C:
flang clang

Benchmarks using both C and C++:
clang++ clang

Benchmarks using Fortran, C, and C++:
clang++ clang flang

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

519.lbm_r: basepeak = yes

538.imagick_r: -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver2 -mno-sse4a -fstruct-layout=5
-mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -lmvec -lamdlibm -ljemalloc
-lflang

544.nab_r: Same as 538.imagick_r

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Peak Optimization Flags (Continued)

C++ benchmarks:

508.namd_r: -std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver2 -flv-function-specialization
-mllvm -unroll-threshold=100
-mllvm -enable-partial-unswitch
-mllvm -loop-unswitch-threshold=200000
-mllvm -vector-library=LIBMVEC
-mllvm -inline-threshold=1000 -lmvec -lamdlibm -ljemalloc
-lflang

510.parest_r: -std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-suppress-fmas -Ofast -march=znver2
-flv-function-specialization -mllvm -unroll-threshold=100
-mllvm -enable-partial-unswitch
-mllvm -loop-unswitch-threshold=200000
-mllvm -vector-library=LIBMVEC
-mllvm -inline-threshold=1000 -lmvec -lamdlibm -ljemalloc
-lflang

Fortran benchmarks:

503.bwaves_r: -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3
-march=znver2 -funroll-loops -Mrecursive
-mllvm -vector-library=LIBMVEC -Kieee
-fno-finite-math-only -lmvec -lamdlibm -ljemalloc
-lflang

549.fotonik3d_r: Same as 503.bwaves_r

554.roms_r: -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -O3 -march=znver2
-funroll-loops -Mrecursive -mllvm -vector-library=LIBMVEC
-Kieee -fno-finite-math-only -lmvec -lamdlibm -ljemalloc

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Peak Optimization Flags (Continued)

554.roms_r (continued):
-lflang

Benchmarks using both Fortran and C:

521.wrf_r: basepeak = yes

527.cam4_r: -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver2 -mno-sse4a -fstruct-layout=5
-mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -O3 -funroll-loops
-Mrecursive -Kieee -fno-finite-math-only -lmvec
-lamdlibm -ljemalloc -lflang

Benchmarks using both C and C++:

511.povray_r: -std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver2 -mno-sse4a -fstruct-layout=5
-mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -mllvm -unroll-threshold=100
-mllvm -enable-partial-unswitch
-mllvm -loop-unswitch-threshold=200000 -lmvec -lamdlibm
-ljemalloc -lflang

526.blender_r: -std=c++98 -flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver2 -mno-sse4a -fstruct-layout=5

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Peak Optimization Flags (Continued)

526.blender_r (continued):
-mllvm -vectorize-memory-aggressively
-mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -vector-library=LIBMVEC
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp -mllvm -inline-threshold=1000
-flv-function-specialization -mllvm -unroll-threshold=100
-mllvm -enable-partial-unswitch
-mllvm -loop-unswitch-threshold=200000 -lmvec -lamdlibm
-ljemalloc -lflang

Benchmarks using Fortran, C, and C++:

507.cactuBSSN_r: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/aocc200-flags-B1-speed-Dell.html
http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-revE7.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/aocc200-flags-B1-speed-Dell.xml
http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-revE7.xml

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