# SPEC CPU®2017 Integer Rate Result

**Tyrone Systems**  
(Test Sponsor: Netweb Pte Ltd)  
DIT400TR-55R/55RL  
(2.10 GHz, Intel Xeon Silver 4208)  

<table>
<thead>
<tr>
<th>SPECrate®2017_int_base</th>
<th>SPECrate®2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>83.8</td>
<td>86.5</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 006042  
**Test Date:** Feb-2020

**Test Sponsor:** Netweb Pte Ltd  
**Hardware Availability:** Sep-2019

**Tested by:** Netweb  
**Software Availability:** Aug-2019

### Hardware

<table>
<thead>
<tr>
<th>Feature</th>
<th>Details</th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU Name</td>
<td>Intel Xeon Silver 4208</td>
</tr>
<tr>
<td>Max MHz</td>
<td>3200</td>
</tr>
<tr>
<td>Nominal</td>
<td>2100</td>
</tr>
<tr>
<td>Enabled</td>
<td>16 cores, 2 chips, 2 threads/core</td>
</tr>
<tr>
<td>Orderable</td>
<td>1, 2 (chip/s)</td>
</tr>
<tr>
<td>Cache L1</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>L2</td>
<td>1 MB I+D on chip per core</td>
</tr>
<tr>
<td>L3</td>
<td>11 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other</td>
<td>None</td>
</tr>
<tr>
<td>Memory</td>
<td>384 GB (12 x 32 GB 2Rx4 PC4-2933Y-R, running at 2400)</td>
</tr>
<tr>
<td>Storage</td>
<td>1 x 480 GB SSD</td>
</tr>
<tr>
<td>Other</td>
<td>None</td>
</tr>
</tbody>
</table>

### Software

<table>
<thead>
<tr>
<th>Feature</th>
<th>Details</th>
</tr>
</thead>
<tbody>
<tr>
<td>OS</td>
<td>CentOS Linux release 7.7.1908 (Core) 3.10.0-1062.el7.x86_64</td>
</tr>
<tr>
<td>Compiler</td>
<td>C/C++: Version 19.0.4.243 of Intel C/C++ Compiler Build 20190416 for Linux; Fortran: Version 19.0.4.243 of Intel Fortran Compiler Build 20190416 for Linux</td>
</tr>
<tr>
<td>Parallel</td>
<td>No</td>
</tr>
<tr>
<td>Firmware</td>
<td>Version V8.101 released Aug-2019</td>
</tr>
<tr>
<td>File System</td>
<td>xfs</td>
</tr>
<tr>
<td>System State</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers</td>
<td>64-bit</td>
</tr>
<tr>
<td>Peak Pointers</td>
<td>32/64-bit</td>
</tr>
<tr>
<td>Other</td>
<td>jemalloc memory allocator V5.0.1</td>
</tr>
<tr>
<td>Power Management</td>
<td>Default</td>
</tr>
</tbody>
</table>
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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>32</td>
<td>805</td>
<td>63.3</td>
<td>809</td>
<td>63.0</td>
<td>808</td>
<td>63.1</td>
<td>32</td>
<td>706</td>
<td>72.2</td>
<td>701</td>
<td>72.7</td>
<td>702</td>
<td>72.6</td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>32</td>
<td>630</td>
<td>71.9</td>
<td>641</td>
<td>70.7</td>
<td>627</td>
<td>72.3</td>
<td>32</td>
<td>574</td>
<td>78.9</td>
<td>574</td>
<td>79.0</td>
<td>574</td>
<td>79.0</td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>32</td>
<td>453</td>
<td>114</td>
<td>450</td>
<td>115</td>
<td>452</td>
<td>114</td>
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<td>114</td>
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<td>115</td>
<td>454</td>
<td>114</td>
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<tr>
<td>520.omnetpp_r</td>
<td>32</td>
<td>719</td>
<td>58.4</td>
<td>721</td>
<td>58.2</td>
<td>715</td>
<td>58.8</td>
<td>32</td>
<td>718</td>
<td>58.4</td>
<td>718</td>
<td>58.5</td>
<td>719</td>
<td>58.4</td>
</tr>
<tr>
<td>523.xalancbmk_r</td>
<td>32</td>
<td>335</td>
<td>101</td>
<td>337</td>
<td>100</td>
<td>333</td>
<td>101</td>
<td>32</td>
<td>318</td>
<td>106</td>
<td>318</td>
<td>106</td>
<td>318</td>
<td>106</td>
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<tr>
<td>525.x264_r</td>
<td>32</td>
<td>383</td>
<td>146</td>
<td>389</td>
<td>144</td>
<td>382</td>
<td>147</td>
<td>32</td>
<td>369</td>
<td>152</td>
<td>369</td>
<td>152</td>
<td>368</td>
<td>152</td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
<td>32</td>
<td>539</td>
<td>68.0</td>
<td>539</td>
<td>68.0</td>
<td>539</td>
<td>68.0</td>
<td>32</td>
<td>539</td>
<td>68.0</td>
<td>539</td>
<td>68.0</td>
<td>539</td>
<td>68.0</td>
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<tr>
<td>541.leela_r</td>
<td>32</td>
<td>861</td>
<td>61.6</td>
<td>861</td>
<td>61.6</td>
<td>848</td>
<td>62.5</td>
<td>32</td>
<td>848</td>
<td>62.5</td>
<td>858</td>
<td>61.7</td>
<td>855</td>
<td>62.0</td>
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<tr>
<td>548.exchange2_r</td>
<td>32</td>
<td>515</td>
<td>163</td>
<td>515</td>
<td>163</td>
<td>512</td>
<td>164</td>
<td>32</td>
<td>516</td>
<td>162</td>
<td>515</td>
<td>163</td>
<td>515</td>
<td>163</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>32</td>
<td>622</td>
<td>55.6</td>
<td>621</td>
<td>55.7</td>
<td>621</td>
<td>55.6</td>
<td>32</td>
<td>621</td>
<td>55.7</td>
<td>622</td>
<td>55.6</td>
<td>622</td>
<td>55.6</td>
</tr>
</tbody>
</table>

**SPECrate®2017_int_base = 83.8**
**SPECrate®2017_int_peak = 86.5**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

SPEC has learned that this result, which used an evaluation compiler, was submitted contrary to the compiler license terms.

Intel has granted a one-time waiver for this result.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor.

For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = 
"/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-
32:/home/cpu2017/je5.0.1-64"
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CPU2017 License: 006042
Test Sponsor: Netweb Pte Ltd
Tested by: Netweb

General Notes

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Fisystem page cache synced and cleared with:
sync; echo 3>/proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7edb1e6e46a485a0011
running on NODE2 Tue Feb 11 06:09:23 2020

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
    model name : Intel(R) Xeon(R) Silver 4208 CPU @ 2.10GHz
    2 "physical id"s (chips)
    32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 8
siblings : 16
physical 0: cores 0 1 2 3 4 5 6 7
physical 1: cores 0 1 2 3 4 5 6 7

From lscpu:
    Architecture: x86_64
    CPU op-mode(s): 32-bit, 64-bit
    Byte Order: Little Endian
    CPU(s): 32
    On-line CPU(s) list: 0-31

(Continued on next page)
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Hardware Availability: Sep-2019
Software Availability: Aug-2019

Platform Notes (Continued)

Thread(s) per core: 2
Core(s) per socket: 8
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Silver 4208 CPU @ 2.10GHz
Stepping: 7
CPU MHz: 799.932
CPU max MHz: 3200.0000
CPU min MHz: 800.0000
BogoMIPS: 4200.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 11264K
NUMA node0 CPU(s): 0-15,24-31
NUMA node1 CPU(s): 16-31
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscl
lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmpref eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg
fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3nowprefetch ebpf cat_13 cdp_13 intel_ppln
intel_pt ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow fwp plpmode etp
vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cmp mpx rd_a
avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt
xsavec xgetbv1 cmp_llc cmp_occup_llc cmp_mbb_total cmp_mbb_local dtherm ida arat pln
pts hwp hwp_act_window hwp_epp hwp_pkg_req pku ospke avx512_vnni md_clear spec_ctrl
intel_stibp flush_l1d arch_capabilities

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7 16 17 18 19 20 21 22 23
node 0 size: 195228 MB
node 0 free: 190600 MB
node 1 cpus: 8 9 10 11 12 13 14 15 24 25 26 27 28 29 30 31
node 1 size: 196608 MB
node 1 free: 191931 MB
node distances:
node 0 1

(Continued on next page)
Platform Notes (Continued)

0:  10  21
1:  21  10

From /proc/meminfo
MemTotal:       394865116 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
centos-release: CentOS Linux release 7.7.1908 (Core)
centos-release-upstream: Derived from Red Hat Enterprise Linux 7.7 (Source)

os-release:
NAME="CentOS Linux"
VERSION="7 (Core)"
ID="centos"
ID_LIKE="rhel fedora"
VERSION_ID="7"
PRETTY_NAME="CentOS Linux 7 (Core)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:centos:centos:7"
redhat-release: CentOS Linux release 7.7.1908 (Core)
system-release: CentOS Linux release 7.7.1908 (Core)
system-release-cpe: cpe:/o:centos:centos:7

uname -a:
Linux NODE2 3.10.0-1062.el7.x86_64 #1 SMP Wed Aug 7 18:08:02 UTC 2019 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2018-3620 (L1 Terminal Fault):        Not affected
Microarchitectural Data Sampling:           Not affected
CVE-2017-5754 (Meltdown):                  Not affected
CVE-2018-3639 (Speculative Store Bypass):  Mitigation: Speculative Store Bypass disabled
                                         via prctl and seccomp
CVE-2017-5753 (Spectre variant 1):         Mitigation: Load fences, __user pointer
                                         sanitization
CVE-2017-5715 (Spectre variant 2):         Mitigation: Full retpoline, IBPB

run-level 3 Feb 11 05:57
SPEC is set to: /home/cpu2017

Filesystem              Type Size Used Avail Use% Mounted on
/dev/mapper/centos-home xfs   392G 149G 243G  39% /home

From /sys/devices/virtual/dmi/id
BIOS:    American Megatrends Inc. V8.101 08/02/2019

(Continued on next page)
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Platform Notes (Continued)

Vendor: Tyrone Systems
Product: DIT400TR-55R
Serial: empty

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
12x Samsung M393A4K40CB2-CVF 32 GB 2 rank 2933

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
| C       | 502.gcc_r(peak)
|-------------------------------------------------------
| Intel(R) C Intel(R) 64 Compiler for applications running on IA-32, Version
| 19.0.4.243 Build 20190416
| Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
| icc: NOTE: The evaluation period for this product ends on 2-nov-2019 UTC.
|-------------------------------------------------------

==============================================================================
| C       | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak)
|--------------------------------------------------------
| 525.x264_r(base, peak) 557.xz_r(base, peak)
|--------------------------------------------------------
| Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
| Version 19.0.4.243 Build 20190416
| Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
| icc: NOTE: The evaluation period for this product ends on 2-nov-2019 UTC.
|--------------------------------------------------------

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|-------------------------------------------------------
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| C       | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak)
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Compiler Version Notes (Continued)

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Version 19.0.4.243 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
icc: NOTE: The evaluation period for this product ends on 2-nov-2019 UTC.

==============================================================================
C++     | 523.xalancbmk_r(peak)
==============================================================================
Intel(R) C++ Intel(R) 64 Compiler for applications running on IA-32, Version
19.0.4.243 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
icpc: NOTE: The evaluation period for this product ends on 2-nov-2019 UTC.

==============================================================================
C++     | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base)
| 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)
==============================================================================
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.243 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
icpc: NOTE: The evaluation period for this product ends on 2-nov-2019 UTC.

==============================================================================
C++     | 523.xalancbmk_r(peak)
==============================================================================
Intel(R) C++ Intel(R) 64 Compiler for applications running on IA-32, Version
19.0.4.243 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
icpc: NOTE: The evaluation period for this product ends on 2-nov-2019 UTC.

==============================================================================
C++     | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base)
| 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)
==============================================================================
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.243 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
icpc: NOTE: The evaluation period for this product ends on 2-nov-2019 UTC.

==============================================================================
Fortran | 548.exchange2_r(base, peak)
(Continued on next page)
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Compiler Version Notes (Continued)

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.4.243 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
ifort: NOTE: The evaluation period for this product ends on 2-nov-2019 UTC.

Base Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.243/linux/compiler/lib/intel64
-lqkmalloc

C++ benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div

(Continued on next page)
**Base Optimization Flags (Continued)**

C++ benchmarks (continued):
- `-qopt-mem-layout-trans=4`
- `-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.243/linux/compiler/lib/intel64`
- `lqkmalloc`

Fortran benchmarks:
- `-W1,-z,muldefs` `-xCORE-AVX512` `-ipo` `-O3` `-no-prec-div`
- `-qopt-mem-layout-trans=4` `-nostandard-realloc-lhs` `-align array32byte`
- `-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.243/linux/compiler/lib/intel64`
- `lqkmalloc`

**Peak Compiler Invocation**

C benchmarks (except as noted below):
```bash
icc -m64 -std=c11
```


C++ benchmarks (except as noted below):
```bash
icpc -m64
```

523.xalancbmk_r: `icpc -m32 -L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.243/linux/compiler/lib/ia32_lin`

Fortran benchmarks:
```bash
ifort -m64
```

**Peak Portability Flags**

500.perlbench_r: `-DSPEC_LP64` `-DSPEC_LINUX_X64`
502.gcc_r: `--D_FILE_OFFSET_BITS=64`
505.mcf_r: `-DSPEC_LP64`
520.omnetpp_r: `-DSPEC_LP64`
523.xalancbmk_r: `--D_FILE_OFFSET_BITS=64` `-DSPEC_LINUX`
525.x264_r: `-DSPEC_LP64`
531.deepsjeng_r: `-DSPEC_LP64`
541.leela_r: `-DSPEC_LP64`
548.exchange2_r: `-DSPEC_LP64`
557.xz_r: `-DSPEC_LP64`
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(2.10 GHz, Intel Xeon Silver 4208)

SPECrate®2017_int_base = 83.8
SPECrate®2017_int_peak = 86.5

CPU2017 License: 006042
Test Sponsor: Netweb Pte Ltd
Tested by: Netweb

Test Date: Feb-2020
Hardware Availability: Sep-2019
Software Availability: Aug-2019

Peak Optimization Flags

C benchmarks:

500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4
-fno-strict-overflow
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.243/linux/compiler/lib/intel64
-lqkmalloc

502.gcc_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4
-L/usr/local/je5.0.1-32/lib -ljemalloc

505.mcf_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.243/linux/compiler/lib/intel64
-lqkmalloc

525.x264_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-alias
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.243/linux/compiler/lib/intel64
-lqkmalloc

557.xz_r: Same as 505.mcf_r

C++ benchmarks:

520.omnetpp_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.243/linux/compiler/lib/intel64
-lqkmalloc

523.xalancbmk_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4
-L/usr/local/je5.0.1-32/lib -ljemalloc

531.deepsjeng_r: Same as 520.omnetpp_r

541.leela_r: Same as 520.omnetpp_r

Fortran benchmarks:

-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.243/linux/compiler/lib/intel64
-lqkmalloc
SPEC CPU®2017 Integer Rate Result

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
DIT400TR-55R/55RL
(2.10 GHz, Intel Xeon Silver 4208)

SPECrate®2017_int_base = 83.8
SPECrate®2017_int_peak = 86.5

CPU2017 License: 006042
Test Sponsor: Netweb Pte Ltd
Tested by: Netweb

Test Date: Feb-2020
Hardware Availability: Sep-2019
Software Availability: Aug-2019

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/TyroneIT-Platform-Settings-V1-CLX-revA.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/TyroneIT-Platform-Settings-V1-CLX-revA.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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