## SPEC CPU®2017 Floating Point Rate Result

**Fujitsu**  
PRIMERGY RX2540 M5, Intel Xeon Gold 6208U, 2.90 GHz

| Copies | 0 | 15.0 | 30.0 | 45.0 | 60.0 | 75.0 | 90.0 | 105 | 115 | 125 | 135 | 145 | 155 | 165 | 175 | 185 | 195 | 205 | 215 | 225 | 235 | 245 | 255 |
|--------|---|------|------|------|------|------|------|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| 503.bwaves_r | 32 | | | | | | | | | | | | | | | | | | | | | | | | | |
| 507.cactuBSSN_r | 32 | | | | | | | | | | | | | | | | | | | | | | | | | |
| 508.namd_r | 32 | 87.7 | 87.7 | 87.7 | 87.7 | 87.7 | 87.7 | 87.7 | 87.7 | 87.7 | 87.7 | 87.7 | 87.7 | 87.7 | 87.7 | 87.7 | 87.7 | 87.7 | 87.7 | 87.7 | 87.7 | 87.7 | 87.7 | 87.7 |
| 510.parest_r | 32 | 78.2 | 78.2 | 78.2 | 78.2 | 78.2 | 78.2 | 78.2 | 78.2 | 78.2 | 78.2 | 78.2 | 78.2 | 78.2 | 78.2 | 78.2 | 78.2 | 78.2 | 78.2 | 78.2 | 78.2 | 78.2 | 78.2 | 78.2 |
| 511.povray_r | 32 | 60.4 | 60.4 | 60.4 | 60.4 | 60.4 | 60.4 | 60.4 | 60.4 | 60.4 | 60.4 | 60.4 | 60.4 | 60.4 | 60.4 | 60.4 | 60.4 | 60.4 | 60.4 | 60.4 | 60.4 | 60.4 | 60.4 | 60.4 |
| 519.lbm_r | 32 | 57.5 | 57.5 | 57.5 | 57.5 | 57.5 | 57.5 | 57.5 | 57.5 | 57.5 | 57.5 | 57.5 | 57.5 | 57.5 | 57.5 | 57.5 | 57.5 | 57.5 | 57.5 | 57.5 | 57.5 | 57.5 | 57.5 | 57.5 |
| 521.wrf_r | 32 | 110 | 110 | 110 | 110 | 110 | 110 | 110 | 110 | 110 | 110 | 110 | 110 | 110 | 110 | 110 | 110 | 110 | 110 | 110 | 110 | 110 | 110 | 110 |
| 538.imagick_r | 32 | 32 | 87.7 | 87.7 | 87.7 | 87.7 | 87.7 | 87.7 | 87.7 | 87.7 | 87.7 | 87.7 | 87.7 | 87.7 | 87.7 | 87.7 | 87.7 | 87.7 | 87.7 | 87.7 | 87.7 | 87.7 | 87.7 | 87.7 |
| 544.nab_r | 32 | 32 | 78.2 | 78.2 | 78.2 | 78.2 | 78.2 | 78.2 | 78.2 | 78.2 | 78.2 | 78.2 | 78.2 | 78.2 | 78.2 | 78.2 | 78.2 | 78.2 | 78.2 | 78.2 | 78.2 | 78.2 | 78.2 | 78.2 |
| 549.fotonik3d_r | 32 | 32 | 60.4 | 60.4 | 60.4 | 60.4 | 60.4 | 60.4 | 60.4 | 60.4 | 60.4 | 60.4 | 60.4 | 60.4 | 60.4 | 60.4 | 60.4 | 60.4 | 60.4 | 60.4 | 60.4 | 60.4 | 60.4 | 60.4 |
| 554.roms_r | 32 | 32 | 118 | 118 | 118 | 118 | 118 | 118 | 118 | 118 | 118 | 118 | 118 | 118 | 118 | 118 | 118 | 118 | 118 | 118 | 118 | 118 | 118 | 118 |

### Hardware

<table>
<thead>
<tr>
<th>CPU Name:</th>
<th>Intel Xeon Gold 6208U</th>
</tr>
</thead>
<tbody>
<tr>
<td>Max MHz:</td>
<td>3900</td>
</tr>
<tr>
<td>Nominal:</td>
<td>2900</td>
</tr>
<tr>
<td>Enabled:</td>
<td>16 cores, 1 chip, 2 threads/core</td>
</tr>
<tr>
<td>Orderable:</td>
<td>1,2 chips</td>
</tr>
<tr>
<td>Cache L1:</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>L2:</td>
<td>1 MB I+D on chip per core</td>
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<tr>
<td>L3:</td>
<td>22 MB I+D on chip per chip</td>
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<tr>
<td>Other:</td>
<td>None</td>
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<tr>
<td>Memory:</td>
<td>384 GB (12 x 32 GB 2Rx4 PC4-2933Y-R)</td>
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<tr>
<td>Storage:</td>
<td>1 x SATA M.2 SSD, 240 GB</td>
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<tr>
<td>Other:</td>
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### Software

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<tr>
<th>OS:</th>
<th>SUSE Linux Enterprise Server 15</th>
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<tr>
<td>Compiler:</td>
<td>C/C++: Version 19.0.4.227 of Intel C/C++</td>
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<tr>
<td></td>
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<td>Fortran: Version 19.0.4.227 of Intel Fortran</td>
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<td>Parallel:</td>
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<td></td>
<td>released Apr-2020</td>
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<td>File System:</td>
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<td>System State:</td>
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<tr>
<td>Peak Pointers:</td>
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<td>Other:</td>
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<tr>
<td>Power Management:</td>
<td>BIOS set to prefer performance at the cost of additional power usage</td>
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SPEC CPU®2017 Floating Point Rate Result

Fujitsu

PRIMERGY RX2540 M5, Intel Xeon Gold 6208U, 2.90 GHz

CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu

Test Date: Mar-2020
Hardware Availability: Feb-2020
Software Availability: May-2019

Results Table

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</tbody>
</table>

SPECrate®2017_fp_base = 105
SPECrate®2017_fp_peak = Not Run

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Kernel Boot Parameter set with: nohz_full=1-31
Process tuning settings:
  echo 10000000 > /proc/sys/kernel/sched_min_granularity_ns

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
  LD_LIBRARY_PATH = "/home/Benchmark/speccpu2017-1.1.0/lib/intel64"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5

(Continued on next page)
# SPEC CPU®2017 Floating Point Rate Result

## General Notes (Continued)

- Transparent Huge Pages enabled by default
- Prior to runcpu invocation
- Filesystem page cache synced and cleared with:
  ```
  sync; echo 3 > /proc/sys/vm/drop_caches
  ```
- runcpu command invoked through numactl i.e.:
  ```
  numactl --interleave=all runcpu <etc>
  ```
- NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
- Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
- Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

## Platform Notes

- BIOS configuration:
  - Patrol Scrub = Disabled
  - WR CRC feature Control = Disabled
  - Fan Control = Full

- Sysinfo program /home/Benchmark/speccpu2017-1.1.0/bin/sysinfo
  ```
  Rev: r6365 of 2019-08-21 295195f888a3d7edbl1e6e46a485a0001
  running on RX2540M5_CLXR Mon Mar 9 18:25:53 2020
  ```

- SUT (System Under Test) info as seen by some common utilities.
  For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

- From /proc/cpuinfo
  ```
  model name : Intel(R) Xeon(R) Gold 6208U CPU @ 2.90GHz
  1 "physical id"s (chips)
  32 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 16
  siblings : 32
  physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
  ```

- From lscpu:
  ```
  Architecture: x86_64
  CPU op-mode(s): 32-bit, 64-bit
  Byte Order: Little Endian
  CPU(s): 32
  On-line CPU(s) list: 0-31
  Thread(s) per core: 2
  Core(s) per socket: 16
  ```

(Continued on next page)
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PRIMERGY RX2540 M5, Intel Xeon Gold 6208U, 2.90 GHz

**SPECrate®2017_fp_base = 105**

**SPECrate®2017_fp_peak = Not Run**

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<tr>
<td>Test Sponsor:</td>
<td>Fujitsu</td>
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<tr>
<td>Tested by:</td>
<td>Fujitsu</td>
</tr>
<tr>
<td>Test Date:</td>
<td>Mar-2020</td>
</tr>
<tr>
<td>Hardware Availability:</td>
<td>Feb-2020</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>May-2019</td>
</tr>
</tbody>
</table>

**Platform Notes (Continued)**

- Socket(s): 1
- NUMA node(s): 2
- Vendor ID: GenuineIntel
- CPU family: 6
- Model: 85
- Model name: Intel(R) Xeon(R) Gold 6208U CPU @ 2.90GHz
- Stepping: 7
- CPU MHz: 2900.000
- CPU max MHz: 3900.0000
- CPU min MHz: 1200.0000
- BogoMIPS: 5800.00
- Virtualization: VT-x
- L1d cache: 32K
- L1i cache: 32K
- L2 cache: 1024K
- L3 cache: 22528K
- NUMA node0 CPU(s): 0-3,8-11,16-19,24-27
- NUMA nodel CPU(s): 4-7,12-15,20-23,28-31
- Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3 invpcid_single intel_pplin ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl xsaveopt xsavec xalr64 xsaveprec xsaves cqm_llc cqm_occmap_llc cqm_mbb_total cqm_mbb_local dtherm ida arat pni pts hwp hwp_act_window hwp_epp hwp_pkg_req pku ospke avx512_vnni flush_l1d arch_capabilities

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

- available: 2 nodes (0-1)
- node 0 cpus: 0 1 2 3 8 9 10 11 16 17 18 19 24 25 26 27
- node 0 size: 191976 MB
- node 0 free: 191507 MB
- node 1 cpus: 4 5 6 7 12 13 14 15 20 21 22 23 28 29 30 31
- node 1 size: 193292 MB
- node 1 free: 192960 MB
- node distances:
  - node 0 1
    - 0: 10 11
    - 1: 11 10

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PRIMERGY RX2540 M5, Intel Xeon Gold 6208U, 2.90 GHz

SPEC®2017 fp_base = 105
SPEC®2017 fp_peak = Not Run

CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu

Platform Notes (Continued)

From /proc/meminfo
MemTotal: 394514732 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
os-release:
NAME="SLES"
VERSION="15"
VERSION_ID="15"
PRETTY_NAME="SUSE Linux Enterprise Server 15"
ID="sles"
ID_LIKE="suse"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:15"

uname -a:
Linux RX2540M5_CLXR 4.12.14-25.28-default #1 SMP Wed Jan 16 20:00:47 UTC 2019
(dd6077c) x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: No status reported
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

run-level 3 Mar 9 18:12

SPEC is set to: /home/Benchmark/speccpu2017-1.1.0
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda5 xfs 191G 113G 78G 60% /home

From /sys/devices/virtual/dmi/id
BIOS: FUJITSU // American Megatrends Inc. V5.0.0.14 R1.18.0 for D3384-B1x
02/10/2020
Vendor: FUJITSU
Product: PRIMERGY RX2540 M5
Product Family: SERVER
Serial: YMSQXXXXXX

Additional information from dmidecode follows. WARNING: Use caution when you interpret
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PRIMERGY RX2540 M5, Intel Xeon Gold 6208U, 2.90 GHz

SPEC CPU®2017 Floating Point Rate Result
Copyright 2017-2020 Standard Performance Evaluation Corporation

SPECrate®2017_fp_base = 105
SPECrate®2017_fp_peak = Not Run

Platform Notes (Continued)
this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
  12x NO DIMM NO DIMM
  12x Samsung M393A4K40CB2-CVF 32 GB 2 rank 2933, configured at 2934

(End of data from sysinfo program)

Compiler Version Notes

C       | 519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)
-------------------------------
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

C++     | 508.namd_r(base) 510.parest_r(base)
-------------------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

C++, C   | 511.povray_r(base) 526.blender_r(base)
-------------------------------
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

C++, C, Fortran | 507.cactuBSSN_r(base)
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Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.4.227 Build 20190416

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Fujitsu
PRIMERGY RX2540 M5, Intel Xeon Gold 6208U, 2.90 GHz

Compiler Version Notes (Continued)

Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
Intel (R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
  64, Version 19.0.4.227 Build 20190416
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------------------------------------------------------------------------------
Fortran         | 503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)
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Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
  64, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
Fortran, C      | 521.wrf_r(base) 527.cam4_r(base)
------------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
  64, Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
  Version 19.0.4.227 Build 20190416
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

Base Compiler Invocation

C benchmarks:
  icc -m64 -std=c11

C++ benchmarks:
  icpc -m64

Fortran benchmarks:
  ifort -m64

Benchmarks using both Fortran and C:
  ifort -m64 icc -m64 -std=c11

Benchmarks using both C and C++:
  icpc -m64 icc -m64 -std=c11 ifort -m64

Benchmarks using Fortran, C, and C++:
  icpc -m64 icc -m64 -std=c11 ifort -m64
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<thead>
<tr>
<th>SPECrate®2017_fp_base</th>
<th>105</th>
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<tbody>
<tr>
<td>SPECrate®2017_fp_peak</td>
<td>Not Run</td>
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**CPU2017 License:** 19  
**Test Sponsor:** Fujitsu  
**Tested by:** Fujitsu  
**Test Date:** Mar-2020  
**Hardware Availability:** Feb-2020  
**Software Availability:** May-2019

### Base Portability Flags

```
503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64
```

### Base Optimization Flags

**C benchmarks:**  
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=4

**C++ benchmarks:**  
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=4

**Fortran benchmarks:**  
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs  
-align array32byte

**Benchmarks using both Fortran and C:**  
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs  
-align array32byte

**Benchmarks using both C and C++:**  
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=4

**Benchmarks using Fortran, C, and C++:**  
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-mem-layout-trans=4 -auto -nostandard-realloc-lhs  
-align array32byte
Fujitsu
PRIMERGY RX2540 M5, Intel Xeon Gold 6208U, 2.90 GHz

SPECrate®2017_fp_base = 105
SPECrate®2017_fp_peak = Not Run

CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu

Test Date: Mar-2020
Hardware Availability: Feb-2020
Software Availability: May-2019

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.0-CSL-RevE.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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