



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Dell Inc.

SPECSpeed®2017\_int\_base = 9.57

SPECSpeed®2017\_int\_peak = 9.66

CPU2017 License: 55

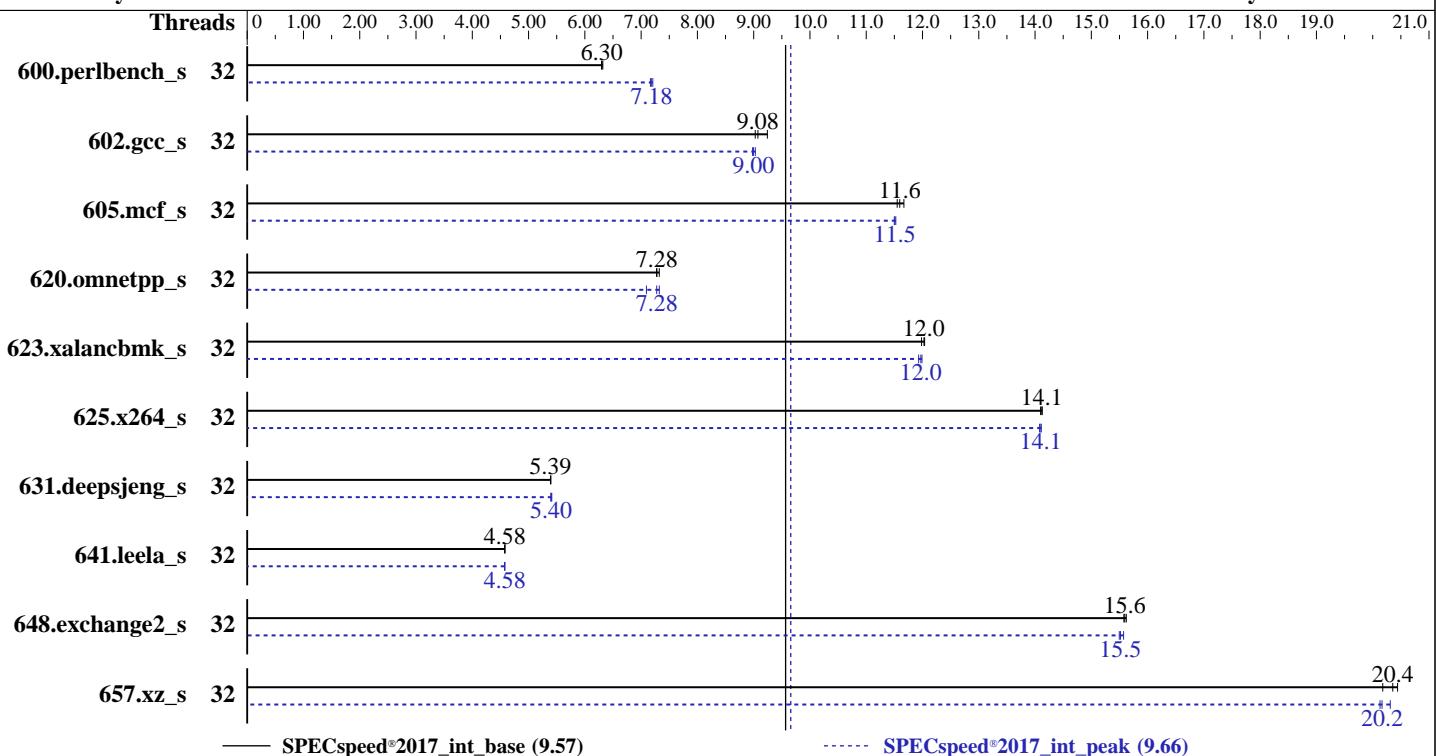
Test Date: Mar-2020

Test Sponsor: Dell Inc.

Hardware Availability: Feb-2020

Tested by: Dell Inc.

Software Availability: Feb-2020



Hardware		Software	
CPU Name:	Intel Xeon Gold 6226R	OS:	Ubuntu 18.04.4 LTS
Max MHz:	3900	Compiler:	kernel 4.15.0-91-generic
Nominal:	2900	Parallel:	C/C++: Version 19.0.4.227 of Intel C/C++ Compiler Build 20190416 for Linux;
Enabled:	32 cores, 2 chips	Firmware:	Fortran: Version 19.0.4.227 of Intel Fortran Compiler Build 20190416 for Linux
Orderable:	1,2 chips	File System:	Yes
Cache L1:	32 KB I + 32 KB D on chip per core	System State:	Version 2.7.3 released Mar-2020
L2:	1 MB I+D on chip per core	Base Pointers:	ext4
L3:	22 MB I+D on chip per chip	Peak Pointers:	Run level 5 (multi-user)
Other:	None	Other:	64-bit
Memory:	384 GB (12 x 32 GB 2Rx8 PC4-2933V-R, running at 2933)	Power Management:	64-bit jemalloc memory allocator V5.0.1
Storage:	1 x 480 GB SATA SSD		BIOS set to prefer performance at the cost of additional power usage.
Other:	None		



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## Results Table

Benchmark	Base								Peak							
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	32	281	6.32	282	6.30	<b>282</b>	<b>6.30</b>	32	246	7.21	248	7.17	<b>247</b>	<b>7.18</b>		
602.gcc_s	32	<b>439</b>	<b>9.08</b>	441	9.03	431	9.24	32	441	9.03	<b>443</b>	<b>9.00</b>	443	8.98		
605.mcf_s	32	405	11.7	<b>407</b>	<b>11.6</b>	409	11.6	32	<b>410</b>	<b>11.5</b>	410	11.5	411	11.5		
620.omnetpp_s	32	223	7.32	224	7.27	<b>224</b>	<b>7.28</b>	32	230	7.09	223	7.32	<b>224</b>	<b>7.28</b>		
623.xalancbmk_s	32	118	12.0	118	12.0	<b>118</b>	<b>12.0</b>	32	118	12.0	<b>118</b>	<b>12.0</b>	119	11.9		
625.x264_s	32	125	14.1	<b>125</b>	<b>14.1</b>	125	14.1	32	125	14.1	125	14.1	<b>125</b>	<b>14.1</b>		
631.deepsjeng_s	32	266	5.39	266	5.39	<b>266</b>	<b>5.39</b>	32	265	5.41	<b>265</b>	<b>5.40</b>	266	5.39		
641.leela_s	32	373	4.58	373	4.58	<b>373</b>	<b>4.58</b>	32	<b>373</b>	<b>4.58</b>	373	4.58	373	4.58		
648.exchange2_s	32	<b>189</b>	<b>15.6</b>	189	15.6	188	15.6	32	189	15.6	190	15.5	<b>189</b>	<b>15.5</b>		
657.xz_s	32	<b>304</b>	<b>20.4</b>	306	20.2	302	20.4	32	304	20.3	<b>307</b>	<b>20.2</b>	307	20.1		

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH =
    "/home/ODM-SPECcpu2017-194/cpu2017/lib/intel64:/home/ODM-SPECcpu2017-194
    /cpu2017/je5.0.1-64"
OMP_STACKSIZE = "192M"
```

## General Notes

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

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## General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

sync; echo 3 > /proc/sys/vm/drop\_caches

runcpu command invoked through numactl i.e.:

numactl --interleave=all runcpu <etc>

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

## Platform Notes

BIOS settings:

Sub NUMA Cluster enabled

Virtualization Technology disabled

System Profile set to Custom

CPU Performance set to Maximum Performance

C States set to Autonomous

C1E disabled

Uncore Frequency set to Dynamic

Energy Efficiency Policy set to Performance

Memory Patrol Scrub set to standard

Logical Processor disabled

CPU Interconnect Bus Link Power Management disabled

PCI ASPM L1 Link Power Management disabled

UPI Prefetch enabled

LLC Prefetch disabled

Dead Line LLC Alloc enabled

Director AtoS disabled

Sysinfo program /home/ODM-SPECcpu2017-194/cpu2017/bin/sysinfo

Rev: r6365 of 2019-08-21 295195f888a3d7edb1e6e46a485a0011

running on intel-sut Wed Mar 25 21:22:25 2020

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Gold 6226R CPU @ 2.90GHz

2 "physical id"s (chips)

32 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

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## Platform Notes (Continued)

```
cpu cores : 16
siblings : 16
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
```

From lscpu:

```
Architecture:           x86_64
CPU op-mode(s):        32-bit, 64-bit
Byte Order:             Little Endian
CPU(s):                32
On-line CPU(s) list:  0-31
Thread(s) per core:   1
Core(s) per socket:   16
Socket(s):             2
NUMA node(s):          2
Vendor ID:             GenuineIntel
CPU family:            6
Model:                 85
Model name:            Intel(R) Xeon(R) Gold 6226R CPU @ 2.90GHz
Stepping:               7
CPU MHz:               3554.707
BogoMIPS:              5800.00
Virtualization:        VT-x
L1d cache:             32K
L1i cache:             32K
L2 cache:              1024K
L3 cache:              22528K
NUMA node0 CPU(s):    0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30
NUMA node1 CPU(s):    1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
                       pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                       lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtTopology nonstop_tsc cpuid
                       aperfmpf perf_pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
                       xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
                       avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_13 cdp_13
                       invpcid_single intel_ppin ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi
                       flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid cqmp
                       mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd
                       avx512bw avx512vl xsaveopt xsavec xgetbv1 xsavec cqmp_llc cqmp_occu_llc cqmp_mbmm_total
                       cqmp_mbmm_local dtherm ida arat pln pts pku ospke avx512_vnni md_clear flush_l1d
                       arch_capabilities
```

```
/proc/cpuinfo cache data
cache size : 22528 KB
```

From numactl --hardware    WARNING: a numactl 'node' might or might not correspond to a physical chip.

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## Platform Notes (Continued)

```
available: 2 nodes (0-1)
node 0 cpus: 0 2 4 6 8 10 12 14 16 18 20 22 24 26 28 30
node 0 size: 192054 MB
node 0 free: 191568 MB
node 1 cpus: 1 3 5 7 9 11 13 15 17 19 21 23 25 27 29 31
node 1 size: 193532 MB
node 1 free: 193041 MB
node distances:
node    0    1
 0:   10   21
 1:   21   10

From /proc/meminfo
MemTotal:           394840608 kB
HugePages_Total:        0
Hugepagesize:         2048 kB

/usr/bin/lsb_release -d
Ubuntu 18.04.4 LTS

From /etc/*release* /etc/*version*
debian_version: buster/sid
os-release:
  NAME="Ubuntu"
  VERSION="18.04.4 LTS (Bionic Beaver)"
  ID=ubuntu
  ID_LIKE=debian
  PRETTY_NAME="Ubuntu 18.04.4 LTS"
  VERSION_ID="18.04"
  HOME_URL="https://www.ubuntu.com/"
  SUPPORT_URL="https://help.ubuntu.com/"

uname -a:
Linux intel-sut 4.15.0-91-generic #92-Ubuntu SMP Fri Feb 28 11:09:48 UTC 2020 x86_64
x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

itlb_multihit:                                KVM: Vulnerable
CVE-2018-3620 (L1 Terminal Fault):            Not affected
Microarchitectural Data Sampling:               Not affected
CVE-2017-5754 (Meltdown):                     Not affected
CVE-2018-3639 (Speculative Store Bypass):     Mitigation: Speculative Store Bypass disabled
                                                via prctl and seccomp
CVE-2017-5753 (Spectre variant 1):            Mitigation: usercopy/swapgs barriers and __user
                                                pointer sanitization
CVE-2017-5715 (Spectre variant 2):             Mitigation: Enhanced IBRS, IBPB: conditional,
```

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## Platform Notes (Continued)

tsx\_async\_abort: RSB filling  
Mitigation: TSX disabled

run-level 5 Mar 25 21:21

SPEC is set to: /home/ODM-SPECcpu2017-194/cpu2017  
Filesystem Type Size Used Avail Use% Mounted on  
/dev/sda2 ext4 439G 71G 346G 17% /

From /sys/devices/virtual/dmi/id  
BIOS: Dell Inc. 2.7.3 03/25/2020  
Vendor: Dell Inc.  
Product: PowerEdge C6420  
Product Family: PowerEdge  
Serial: 1234567

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:  
8x 00AD00B300AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933  
1x 00AD063200AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933  
3x 00AD069D00AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933  
4x Not Specified Not Specified

(End of data from sysinfo program)

## Compiler Version Notes

=====

C	600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base,   peak) 625.x264_s(base, peak) 657.xz_s(base, peak)
---	--

=====

-----  
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.4.227 Build 20190416  
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.  
-----

=====

C++	620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak)   631.deepsjeng_s(base, peak) 641.leela_s(base, peak)
-----	--

=====

-----  
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,  
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## Compiler Version Notes (Continued)

=====

Fortran | 648.exchange2\_s(base, peak)

-----  
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
64, Version 19.0.4.227 Build 20190416  
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

-----

## Base Compiler Invocation

C benchmarks:

icc -m64 -std=c11

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

## Base Portability Flags

600.perlbench\_s: -DSPEC\_LP64 -DSPEC\_LINUX\_X64  
602.gcc\_s: -DSPEC\_LP64  
605.mcf\_s: -DSPEC\_LP64  
620.omnetpp\_s: -DSPEC\_LP64  
623.xalancbmk\_s: -DSPEC\_LP64 -DSPEC\_LINUX  
625.x264\_s: -DSPEC\_LP64  
631.deepsjeng\_s: -DSPEC\_LP64  
641.leela\_s: -DSPEC\_LP64  
648.exchange2\_s: -DSPEC\_LP64  
657.xz\_s: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=4 -qopenmp -DSPEC\_OPENMP  
-L/usr/local/jet5.0.1-64/lib -ljemalloc

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## Base Optimization Flags (Continued)

C++ benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=4  
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64  
-lqkmalloc
```

Fortran benchmarks:

```
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4  
-nostandard-realloc-lhs
```

## Peak Compiler Invocation

C benchmarks:

```
icc -m64 -std=c11
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```
600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2  
-xCORE-AVX512 -qopt-mem-layout-trans=4 -ipo -O3  
-no-prec-div -DSPEC_SUPPRESS_OPENMP -qopenmp  
-DSPEC_OPENMP -fno-strict-overflow  
-L/usr/local/jet5.0.1-64/lib -ljemalloc
```

```
602.gcc_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2  
-xCORE-AVX512 -qopt-mem-layout-trans=4 -ipo -O3  
-no-prec-div -DSPEC_SUPPRESS_OPENMP  
-L/usr/local/jet5.0.1-64/lib -ljemalloc
```

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## Peak Optimization Flags (Continued)

605.mcf\_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo  
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4  
-DSPEC\_SUPPRESS\_OPENMP -qopenmp -DSPEC\_OPENMP  
-L/usr/local/je5.0.1-64/lib -ljemalloc

625.x264\_s: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=4 -qopenmp -DSPEC\_OPENMP  
-L/usr/local/je5.0.1-64/lib -ljemalloc

657.xz\_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2  
-xCORE-AVX512 -qopt-mem-layout-trans=4 -ipo -O3  
-no-prec-div -DSPEC\_SUPPRESS\_OPENMP -qopenmp  
-DSPEC\_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:

620.omnetpp\_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo  
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4  
-DSPEC\_SUPPRESS\_OPENMP  
-L/usr/local/IntelCompiler19/compilers\_and\_libraries\_2019.4.227/linux/compiler/lib/intel64  
-lqkalloc

623.xalancbmk\_s: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=4  
-L/usr/local/IntelCompiler19/compilers\_and\_libraries\_2019.4.227/linux/compiler/lib/intel64  
-lqkalloc

631.deepsjeng\_s: Same as 623.xalancbmk\_s

641.leela\_s: Same as 623.xalancbmk\_s

Fortran benchmarks:

-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4  
-nostandard-realloc-lhs

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic19.0ul-official-linux64.2019-07-09.html>  
<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-revE9.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic19.0ul-official-linux64.2019-07-09.xml>  
<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-revE9.xml>



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