NEC Corporation

Express5800/R120h-1M (Intel Xeon Gold 6252)

SPEC CPU®2017 Integer Speed Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

SPECspeed®2017_int_base = 9.57
SPECspeed®2017_int_peak = 9.71

NEC Corporation

CPU2017 License: 9006
Test Sponsor: NEC Corporation
Tested by: NEC Corporation

Test Date: Apr-2020
Hardware Availability: Dec-2019
Software Availability: Sep-2019

Hardware

CPU Name: Intel Xeon Gold 6252
Max MHz: 3700
Nominal: 2100
Enabled: 48 cores, 2 chips, 2 threads/core
Orderable: 2 chips
Cache L1: 32 KB I + 32 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 35.75 MB I+D on chip per chip
Other: None
Memory: 384 GB (24 x 16 GB 2Rx8 PC4-2933Y-R)
Storage: 1 x 1 TB SATA, 7200 RPM, RAID 0
Other: None

Software

OS: Red Hat Enterprise Linux Server release 7.7 (Maipo)
Kernel 3.10.0-1062.1.1.el7.x86_64
Compiler: C/C++: Version 19.0.4.227 of Intel C/C++
Compiler Build 20190416 for Linux;
Fortran: Version 19.0.4.227 of Intel Fortran
Compiler Build 20190416 for Linux
Parallel: Yes
Firmware: NEC BIOS Version U32 v2.22 11/13/2019 released
File System: ext4
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS set to prefer performance at the cost of additional power usage.
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Results Table

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<th>Seconds</th>
<th>Ratio</th>
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<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>96</td>
<td>290</td>
<td>6.13</td>
<td>287</td>
<td>6.18</td>
<td>287</td>
<td>6.18</td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>96</td>
<td>446</td>
<td>8.92</td>
<td>449</td>
<td>8.88</td>
<td>442</td>
<td>9.01</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>96</td>
<td>410</td>
<td>11.5</td>
<td>410</td>
<td>11.5</td>
<td>96</td>
<td>407</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>96</td>
<td>187</td>
<td>8.71</td>
<td>189</td>
<td>8.64</td>
<td>188</td>
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</tr>
<tr>
<td>623.xalanchmk_s</td>
<td>96</td>
<td>124</td>
<td>11.4</td>
<td>125</td>
<td>11.3</td>
<td>125</td>
<td>11.4</td>
</tr>
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<td>625.x264_s</td>
<td>96</td>
<td>129</td>
<td>13.7</td>
<td>129</td>
<td>13.7</td>
<td>129</td>
<td>13.6</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>96</td>
<td>279</td>
<td>5.14</td>
<td>279</td>
<td>5.14</td>
<td>278</td>
<td>5.15</td>
</tr>
<tr>
<td>641.leela_s</td>
<td>96</td>
<td>394</td>
<td>4.33</td>
<td>394</td>
<td>4.33</td>
<td>394</td>
<td>4.33</td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>96</td>
<td>199</td>
<td>14.8</td>
<td>199</td>
<td>14.8</td>
<td>199</td>
<td>14.8</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>96</td>
<td>271</td>
<td>22.8</td>
<td>271</td>
<td>22.8</td>
<td>269</td>
<td>23.0</td>
</tr>
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</table>

SPECspeed®2017_int_base = 9.57
SPECspeed®2017_int_peak = 9.71

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.5
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
 sync; echo 3 > /proc/sys/vm/drop_caches

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation

(Continued on next page)
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CPU2017 License: 9006
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General Notes (Continued)

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

BIOS Settings:
Thermal Configuration: Maximum Cooling
Workload Profile: General Peak Frequency Compute
Memory Patrol Scrubbing: Disabled
LLC Dead Line Allocation: Disabled
LLC Prefetch: Enabled
Enhanced Processor Performance: Enabled
Workload Profile: Custom
Advanced Memory Protection: Advanced ECC Support
NUMA Group Size Optimization: Flat

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7edeb1e6e46a485a0011
running on r120h1m Wed Apr 8 15:58:15 2020

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6252 CPU @ 2.10GHz
  2 "physical id"s (chips)
  96 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 24
siblings : 48
physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 25 26 27 28 29
physical 1: cores 0 1 2 3 4 5 6 9 10 11 12 13 16 17 18 19 20 21 24 25 26 27 28 29

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 96
On-line CPU(s) list: 0-95
Thread(s) per core: 2
Core(s) per socket: 24
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel

(Continued on next page)
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<tr>
<td>CPU family:</td>
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<tr>
<td>Model:</td>
</tr>
<tr>
<td>Model name:</td>
</tr>
<tr>
<td>Stepping:</td>
</tr>
<tr>
<td>CPU MHz:</td>
</tr>
<tr>
<td>BogoMIPS:</td>
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<tr>
<td>Virtualization:</td>
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<tr>
<td>L1d cache:</td>
</tr>
<tr>
<td>L1i cache:</td>
</tr>
<tr>
<td>L2 cache:</td>
</tr>
<tr>
<td>L3 cache:</td>
</tr>
<tr>
<td>NUMA node0 CPU(s):</td>
</tr>
<tr>
<td>NUMA node1 CPU(s):</td>
</tr>
</tbody>
</table>

Flags: fpu vme de pse tsc msr pae mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good ntopologia nonstop_tsc aperfmpref eagerfpu npi pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch epb cat_13 cdp_l3 invpcid_single intel_pinn intel_pt ssbd mba ibrs ibp ibst ibsp ibrs enhanced tpr_shadow vmmi flexpriority ept vpid fsgsbase tsc_adjust bni hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1 cqm_llc cqm_occup_llc cqm_mbb_total cqm_mbb_local dtherm ida arat pin pts pku ospke avx512_vnni md_clear spec_ctrl intel_stibp flush_l1d arch_capabilities |

/proc/cpuinfo cache data  
  cache size : 36608 KB

From numactl --hardware  
**WARNING:** a numactl 'node' might or might not correspond to a physical chip.  
**available:** 2 nodes (0-1)  
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71  
node 0 size: 196264 MB  
node 0 free: 191430 MB  
node 1 cpus: 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 72 73 74 75 76 77 78 79 80 81 82 83 84 85 86 87 88 89 90 91 92 93 94 95  
node 1 size: 196607 MB  
node 1 free: 192065 MB  
node distances:  
node 0 1  
0: 10 21  
1: 21 10

From /proc/meminfo  
MemTotal: 395914376 KB

(Continued on next page)
Platform Notes (Continued)

- HugePages_Total:    0
- Hugepagesize:   2048 kB

From /etc/*release* /etc/*version*
  os-release:
    NAME="Red Hat Enterprise Linux Server"
    VERSION="7.7 (Maipo)"
    ID="rhel"
    ID_LIKE="fedora"
    VARIANT="Server"
    VARIANT_ID="server"
    VERSION_ID="7.7"
    PRETTY_NAME="Red Hat Enterprise Linux Server 7.7 (Maipo)"
  redhat-release: Red Hat Enterprise Linux Server release 7.7 (Maipo)
  system-release: Red Hat Enterprise Linux Server release 7.7 (Maipo)
  system-release-cpe: cpe:/o:redhat:enterprise_linux:7.7:ga:server

uname -a:
  Linux r120h1m 3.10.0-1062.1.1.el7.x86_64 #1 SMP Tue Aug 13 18:39:59 UTC 2019 x86_64
  x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

- CVE-2018-3620 (L1 Terminal Fault): Not affected
- Microarchitectural Data Sampling: Not affected
- CVE-2017-5754 (Meltdown): Not affected
- CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
- CVE-2017-5753 (Spectre variant 1): Mitigation: Load fences, usercopy/swapgs barriers and __user pointer sanitization
- CVE-2017-5715 (Spectre variant 2): Mitigation: Full retpoline, IBPB

run-level 3 Apr 8 15:52

SPEC is set to: /home/cpu2017
  Filesystem  Type  Size  Used Avail Use% Mounted on
  /dev/sda3     ext4  908G  114G  749G  14% /

From /sys/devices/virtual/dmi/id
  BIOS:    NEC U32 11/13/2019
  Vendor:  NEC
  Product: Express5800/R120h-1M
  Serial:  JPN0084094

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are...
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Platform Notes (Continued)

frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.  
Memory:  
24x HPE P03050-091 16 GB 2 rank 2933

(End of data from sysinfo program)

Compiler Version Notes

******************************************************************************
| C       | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak) 657.xz_s(base, peak) |
******************************************************************************

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.4.227 Build 20190416  
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

******************************************************************************
| C++     | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base, peak) 641.leela_s(base, peak) |
******************************************************************************

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.4.227 Build 20190416  
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

******************************************************************************
| Fortran | 648.exchange2_s(base, peak) |
******************************************************************************

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.0.4.227 Build 20190416  
Copyright (C) 1985-2019 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:  
icc -m64 -std=c11

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64
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Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

Fortran benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs

Peak Compiler Invocation

C benchmarks:
icc -m64 -std=c11

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64
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Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX512 -qopt-mem-layout-trans=4 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -fno-strict-overflow
-L/usr/local/je5.0.1-64/lib -ljemalloc

602.gcc_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX512 -qopt-mem-layout-trans=4 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

605.mcf_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=4
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

625.x264_s: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

657.xz_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -O2
-xCORE-AVX512 -qopt-mem-layout-trans=4 -ipo -O3
-no-prec-div -DSPEC_SUPPRESS_OPENMP -qopenmp
-DSPEC_OPENMP -L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:

620.omnetpp_s: basepeak = yes

623.xalancbmk_s: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2019.4.227/linux/compiler/lib/intel64
-lqkmalloc

631.deepsjeng_s: Same as 623.xalancbmk_s

641.leela_s: Same as 623.xalancbmk_s

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**Peak Optimization Flags (Continued)**

Fortran benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-mem-layout-trans=4  
-nostandard-realloc-lhs

The flags files that were used to format this result can be browsed at:
http://www.spec.org/cpu2017/flags/NEC-Platform-Settings-V1.2-R120h-RevE.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/NEC-Platform-Settings-V1.2-R120h-RevE.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.0 on 2020-04-08 02:58:14-0400.  