## SPEC CPU®2017 Integer Rate Result

**Dell Inc.**

PowerEdge C6420 (Intel Xeon Gold 6208U, 2.90 GHz)

### CPU2017 License:
55

**Test Sponsor:** Dell Inc.

**Test Date:** May-2020

**Hardware Availability:** Feb-2020

**Tested by:** Dell Inc.

**Software Availability:** Apr-2020

### SPECrate®2017_int_base = 115

### SPECrate®2017_int_peak = 120

### Hardware

<table>
<thead>
<tr>
<th>CPU Name</th>
<th>Intel Xeon Gold 6208U</th>
</tr>
</thead>
<tbody>
<tr>
<td>Max MHz</td>
<td>3900</td>
</tr>
<tr>
<td>Nominal</td>
<td>2900</td>
</tr>
<tr>
<td>Enabled</td>
<td>16 cores, 1 chip, 2 threads/core</td>
</tr>
<tr>
<td>Orderable</td>
<td>1 chips</td>
</tr>
<tr>
<td>Cache L1</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>Cache L2</td>
<td>1 MB I+D on chip per core</td>
</tr>
<tr>
<td>Cache L3</td>
<td>22 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other</td>
<td>None</td>
</tr>
<tr>
<td>Memory</td>
<td>192 GB (6 x 32 GB 2Rx8 PC4-2933V-R, running at 2933)</td>
</tr>
<tr>
<td>Storage</td>
<td>1 x 480 GB SATA SSD</td>
</tr>
<tr>
<td>Other</td>
<td>None</td>
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</table>

### Software

<table>
<thead>
<tr>
<th>OS</th>
<th>CentOS Linux 8.1.1911</th>
</tr>
</thead>
<tbody>
<tr>
<td>Compiler</td>
<td>C/C++: Version 19.1.1.217 of Intel C/C++ Compiler for Linux; Fortran: Version 19.1.1.217 of Intel Fortran Compiler for Linux</td>
</tr>
<tr>
<td>Parallel</td>
<td>No</td>
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<tr>
<td>Firmware</td>
<td>Version 2.7.3 released Mar-2020</td>
</tr>
<tr>
<td>File System</td>
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</tr>
<tr>
<td>System State</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers</td>
<td>64-bit</td>
</tr>
<tr>
<td>Peak Pointers</td>
<td>32/64-bit</td>
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<tr>
<td>Other</td>
<td>jemalloc memory allocator V5.0.1</td>
</tr>
</tbody>
</table>

### Power Management

- BIOS set to prefer performance at the cost of additional power usage

### Test Results

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>SPECrate®2017_int_base</th>
<th>SPECrate®2017_int_peak</th>
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<td>32</td>
<td>76.9</td>
<td>90.0</td>
</tr>
<tr>
<td>gcc_r</td>
<td>32</td>
<td>89.2</td>
<td>103</td>
</tr>
<tr>
<td>mcf_r</td>
<td>32</td>
<td>74.1</td>
<td>196</td>
</tr>
<tr>
<td>omnetpp_r</td>
<td>32</td>
<td></td>
<td></td>
</tr>
<tr>
<td>xalancbmk_r</td>
<td>32</td>
<td>154</td>
<td></td>
</tr>
<tr>
<td>x264_r</td>
<td>32</td>
<td>234</td>
<td>243</td>
</tr>
<tr>
<td>deepsjeng_r</td>
<td>32</td>
<td>91.6</td>
<td></td>
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<tr>
<td>leela_r</td>
<td>32</td>
<td>85.7</td>
<td></td>
</tr>
<tr>
<td>exchange2_r</td>
<td>32</td>
<td>219</td>
<td></td>
</tr>
<tr>
<td>xz_r</td>
<td>32</td>
<td>67.5</td>
<td>68.5</td>
</tr>
</tbody>
</table>

---

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SPECCPU®2017 Integer Rate Result

Dell Inc.
PowerEdge C6420 (Intel Xeon Gold 6208U, 2.90 GHz)

SPECrate®2017_int_base = 115
SPECrate®2017_int_peak = 120

Results Table

<table>
<thead>
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<th>Seconds</th>
<th>Ratio</th>
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<th>Ratio</th>
<th>Copies</th>
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<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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</tr>
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<td>77.3</td>
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<td>508</td>
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<td>502</td>
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<tr>
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<tr>
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</tr>
<tr>
<td>548.exchange2_r</td>
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</tr>
<tr>
<td>557.xz_r</td>
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<td>67.5</td>
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<td>67.7</td>
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<td>504</td>
<td>68.5</td>
<td></td>
<td></td>
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<td></td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The inconsistent Compiler version information under Compiler Version section is due to a discrepancy in Intel Compiler.

The correct version of C/C++ compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux

The correct version of Fortran compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor.

For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

LD_LIBRARY_PATH =
"/dev/shm/cpu2017-ic19.1u1/lib/intel64:/dev/shm/cpu2017-ic19.1u1/lib/ia32:/dev/shm/cpu2017-ic19.1u1/je5.0.1-32"
MALLOCP_CONF = "retain:true"
Dell Inc.

PowerEdge C6420 (Intel Xeon Gold 6208U, 2.90 GHz)

<table>
<thead>
<tr>
<th>SPECrate®2017_int_base = 115</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate®2017_int_peak = 120</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 55
**Test Sponsor:** Dell Inc.
**Tested by:** Dell Inc.

**Test Date:** May-2020
**Hardware Availability:** Feb-2020
**Software Availability:** Apr-2020

---

### General Notes

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM memory using Redhat Enterprise Linux 7.5

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3>/proc/sys/vm/drop_caches
```

runcpu command invoked through numactl i.e.:

```
numactl --interleave=all runcpu <etc>
```

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5


---

### Platform Notes

BIOS settings:

Sub NUMA Cluster enabled
Virtualization Technology disabled
System Profile set to Custom
CPU Performance set to Maximum Performance
C States set to Autonomous
C1E disabled
Uncore Frequency set to Dynamic
Energy Efficiency Policy set to Performance
Memory Patrol Scrub set to standard
Logical Processor enabled
CPU Interconnect Bus Link Power Management disabled
PCI ASPM L1 Link Power Management disabled
UPI Prefetch enabled
LLC Prefetch disabled
Dead Line LLC Alloc enabled
Directory AtoS disabled

Sysinfo program /dev/shm/cpu2017-ic19.1u1/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7edb1e6e46a485a0011
running on localhost.localdomain Tue May 19 11:08:37 2020

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see

https://www.spec.org/cpu2017/Docs/config.html#sysinfo

(Continued on next page)
SPEC CPU®2017 Integer Rate Result
Copyright 2017-2020 Standard Performance Evaluation Corporation

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CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: May-2020
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Platform Notes (Continued)

From /proc/cpuinfo
    model name : Intel(R) Xeon(R) Gold 6208U CPU @ 2.90GHz
        1 "physical id"s (chips)
        32 "processors"
    cores, siblings (Caution: counting these is hw and system dependent. The following
    excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
        cpu cores : 16
        siblings : 32
        physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15

From lscpu:
    Architecture: x86_64
    CPU op-mode(s): 32-bit, 64-bit
    Byte Order: Little Endian
    CPU(s): 32
    On-line CPU(s) list: 0-31
    Thread(s) per core: 2
    Core(s) per socket: 16
    Socket(s): 1
    NUMA node(s): 2
    Vendor ID: GenuineIntel
    CPU family: 6
    Model: 85
    Model name: Intel(R) Xeon(R) Gold 6208U CPU @ 2.90GHz
    Stepping: 7
    CPU MHz: 3096.258
    CPU max MHz: 3900.000
    CPU min MHz: 1200.000
    BogoMIPS: 5800.00
    Virtualization: VT-x
    L1d cache: 32K
    L1i cache: 32K
    L2 cache: 1024K
    L3 cache: 22528K
    NUMA node0 CPU(s): 0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30
    NUMA node1 CPU(s): 1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31
    Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
    pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
    lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
    aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
    xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
    xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3
    invpcid_single intel_patin ssbd mba ibrs ibpb ibrs_enhanced tpr_shadow vnmi
    flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm
    cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd
    avx512bw avx512vl xsaveopt xsavecp xsaves cqm_llc cqm_occop_llc cqm_mbm_total
    cqm_mbm_local dtherm ida arat pln pts pku ospke avx512_vni md_clear flush_lld

(Continued on next page)
Dell Inc.  
PowerEdge C6420 (Intel Xeon Gold 6208U, 2.90 GHz)  
SPEC CPU®2017 Integer Rate Result

**SPECrate®2017_int_base = 115**  
**SPECrate®2017_int_peak = 120**

CPU2017 License: 55  
Test Sponsor: Dell Inc.  
Test Date: May-2020  
Tested by: Dell Inc.  
Hardware Availability: Feb-2020  
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---

**Platform Notes (Continued)**

```
arch_capabilities

 proc/cpuinfo cache data
  cache size : 22528 KB

 From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
 physical chip.
 available: 2 nodes (0-1)
 node 0 cpus: 0 2 4 6 8 10 12 14 16 18 20 22 24 26 28 30
 node 0 size: 95280 MB
 node 0 free: 94772 MB
 node 1 cpus: 1 3 5 7 9 11 13 15 17 19 21 23 25 27 29 31
 node 1 size: 96764 MB
 node 1 free: 85752 MB
 node distances:
    node 0 1
      0:  10  11
      1:  11  10

 From /proc/meminfo
 MemTotal:       196653716 kB
 HugePages_Total:       0
 Hugepagesize:       2048 kB

 From /etc/*release*/etc/*version*
 centos-release: CentOS Linux release 8.1.1911 (Core)
 centos-release-upstream: Derived from Red Hat Enterprise Linux 8.1 (Source)
 os-release:
    NAME="CentOS Linux"
    VERSION="8 (Core)"
    ID="centos"
    ID_LIKE="rhel fedora"
    VERSION_ID="8"
    PLATFORM_ID="platform:el8"
    PRETTY_NAME="CentOS Linux 8 (Core)"
    ANSI_COLOR="0;31"
 redhat-release: CentOS Linux release 8.1.1911 (Core)
 system-release: CentOS Linux release 8.1.1911 (Core)
 system-release-cpe: cpe:/o:centos:centos:8

 uname -a:
 Linux localhost.localdomain 4.18.0-147.5.1.el8_1.x86_64 #1 SMP Wed Feb 5 02:00:39 UTC
 2020 x86_64 x86_64 x86_64 GNU/Linux

 Kernel self-reported vulnerability status:
 iitlb_multihit: Processor vulnerable
```

(Continued on next page)
Dell Inc.
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SPECRate®2017_int_base = 115
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CPU2017 License: 55
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Tested by: Dell Inc.
Test Date: May-2020
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Platform Notes (Continued)

CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swapgs barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
tsx_async_abort:
Mitigation: Clear CPU buffers; SMT vulnerable

run-level 3 May 19 10:51

SPEC is set to: /dev/shm/cpu2017-ic19.1u1
Filesystem Type Size Used Avail Use% Mounted on
tmpfs tmpfs 94G 4.2G 90G 5% /dev/shm

BIOS: Dell Inc. 2.7.3 03/25/2020
Vendor: Dell Inc.
Product: PowerEdge C6420
Product Family: PowerEdge
Serial: 1234567

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
3x 00AD00B300AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933
1x 00AD063200AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933
2x 00AD069D00AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933
10x Not Specified Not Specified

(End of data from sysinfo program)

Compiler Version Notes

C | 502.gcc_r(peak)

Intel(R) C Compiler for applications running on IA-32, Version 2021.1 NextGen
Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

(Continued on next page)
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Compiler Version Notes (Continued)

==============================================================================
| C       | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak) |
|         | 525.x264_r(base, peak) 557.xz_r(base) |

Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

------------------------------------------------------------------------------

| C       | 500.perlbench_r(peak) 557.xz_r(peak) |

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

------------------------------------------------------------------------------

| C       | 502.gcc_r(peak) |

Intel(R) C Compiler for applications running on IA-32, Version 2021.1 NextGen
Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

------------------------------------------------------------------------------

| C       | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak) |
|         | 525.x264_r(base, peak) 557.xz_r(base) |

Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

------------------------------------------------------------------------------

| C       | 500.perlbench_r(peak) 557.xz_r(peak) |

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

------------------------------------------------------------------------------

| C       | 502.gcc_r(peak) |

Intel(R) C Compiler for applications running on IA-32, Version 2021.1 NextGen
Build 20200304

(Continued on next page)
Compiler Version Notes (Continued)

Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
| C       | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)         |
|         | 525.x264_r(base, peak) 557.xz_r(base)                            |
==============================================================================

Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
| C       | 500.perlbench_r(peak) 557.xz_r(peak)                             |
==============================================================================

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
| C++     | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak)            |
|         | 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)              |
==============================================================================

Intel(R) C++ Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
| Fortran | 548.exchange2_r(base, peak)                                      |
==============================================================================

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc
**SPEC CPU®2017 Integer Rate Result**

**Dell Inc.**

PowerEdge C6420 (Intel Xeon Gold 6208U, 2.90 GHz)

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<thead>
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<td>115</td>
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<tr>
<th>CPU2017 License</th>
<th>Test Date</th>
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<tr>
<td>55</td>
<td>May-2020</td>
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<table>
<thead>
<tr>
<th>Test Sponsor</th>
<th>Hardware Availability</th>
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<tr>
<td>Dell Inc.</td>
<td>Feb-2020</td>
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<th>Software Availability</th>
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<td>Apr-2020</td>
</tr>
</tbody>
</table>

**Base Compiler Invocation (Continued)**

Fortran benchmarks:
ifort

**Base Portability Flags**

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

**Base Optimization Flags**

C benchmarks:
- m64 -qnextgen -std=c11
- W1, -plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs
- xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse -funroll-loops
- fuse-ld=gold -qopt-mem-layout-trans=4
- L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin
- lqkmalloc

C++ benchmarks:
- m64 -qnextgen -W1, -plugin-opt=-x86-branches-within-32B-boundaries
- W1,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse
- funroll-loops -fuse-lde=gold -qopt-mem-layout-trans=4
- L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin
- lqkmalloc

Fortran benchmarks:
- m64 -Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs
- xCORE-AVX512 -O3 -ipo -no-prec-div -qopt-mem-layout-trans=4
- nostandard-realloc-lhs -align array32byte -auto
- mbranches-within-32B-boundaries
- L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin
- lqkmalloc
Peak Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:
500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-strict-overflow
-mbranches-within-32B-boundaries
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin
-ljemalloc

502.gcc_r: -m32
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/ia32_lin
-std=gnu89
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -flto
-Ofast(pass 1) -O3 -ffast-math -qnextgen -fuse-ld=gold
-qopt-mem-layout-trans=4 -L/usr/local/jemalloc32-5.0.1/lib
-ljemalloc

(Continued on next page)
**SPEC CPU®2017 Integer Rate Result**

**Dell Inc.**

PowerEdge C6420 (Intel Xeon Gold 6208U, 2.90 GHz)

| SPECrate®2017_int_base = 115 |
| SPECrate®2017_int_peak = 120 |

**CPU2017 License:** 55  
**Test Date:** May-2020

**Test Sponsor:** Dell Inc.  
**Hardware Availability:** Feb-2020

**Tested by:** Dell Inc.  
**Software Availability:** Apr-2020

### Peak Optimization Flags (Continued)

505.mcf_r: basepeak = yes

525.x264_r: -m64 -gnextgen -std=c11  
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries  
-Wl,-z,muldefs -xCORE-AVX512 -flto -O3 -ffast-math  
-fuse-ld=gold -gopt-mem-layout-trans=4 -fno-alias  
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin  
-lqkmalloc

557.xz_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-gopt-mem-layout-trans=4 -mbranches-within-32B-boundaries  
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin  
-lqkmalloc

C++ benchmarks:

520.omnetpp_r: basepeak = yes

523.xalancbmk_r: basepeak = yes

531.deepsjeng_r: basepeak = yes

541.leela_r: basepeak = yes

Fortran benchmarks:

548.exchange2_r: basepeak = yes

The flags files that were used to format this result can be browsed at:


You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2017/flags/Intel-ic19.1u1-official-linux64_revA.xml


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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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