# SPEC CPU®2017 Integer Rate Result

**Lenovo Global Technology**

ThinkSystem SR590

(3.20 GHz, Intel Xeon Silver 4215R)

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**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

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<table>
<thead>
<tr>
<th>Copies</th>
<th>SPECrate®2017_int_base</th>
<th>SPECrate®2017_int_peak</th>
</tr>
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<tbody>
<tr>
<td>32</td>
<td>121</td>
<td>Not Run</td>
</tr>
</tbody>
</table>

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**Test Date:** Jun-2020  
**Hardware Availability:** Mar-2020  
**Software Availability:** Apr-2020

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### Hardware

- **CPU Name:** Intel Xeon Silver 4215R  
- **Max MHz:** 4000  
- **Nominal:** 3200  
- **Enabled:** 16 cores, 2 chips, 2 threads/core  
- **Orderable:** 1.2 chips  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **L2:** 1 MB I+D on chip per core  
- **L3:** 11 MB I+D on chip per chip  
- **Other:** None  
- **Memory:** 192 GB (12 x 16 GB 2Rx8 PC4-2933Y-R, running at 2400)  
- **Storage:** 1 x 960 GB SATA SSD  
- **Other:** None  

### Software

- **OS:** SUSE Linux Enterprise Server 15 SP1 (x86_64)  
  Kernel 4.12.14-195-default  
- **Compiler:** C/C++: Version 19.1.1.217 of Intel  
  C/C++ Compiler for Linux;  
  Fortran: Version 19.1.1.217 of Intel Fortran  
  Compiler for Linux  
- **Parallel:** No  
- **Firmware:** Lenovo BIOS Version TEE155L 2.61 released May-2020  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** Not Applicable  
- **Other:** None  
- **Power Management:** BIOS set to prefer performance at the cost of additional power usage
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Results Table

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Compiler Notes

The inconsistent Compiler version information under Compiler Version section is due to a discrepancy in Intel Compiler. The correct version of C/C++ compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux The correct version of Fortran compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = 
"/home/cpu2017-1.1.0-ic19.1.1/lib/intel64:/home/cpu2017-1.1.0-ic19.1.1/lib/ia32:/home/cpu2017-1.1.0-ic19.1.1/je5.0.1-32"
MALLOC_CONF = "retain:true"
## Lenovo Global Technology

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<table>
<thead>
<tr>
<th>SPECrate®2017_int_base =</th>
<th>121</th>
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### General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM  
memory using Redhat Enterprise Linux 8.0  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesytem page cache synced and cleared with:
```bash
    sync; echo 3> /proc/sys/vm/drop_caches
```

runcpu command invoked through numactl i.e.:
```bash
    numactl --interleave=all runcpu <etc>
```

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3640 (Spectre variant 3a) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2018-3639 (Spectre variant 4) is mitigated in the system as tested and documented.

### Platform Notes

**BIOS configuration:**  
Choose Operating Mode set to Maximum Performance and then set it to Custom Mode  
MONITOR/MWAIT set to Enable  
SNC set to Enable  
Stale AtoS set to Enable  
LLC dead line alloc set to Disable

Sysinfo program /home/cpu2017-1.1.0-ic19.1.1/bin/sysinfo  
Rev: r6365 of 2019-08-21 295195f888a3d7ed6b1e6e46a485a0011  
runtime on linux-cnti Tue Jun 2 13:42:48 2020

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

- model name : Intel(R) Xeon(R) Silver 4215R CPU @ 3.20GHz  
- 2 "physical id"s (chips)  
- 32 "processors"  
- cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  - cpu cores : 8  
  - siblings : 16  
  - physical 0: cores 0 1 2 3 4 5 6 7  
  - physical 1: cores 0 1 2 3 4 5 6 7

(Continued on next page)
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SPECrate®2017_int_base = 121
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Platform Notes (Continued)

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
Address sizes: 46 bits physical, 48 bits virtual
CPU(s): 32
On-line CPU(s) list: 0-31
Thread(s) per core: 2
Core(s) per socket: 8
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Silver 4215R CPU @ 3.20GHz
Stepping: 7
CPU MHz: 3200.000
CPU max MHz: 4000.0000
CPU min MHz: 1000.0000
BogoMIPS: 6400.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 11264K
NUMA node0 CPU(s): 0-7,16-23
NUMA node1 CPU(s): 8-15,24-31
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperf mrperf pni pclmulqdq dtes64monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xptr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3
invpcid_single intel_pstate ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi
flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm
cqm mpx rdtd_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd
avx512bw avx512vl xsaveopt xsaves cqm_llc cqm_ocrop_llc cqm_mbm_total
cqm_mbm_local dtherm ida arat pln pts pku ospke avx512_vnni md_clear flush_l1d
arch_capabilities

/proc/cpuinfo cache data
cache size : 11264 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
available: 2 nodes (0-1)

(Continued on next page)
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### Platform Notes (Continued)

- node 0 cpus: 0 1 2 3 4 5 6 7 16 17 18 19 20 21 22 23
- node 0 size: 96385 MB
- node 0 free: 95976 MB
- node 1 cpus: 8 9 10 11 12 13 14 15 24 25 26 27 28 29 30 31
- node 1 size: 96735 MB
- node 1 free: 96299 MB
- node distances:
  - node 0: 10 21
  - node 1: 21 10

From /proc/meminfo:
- MemTotal: 197755552 kB
- HugePages_Total: 0
- Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*:
- os-release:
  - NAME="SLES"
  - VERSION="15-SP1"
  - VERSION_ID="15.1"
  - PRETTY_NAME="SUSE Linux Enterprise Server 15 SP1"
  - ID="sles"
  - ID_LIKE="suse"
  - ANSI_COLOR="0;32"
  - CPE_NAME="cpe:/o:suse:sles:15:sp1"

`uname -a`:
- x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
- CVE-2018-3620 (L1 Terminal Fault): Not affected
- Microarchitectural Data Sampling: Not affected
- CVE-2017-5754 (Meltdown): Not affected
- CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
- CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
- CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

run-level 3 Jun 2 13:41

SPEC is set to: /home/cpu2017-1.1.0-ic19.1.1

<table>
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<tr>
<th>Filesystem</th>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>/dev/sda3</td>
<td>xfs</td>
<td>743G</td>
<td>69G</td>
<td>674G</td>
<td>10%</td>
<td>/</td>
</tr>
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</table>

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| SPECrate®2017_int_base = 121 |
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Platform Notes (Continued)

From /sys/devices/virtual/dmi/id
BIOS: Lenovo -[TEE155L-2.61]- 05/20/2020
Vendor: Lenovo
Product: ThinkSystem SR590 -[7X98RCZ000]-
Product Family: ThinkSystem
Serial: 1234567890

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
4x NO DIMM NO DIMM
12x SK Hynix HMA82GR7CJR8N-WM 16 GB 2 rank 2933

(End of data from sysinfo program)

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Compiler Version Notes

C
500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base)
525.x264_r(base) 557.xz_r(base)

Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

C++
520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)
541.ieela_r(base)

Intel(R) C++ Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

Fortran
548.exchange2_r(base)

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
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**Base Compiler Invocation**

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

**Base Portability Flags**

500.perlb基准_r: -DSPEC_LP64 -DSPEC_LINUX_X64  
502.gcc基准_r: -DSPEC_LP64  
505.mcf基准_r: -DSPEC_LP64  
520.omnetpp基准_r: -DSPEC_LP64  
523.xalanc基准mk_r: -DSPEC_LP64 -DSPEC_LINUX  
525.x264基准_r: -DSPEC_LP64  
531.deepsjeng基准_r: -DSPEC_LP64  
541.leela基准_r: -DSPEC_LP64  
548.exchange2基准_r: -DSPEC_LP64  
557.xz基准_r: -DSPEC_LP64

**Base Optimization Flags**

C benchmarks:
-m64 -qnextgen -std=c11  
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs  
-xCORE-AVX512 -O3 -ffast-math -ftlo -mfpmath=sse -funroll-loops -fuse-ld=gold -qopt-mem-layout-trans=4  
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin -lqkmalloc

C++ benchmarks:
-m64 -qnextgen -Wl,-plugin-opt=-x86-branches-within-32B-boundaries  
-Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -ftlo -mfpmath=sse -funroll-loops -fuse-ld=gold -qopt-mem-layout-trans=4  
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin -lqkmalloc

Fortran benchmarks:
-m64 -Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs  
-xCORE-AVX512 -O3 -ipo -no-prec-div -qopt-mem-layout-trans=4

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Base Optimization Flags (Continued)

Fortran benchmarks (continued):
- nostandard-realloc-lhs -align array32byte -auto
- mbranches-within-32B-boundaries
- L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin
- lqkmalloc

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic19.1u1-official-linux64_revA.xml
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-H.xml

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