**Lenovo Global Technology**

**ThinkSystem SR650**

(3.40 GHz, Intel Xeon Gold 6246R)

---

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology  
**Test Date:** Jun-2020  
**Hardware Availability:** Mar-2020  
**Software Availability:** Apr-2020

---

**SPECrate®2017_int_base = 262**  
**SPECrate®2017_int_peak = Not Run**

---

### Hardware

- **CPU Name:** Intel Xeon Gold 6246R  
- **Max MHz:** 4100  
- **Nominal:** 3400  
- **Enabled:** 32 cores, 2 chips, 2 threads/core  
- **Orderable:** 1,2 chips  
- **Cache L1:** 32 KB I + 32 KB D on chip per core  
- **L2:** 1 MB I+D on chip per core  
- **L3:** 35.75 MB I+D on chip per chip  
- **Memory:** 768 GB (24 x 32 GB 2Rx4 PC4-2933Y-R)  
- **Storage:** 1 x 800 GB SATA SSD  
- **Other:** None

---

### Software

- **OS:** SUSE Linux Enterprise Server 15 SP1 (x86_64)  
- **Kernel:** 4.12.14-195-default  
- **Compiler:** C/C++: Version 19.1.1.217 of Intel C/C++  
- **Compiler for Linux:** Fortran: Version 19.1.1.217 of Intel Fortran  
- **Firmware:** Lenovo BIOS Version IVE155L 2.61 released May-2020  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** Not Applicable  
- **Power Management:** BIOS set to prefer performance at the cost of additional power usage

---

### SPEC CPU®2017 Integer Rate Result

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>0</th>
<th>30</th>
<th>60</th>
<th>90</th>
<th>120</th>
<th>150</th>
<th>180</th>
<th>210</th>
<th>240</th>
<th>270</th>
<th>300</th>
<th>330</th>
<th>360</th>
<th>390</th>
<th>420</th>
<th>450</th>
<th>480</th>
<th>510</th>
<th>540</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>64</td>
<td>174</td>
<td>174</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>64</td>
<td>296</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>64</td>
<td>164</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>64</td>
<td>448</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>523.xalancbmk_r</td>
<td>64</td>
<td>360</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>524.x264_r</td>
<td>64</td>
<td>532</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
<td>64</td>
<td>207</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>541.leela_r</td>
<td>64</td>
<td>195</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>64</td>
<td>496</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>557.xz_r</td>
<td>64</td>
<td>151</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

---

**SPECrate®2017_int_base (262)**
## Lenovo Global Technology

### ThinkSystem SR650
(3.40 GHz, Intel Xeon Gold 6246R)

**SPECrate®2017_int_base =** 262

**SPECrate®2017_int_peak =** Not Run

### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Base Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Peak Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>64</td>
<td>586</td>
<td>174</td>
<td>587</td>
<td>174</td>
<td>585</td>
<td>174</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>64</td>
<td>441</td>
<td>206</td>
<td>440</td>
<td>206</td>
<td>442</td>
<td>205</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>64</td>
<td>232</td>
<td>447</td>
<td>231</td>
<td>448</td>
<td>231</td>
<td>448</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>64</td>
<td>513</td>
<td>164</td>
<td>512</td>
<td>164</td>
<td>511</td>
<td>164</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>523.xalanbmk_r</td>
<td>64</td>
<td>188</td>
<td>359</td>
<td>188</td>
<td>360</td>
<td>187</td>
<td>361</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>525.x264_r</td>
<td>64</td>
<td>212</td>
<td>529</td>
<td>209</td>
<td>535</td>
<td>211</td>
<td>532</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
<td>64</td>
<td>356</td>
<td>206</td>
<td>353</td>
<td>208</td>
<td>353</td>
<td>207</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>541.leela_r</td>
<td>64</td>
<td>533</td>
<td>199</td>
<td>544</td>
<td>195</td>
<td>544</td>
<td>195</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>64</td>
<td>342</td>
<td>491</td>
<td>338</td>
<td>496</td>
<td>338</td>
<td>496</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>557.xz_r</td>
<td>64</td>
<td>457</td>
<td>151</td>
<td>456</td>
<td>152</td>
<td>456</td>
<td>151</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**SPECrate®2017_int_base =** 262

**SPECrate®2017_int_peak =** Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Compiler Notes

The inconsistent Compiler version information under Compiler Version section is due to a discrepancy in Intel Compiler. The correct version of C/C++ compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux

The correct version of Fortran compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:

LD_LIBRARY_PATH =
    "/home/cpu2017-1.1.0-ic19.1.1/lib/intel64:/home/cpu2017-1.1.0-ic19.1.1/lib/ia32:
    /home/cpu2017-1.1.0-ic19.1.1/je5.0.1-32"
MALLOC_CONF = "retain:true"
```

---

Page 2
**SPEC CPU®2017 Integer Rate Result**

**Lenovo Global Technology**

ThinkSystem SR650  
(3.40 GHz, Intel Xeon Gold 6246R)

<table>
<thead>
<tr>
<th>SPECrate®2017_int_base = 262</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate®2017_int_peak = Not Run</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 9017  
**Test Date:** Jun-2020

**Test Sponsor:** Lenovo Global Technology  
**Hardware Availability:** Mar-2020

**Tested by:** Lenovo Global Technology  
**Software Availability:** Apr-2020

### General Notes

- Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0
- Transparent Huge Pages enabled by default
- Prior to runcpu invocation
- Filesystem page cache synced and cleared with:
  - `sync; echo 3>/proc/sys/vm/drop_caches`
  - `runcpu command invoked through numactl i.e.: numactl --interleave=all runcpu <etc>`

**NA:** The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

**Yes:** The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

**Yes:** The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

### Platform Notes

- **BIOS configuration:**
  - Choose Operating Mode set to Maximum Performance and then set it to Custom Mode
  - MONITOR/MWAIT set to Enable
  - SNC set to Enable
  - DCU Streamer Prefetcher set to Disable
  - LLC dead line alloc set to Disable

- **Sysinfo program /home/cpu2017-1.1.0-ic19.1.1/bin/sysinfo**
  - Rev: r6365 of 2019-08-21 295195f888a3d7edbe6e46a485a0011
  - running on linux-xpyz Tue Jun 9 18:14:15 2020

- **SUT (System Under Test) info as seen by some common utilities.**
  - For more information on this section, see [https://www.spec.org/cpu2017/Docs/config.html#sysinfo](https://www.spec.org/cpu2017/Docs/config.html#sysinfo)

- **From /proc/cpuinfo**
  - model name : Intel(R) Xeon(R) Gold 6246R CPU @ 3.40GHz
  - 2 "physical id"s (chips)
  - 64 "processors"
  - cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
    - cpu cores : 16
    - siblings : 32
    - physical 0: cores 1 2 3 5 6 9 10 12 13 16 18 20 21 24 27 29
    - physical 1: cores 1 2 3 6 9 12 13 16 17 18 19 20 21 26 27 29

- **From lscpu:**
  - Architecture: x86_64
  - CPU op-mode(s): 32-bit, 64-bit

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR650
(3.40 GHz, Intel Xeon Gold 6246R)

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

SPECrate®2017_int_base = 262
SPECrate®2017_int_peak = Not Run

Test Date: Jun-2020
Hardware Availability: Mar-2020
Software Availability: Apr-2020

Platform Notes (Continued)

Byte Order: Little Endian
Address sizes: 46 bits physical, 48 bits virtual
CPU(s): 64
On-line CPU(s) list: 0-63
Thread(s) per core: 2
Core(s) per socket: 16
Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6246R CPU @ 3.40GHz
Stepping: 7
CPU MHZ: 3400.000
CPU max MHZ: 4100.0000
CPU min MHZ: 1200.0000
BogoMIPS: 6800.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 36608K
NUMA node0 CPU(s): 0-2, 5, 6, 9, 10, 13, 32-34, 37, 38, 41, 42, 45
NUMA node1 CPU(s): 3, 4, 7, 8, 11, 12, 14, 15, 35, 36, 39, 40, 43, 44, 46, 47
NUMA node2 CPU(s): 16-18, 20, 23-25, 29, 48-50, 52, 55-57, 61
NUMA node3 CPU(s): 19, 21, 22, 26-28, 30, 31, 51, 53, 54, 58-60, 62, 63
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmerf pni pclmulqdq dtes64 monitor tm2 ssse3 sdbg fma cx16
xtrpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx vlda rcrand lahff_lm abm 3nowprefetch cpuid_fault epb cat_l3 cdp_l3
invpcid_single intel_pin ssbd mba ibrs ibbp stibp ibrs_enhanced tpr_shadow vnmi
flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm
cqm mpx rdt_a avx512f avx512dq rsseed adx smap clflushopt clwb intel_pt avx512cd
avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occuppy cqm_mbb_total
cqm_mbb_local dtherm ida arat pln pts pku ospke avx512_vnni md_clear flush_l1d
arch_capabilities

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0 1 2 5 6 9 10 13 32 33 34 37 38 41 42 45
node 0 size: 193122 MB

(Continued on next page)
Platform Notes (Continued)

node 0 free: 192829 MB
node 1 cpus: 3 4 7 8 11 12 14 15 35 36 40 43 44 46 47
node 1 size: 193532 MB
node 1 free: 193308 MB
node 2 cpus: 16 17 18 20 23 24 25 29 48 49 50 52 55 56 57 61
node 2 size: 193532 MB
node 2 free: 193000 MB
node 3 cpus: 19 21 22 26 27 28 30 31 51 53 54 58 59 60 62 63
node 3 size: 193531 MB
node 3 free: 193301 MB
node distances:
node   0   1   2   3
0:  10  11  21  21
1:  11  10  21  21
2:  21  21  10  11
3:  21  21  11  10

From /proc/meminfo
MemTotal:       792289272 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*

os-release:
NAME="SLES"
VERSION="15-SP1"
VERSION_ID="15.1"
PRETTY_NAME="SUSE Linux Enterprise Server 15 SP1"
ID="sles"
ID_LIKE="suse"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:15:sp1"

uname -a:
Linux linux-xpyz 4.12.14-195-default #1 SMP Tue May 7 10:55:11 UTC 2019 (8fba516)
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

(Continued on next page)
Platform Notes (Continued)

run-level 3 Jun 9 18:10

SPEC is set to: /home/cpu2017-1.1.0-ic19.1.1

Filesystem Type Size Used Avail Use% Mounted on
/dev/sda3 xfs 737G 63G 675G 9% /

From /sys/devices/virtual/dmi/id
BIOS: Lenovo -[IVE155L-2.61]- 05/20/2020
Vendor: Lenovo
Product: ThinkSystem SR650 -[7X05RCZ000]-
Product Family: ThinkSystem
Serial: 1234567890

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
24x Samsung M393A4K40CB2-CVF 32 GB 2 rank 2933

(End of data from sysinfo program)

Compiler Version Notes

================================================================================
C | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base)
 | 525.x264_r(base) 557.xz_r(base)
================================================================================

Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

================================================================================
C++ | 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)
 | 541.leela_r(base)
================================================================================

Intel(R) C++ Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

================================================================================
Fortran | 548.exchange2_r(base)
================================================================================

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR650
(3.40 GHz, Intel Xeon Gold 6246R)

SPECrated 2017_int_base = 262
SPECrated 2017_int_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jun-2020
Hardware Availability: Mar-2020
Software Availability: Apr-2020

Compiler Version Notes (Continued)
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Base Portability Flags

500.perlbm_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-m64 -mcodegen -std=c11
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs
-xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse -funroll-loops
-fuse-ld=gold -qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin
-lqkmalloc

C++ benchmarks:
-m64 -mcodegen -Wl,-plugin-opt=-x86-branches-within-32B-boundaries

(Continued on next page)
## Lenovo Global Technology

**ThinkSystem SR650**  
(3.40 GHz, Intel Xeon Gold 6246R)

<table>
<thead>
<tr>
<th>SPECrate®2017_int_base =</th>
<th>262</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate®2017_int_peak =</td>
<td>Not Run</td>
</tr>
</tbody>
</table>

### CPU2017 License:
9017

### Test Sponsor:
Lenovo Global Technology

### Tested by:
Lenovo Global Technology

### Test Date:
Jun-2020

### Hardware Availability:
Mar-2020

### Software Availability:
Apr-2020

## Base Optimization Flags (Continued)

**C++ benchmarks (continued):**
- `-Wl,-z,muldefs`  
- `-xCORE-AVX512`  
- `-O3`  
- `-ffast-math`  
- `-fflto`  
- `-mfpmath=sse`  
- `-funroll-loops`  
- `-fuse-ld=gold`  
- `-qopt-mem-layout-trans=4`  
- `-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin`  
- `-lqkmalloc`

**Fortran benchmarks:**
- `-m64`  
- `-Wl,-plugin-opt=-x86-branches-within-32B-boundaries`  
- `-Wl,-z,muldefs`  
- `-xCORE-AVX512`  
- `-O3`  
- `-ipo`  
- `-no-prec-div`  
- `-qopt-mem-layout-trans=4`  
- `-nostandard-realloc-lhs`  
- `-align array32byte`  
- `-auto`  
- `-mbranches-within-32B-boundaries`  
- `-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin`  
- `-lqkmalloc`

### The flags files that were used to format this result can be browsed at:
- [http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-I.html](http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-I.html)

### You can also download the XML flags sources by saving the following links:
- [http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-I.xml](http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-CLX-I.xml)

---

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.0 on 2020-06-09 06:14:15-0400.


Originally published on 2020-07-07.