**SPEC CPU®2017 Integer Rate Result**

**Fujitsu**

PRIMERGY RX2530 M5, Intel Xeon Gold 6230R, 2.10 GHz

---

**CPU2017 License:** 19  
**Test Sponsor:** Fujitsu  
**Tested by:** Fujitsu  
**Test Date:** Jul-2020  
**Hardware Availability:** Feb-2019  
**Software Availability:** Apr-2020

**SPECrate®2017_int_base = 301**  
**SPECrate®2017_int_peak = 312**

---

**Hardware**

- **CPU Name:** Intel Xeon Gold 6230R  
  **Max MHz:** 4000  
  **Nominal:** 2100  
  **Enabled:** 52 cores, 2 chips, 2 threads/core  
  **Orderable:** 1.2 chips  
  **Cache L1:** 32 KB I + 32 KB D on chip per core  
  **L2:** 1 MB I+D on chip per core  
  **L3:** 35.75 MB I+D on chip per chip  
  **Other:** None  
  **Memory:** 768 GB (24 x 32 GB 2Rx4 PC4-2933Y-R)  
  **Storage:** 1 x SATA M.2 SSD, 480 GB  
  **Other:** None

---

**Software**

- **OS:** Red Hat Enterprise Linux release 8.0 (Ootpa)  
  4.18.0-80.el8.x86_64  
- **Compiler:** C/C++: Version 19.1.1.217 of Intel C/C++ Compiler Build 20200306 for Linux;  
  Fortran: Version 19.1.1.217 of Intel Fortran Compiler Build 20200306 for Linux

---

**CPU2017 Results**

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>SPECrate®2017_int_base</th>
<th>SPECrate®2017_int_peak</th>
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</thead>
<tbody>
<tr>
<td>perlbench_r</td>
<td>104</td>
<td>292</td>
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<tr>
<td>gcc_r</td>
<td>104</td>
<td>230</td>
<td>266</td>
</tr>
<tr>
<td>mcf_r</td>
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<td>204</td>
<td></td>
</tr>
<tr>
<td>omnetpp_r</td>
<td>104</td>
<td></td>
<td></td>
</tr>
<tr>
<td>xalancbmk_r</td>
<td>104</td>
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</tr>
<tr>
<td>x264_r</td>
<td>104</td>
<td></td>
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</tr>
<tr>
<td>deepsjeng_r</td>
<td>104</td>
<td>234</td>
<td></td>
</tr>
<tr>
<td>leela_r</td>
<td>104</td>
<td>228</td>
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</tr>
<tr>
<td>exchange2_r</td>
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<td></td>
</tr>
<tr>
<td>xz_r</td>
<td>104</td>
<td>184</td>
<td></td>
</tr>
</tbody>
</table>

---

**Power Management:** BIOS set to prefer performance at the cost of additional power usage
# SPEC CPU®2017 Integer Rate Result

**Fujitsu**

PRIMERGY RX2530 M5, Intel Xeon Gold 6230R, 2.10 GHz

**SPECrate®2017_int_base = 301**

**SPECrate®2017_int_peak = 312**

## Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
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<tr>
<td>500.perlbench_r</td>
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<td>202</td>
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<td>502.gcc_r</td>
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<td>642</td>
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<td>523.xalancbmk_r</td>
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<td>531.deepsjeng_r</td>
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<td>541.leela_r</td>
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<tr>
<td>548.exchange2_r</td>
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<td>557.xz_r</td>
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<td>592</td>
</tr>
</tbody>
</table>

**SPECrate®2017_int_base = 301**

**SPECrate®2017_int_peak = 312**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The inconsistent Compiler version information under Compiler Version section is due to a discrepancy in Intel Compiler. The correct version of C/C++ compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux. The correct version of Fortran compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Kernel Boot Parameter set with: nohz_full=1-103

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:
```
LD_LIBRARY_PATH = 
    "*/home/benchmark/speccpu/lib/intel64:/home/benchmark/speccpu/lib/ia32:/home/benchmark/speccpu/je5.0.1-32"
MALLOC_CONF = "retain:true"
```
SPEC CPU®2017 Integer Rate Result

Fujitsu
PRIMERGY RX2530 M5, Intel Xeon Gold 6230R, 2.10 GHz

SPECrater®2017_int_base = 301
SPECrater®2017_int_peak = 312

CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu

General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM
memory using Redhat Enterprise Linux 8.0
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
 sync; echo 3>/proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

BIOS configuration:
Intel Virtualization Technology = Disabled
VT-d = Disabled
CPU C1E Support = Disabled
LLC Dead Line Alloc = Disabled
LLC prefetch = Enabled
Patrol Scrub = Disabled
WR CRC feature Control = Disabled
Fan Control = Full

Sysinfo program /home/benchmark/speccpu/bin/sysinfo
Rev: r6365 of 2019-08-21 295195f888a3d7edbb1e6e46a485a0011
running on localhost.localdomain Mon Jul 20 08:40:31 2020

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6230R CPU @ 2.10GHz
  2 "physical id"s (chips)
  104 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 26
siblings : 52

(Continued on next page)
Fujitsu

PRIMERGY RX2530 M5, Intel Xeon Gold 6230R, 2.10 GHz

SPEC CPU®2017 Integer Rate Result

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Fujitsu

SPECrater®2017_int_base = 301
SPECrater®2017_int_peak = 312

CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu
Test Date: Jul-2020
Hardware Availability: Feb-2019
Software Availability: Apr-2020

Platform Notes (Continued)

physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 16 17 18 19 20 21 22 24 25 26 27 28 29
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 16 17 18 19 20 21 22 24 25 26 27 28 29

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 104
On-line CPU(s) list: 0-103
Thread(s) per core: 2
Core(s) per socket: 26
Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6230R CPU @ 2.10GHz
Stepping: 7
CPU MHz: 1974.822
CPU max MHz: 4000.0000
CPU min MHz: 1000.0000
BogoMIPS: 4200.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 36608K
NUMA node0 CPU(s): 0-3,7-9,13-15,20-22,52-55,59-61,65-67,72-74
NUMA node1 CPU(s): 4-6,10-12,16-19,23-25,56-58,62-64,68-71,75-77
NUMA node2 CPU(s): 26-29,33-35,39-41,46-48,78-81,85-87,91-93,98-100
NUMA node3 CPU(s): 30-32,36-38,42-45,49-51,82-84,88-90,94-97,101-103
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtr多年前 pdcid pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abcp3 bts rep_good nopl xtopology nonstop_tsc cpuid
epb cat _l3 cdp _l3
invpcid_single intel_pni ssbd mba ibrs ibpb stibp ibrs _enhanced tpr _shadow vnum
flexpriority ept vpid fagsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rt
qcm_mpx rdt _a avx512f avx512dq rdseed adx smap clflushopt clwb intel _pt avx512cd
avx512bw avx512vl xsaveopt xsaves xsavec xgetbv1 xsaves cq mm _1c cq mm _occup _1 _lc
cq mm _total cq mm _local dfs therm ida arat pln pts hwp hwp _act _window hwp _epp hwp _pkg _req pk
ospke avx512_vnni flush _1d arch _capabilities

/proc/cpuinfo cache data

(Continued on next page)
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CPU2017 License: 19
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Test Date: Jul-2020
Tested by: Fujitsu
Hardware Availability: Feb-2019
Software Availability: Apr-2020

Platform Notes (Continued)

cache size : 36608 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 7 8 9 13 14 15 20 21 22 52 53 54 55 59 60 61 65 66 67 72 73 74
node 0 size: 192046 MB
node 0 free: 190744 MB
node 1 cpus: 4 5 6 10 11 12 16 17 18 19 23 24 25 56 57 58 62 63 64 68 69 70 71 75 76 77
node 1 size: 193530 MB
node 1 free: 192823 MB
node 2 cpus: 26 27 28 29 33 34 35 39 40 41 46 47 48 78 79 80 81 85 86 87 91 92 93 98 99
100
node 2 size: 193506 MB
node 2 free: 192873 MB
node 3 cpus: 30 31 32 36 37 38 42 43 44 45 49 50 51 82 83 84 88 89 90 94 95 96 97 101
102 103
node 3 size: 193530 MB
node 3 free: 193050 MB
node distances:
node 0 1 2 3
0: 10 11 21 21
1: 11 10 21 21
2: 21 21 10 11
3: 21 21 11 10

From /proc/meminfo
MemTotal: 791156292 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release*/etc/*version*

os-release:
NAME="Red Hat Enterprise Linux"
VERSION="8.0 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.0"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.0 (Ootpa)"
ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.0 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.0 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.0:ga

uname -a:
Linux localhost.localdomain 4.18.0-80.el8.x86_64 #1 SMP Wed Mar 13 12:02:46 UTC 2019

(Continued on next page)
Fujitsu
PRIMERGY RX2530 M5, Intel Xeon Gold 6230R, 2.10 GHz

SPEC CPU®2017 Integer Rate Result
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SPECrate®2017_int_base = 301
SPECrate®2017_int_peak = 312

Platform Notes (Continued)

Kernel self-reported vulnerability status:
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: No status reported
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

run-level 3 Jul 17 14:18
SPEC is set to: /home/benchmark/speccpu
From /sys/devices/virtual/dmi/id
BIOS: FUJITSU // American Megatrends Inc. V5.0.0.14 R1.21.0 for D3383-B1x 05/29/2020
Vendor: FUJITSU
Product: PRIMERGY RX2530 M5
Product Family: SERVER
Serial: YMLUXXXXXX

Compiler Version Notes
==============================================================================
C       | 502.gcc_r(peak)
Intel(R) C Compiler for applications running on IA-32, Version 2021.1 NextGen
Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

(Continued on next page)
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PRIMERGY RX2530 M5, Intel Xeon Gold 6230R, 2.10 GHz

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Compiler Version Notes (Continued)

==============================================================================
C       | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)  
      | 525.x264_r(base, peak) 557.xz_r(base)  
==============================================================================
Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1  
NextGen Build 20200304  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
==============================================================================
C       | 500.perlbench_r(peak) 557.xz_r(peak)  
==============================================================================
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.1.1.217 Build 20200306  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
==============================================================================
C       | 502.gcc_r(peak)  
==============================================================================
Intel(R) C Compiler for applications running on IA-32, Version 2021.1 NextGen  
Build 20200304  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
==============================================================================
C       | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)  
      | 525.x264_r(base, peak) 557.xz_r(base)  
==============================================================================
Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1  
NextGen Build 20200304  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
==============================================================================
C       | 500.perlbench_r(peak) 557.xz_r(peak)  
==============================================================================
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.1.1.217 Build 20200306  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
==============================================================================
C       | 502.gcc_r(peak)  
==============================================================================
Intel(R) C Compiler for applications running on IA-32, Version 2021.1 NextGen  
Build 20200304  
(Continued on next page)
**Compiler Version Notes (Continued)**

---

| C       | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)                                                                 |
|         | 525.x264_r(base, peak) 557.xz_r(base)                                                                                   |

Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
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| C       | 500.perlbench_r(peak) 557.xz_r(peak)                                                                                   |

---

| C++     | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak)                                                                |
|         | 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)                                                                  |

Intel(R) C++ Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

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| Fortran | 548.exchange2_r(base, peak)                                                                                           |

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Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

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**Base Compiler Invocation**

- C benchmarks:
  - icc

- C++ benchmarks:
  - icpc

(Continued on next page)
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PRIMERGY RX2530 M5, Intel Xeon Gold 6230R, 2.10 GHz

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SPECratre®2017_int_peak = 312

CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu

Test Date: Jul-2020
Hardware Availability: Feb-2019
Software Availability: Apr-2020

Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-m64 -m64 -qnextgen -std=c11
-Wl, -plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs
-xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse -funroll-loops
-fuse-ld=gold -qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin
-lqkmalloc

C++ benchmarks:
-m64 -m64 -qnextgen -Wl, -plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse
-funroll-loops -fuse-ld=gold -qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin
-lqkmalloc

Fortran benchmarks:
-m64 -Wl, -plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs
-xCORE-AVX512 -O3 -ipo -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-mbranches-within-32B-boundaries
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin
-lqkmalloc
## Peak Compiler Invocation

C benchmarks:
- `icc`

C++ benchmarks:
- `icpc`

Fortran benchmarks:
- `ifort`

## Peak Portability Flags

500.perlbench_r: `-DSPEC_LP64 -DSPEC_LINUX_X64`
502.gcc_r: `-D_FILE_OFFSET_BITS=64`
505.mcf_r: `-DSPEC_LP64`
520.omnetpp_r: `-DSPEC_LP64`
523.xalanchmk_r: `-DSPEC_LP64 -DSPEC_LINUX`
525.x264_r: `-DSPEC_LP64`
531.deepsjeng_r: `-DSPEC_LP64`
541.leela_r: `-DSPEC_LP64`
548.exchange2_r: `-DSPEC_LP64`
557.xz_r: `-DSPEC_LP64`

## Peak Optimization Flags

C benchmarks:


502.gcc_r: `-m32 -std=gnu89 -Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs -fprofile-generate(pass 1) -fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -flto -Ofast(pass 1) -O3 -ffast-math -qnextgen -fuse-ld=gold -qopt-mem-layout-trans=4 -L/usr/local/jemalloc32-5.0.1/lib -ljemalloc`

(Continued on next page)
Peak Optimization Flags (Continued)

505.mcf_r: basepeak = yes

525.x264_r: -m64 -qnextgen -std=c11
-W1,-plugin-opt=-x86-branches-within-32B-boundaries
-W1,-z,muldefs -xCORE-AVX512 -flto -O3 -ffast-math
-fuse-ld=gold -qopt-mem-layout-trans=4 -fno-alias
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin
-lqkmalloc

557.xz_r: -W1,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin
-lqkmalloc

C++ benchmarks:

520.omnetpp_r: basepeak = yes

523.xalancbmk_r: basepeak = yes

531.deepsjeng_r: basepeak = yes

541.leela_r: basepeak = yes

Fortran benchmarks:

548.exchange2_r: basepeak = yes

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic19.1u1-official-linux64_revA.xml
http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.0-CSL-RevE.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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