New H3C Technologies Co., Ltd.

H3C UniServer R4900 G3 (Intel Xeon Gold 6256)

<table>
<thead>
<tr>
<th>SPECrate®2017_fp_base = 210</th>
<th>SPECrate®2017_fp_peak = 217</th>
</tr>
</thead>
</table>

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Tested by: New H3C Technologies Co., Ltd.

Hardware

<table>
<thead>
<tr>
<th>CPU Name:</th>
<th>Intel Xeon Gold 6256</th>
</tr>
</thead>
<tbody>
<tr>
<td>Max MHz:</td>
<td>4500</td>
</tr>
<tr>
<td>Nominal:</td>
<td>3600</td>
</tr>
<tr>
<td>Enabled:</td>
<td>24 cores, 2 chips, 2 threads/core</td>
</tr>
<tr>
<td>Orderable:</td>
<td>1,2 chips</td>
</tr>
<tr>
<td>Cache L1:</td>
<td>32 KB I + 32 KB D on chip per core</td>
</tr>
<tr>
<td>L2:</td>
<td>1 MB I+D on chip per core</td>
</tr>
<tr>
<td>L3:</td>
<td>33 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other:</td>
<td>None</td>
</tr>
<tr>
<td>Memory:</td>
<td>192 GB (12 x 16 GB 2Rx8 PC4-2933V-R)</td>
</tr>
<tr>
<td>Storage:</td>
<td>2 x 600 GB SAS HDD,10000RPM,RAID 1</td>
</tr>
<tr>
<td>Other:</td>
<td>None</td>
</tr>
</tbody>
</table>

Software

<table>
<thead>
<tr>
<th>OS:</th>
<th>Red Hat Enterprise Linux release 8.2 (Ootpa)</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>4.18.0-193.el8.x86_64</td>
</tr>
<tr>
<td>Compiler:</td>
<td>C/C++: Version 19.1.1.217 of Intel C/C++</td>
</tr>
<tr>
<td></td>
<td>Compiler Build 20200306 for Linux;</td>
</tr>
<tr>
<td></td>
<td>Fortran: Version 19.1.1.217 of Intel Fortran</td>
</tr>
<tr>
<td></td>
<td>Compiler Build 20200306 for Linux</td>
</tr>
<tr>
<td>Parallel:</td>
<td>No</td>
</tr>
<tr>
<td>Firmware:</td>
<td>Version 2.00.33 released Aug-2019 BIOS</td>
</tr>
<tr>
<td>File System:</td>
<td>xfs</td>
</tr>
<tr>
<td>System State:</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers:</td>
<td>64-bit</td>
</tr>
<tr>
<td>Peak Pointers:</td>
<td>64-bit</td>
</tr>
<tr>
<td>Other:</td>
<td>jemalloc memory allocator V5.0.1</td>
</tr>
<tr>
<td>Power Management:</td>
<td>BIOS set to prefer performance at the cost of additional power usage</td>
</tr>
</tbody>
</table>
New H3C Technologies Co., Ltd.

H3C UniServer R4900 G3 (Intel Xeon Gold 6256)

**SPECrate®2017_fp_base = 210**

**SPECrate®2017_fp_peak = 217**

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>48</td>
<td>959</td>
<td>502</td>
<td>957</td>
<td>503</td>
<td>959</td>
<td>502</td>
<td>24</td>
<td>477</td>
<td>505</td>
<td>476</td>
<td>505</td>
<td></td>
<td></td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>48</td>
<td>228</td>
<td>266</td>
<td>229</td>
<td>266</td>
<td>228</td>
<td>266</td>
<td>48</td>
<td>228</td>
<td>266</td>
<td>229</td>
<td>266</td>
<td></td>
<td></td>
</tr>
<tr>
<td>508.namd_r</td>
<td>48</td>
<td>301</td>
<td>152</td>
<td>299</td>
<td>153</td>
<td>301</td>
<td>151</td>
<td>48</td>
<td>301</td>
<td>152</td>
<td>299</td>
<td>153</td>
<td></td>
<td></td>
</tr>
<tr>
<td>510.parest_r</td>
<td>48</td>
<td>880</td>
<td>143</td>
<td>881</td>
<td>143</td>
<td>879</td>
<td>143</td>
<td>24</td>
<td>380</td>
<td>165</td>
<td>379</td>
<td>166</td>
<td></td>
<td></td>
</tr>
<tr>
<td>511.povray_r</td>
<td>48</td>
<td>494</td>
<td>227</td>
<td>493</td>
<td>227</td>
<td>493</td>
<td>228</td>
<td>48</td>
<td>416</td>
<td>269</td>
<td>416</td>
<td>270</td>
<td></td>
<td></td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>48</td>
<td>506</td>
<td>100</td>
<td>505</td>
<td>100</td>
<td>505</td>
<td>100</td>
<td>48</td>
<td>506</td>
<td>100</td>
<td>505</td>
<td>100</td>
<td></td>
<td></td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>48</td>
<td>500</td>
<td>215</td>
<td>503</td>
<td>214</td>
<td>516</td>
<td>208</td>
<td>48</td>
<td>260</td>
<td>207</td>
<td>262</td>
<td>205</td>
<td></td>
<td></td>
</tr>
<tr>
<td>526.blender_r</td>
<td>48</td>
<td>374</td>
<td>195</td>
<td>371</td>
<td>197</td>
<td>373</td>
<td>196</td>
<td>48</td>
<td>374</td>
<td>195</td>
<td>371</td>
<td>197</td>
<td></td>
<td></td>
</tr>
<tr>
<td>527.camus4_r</td>
<td>48</td>
<td>395</td>
<td>212</td>
<td>404</td>
<td>208</td>
<td>395</td>
<td>212</td>
<td>48</td>
<td>395</td>
<td>212</td>
<td>404</td>
<td>208</td>
<td></td>
<td></td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>48</td>
<td>220</td>
<td>542</td>
<td>220</td>
<td>544</td>
<td>221</td>
<td>541</td>
<td>48</td>
<td>220</td>
<td>542</td>
<td>220</td>
<td>544</td>
<td></td>
<td></td>
</tr>
<tr>
<td>544.nab_r</td>
<td>48</td>
<td>234</td>
<td>345</td>
<td>235</td>
<td>344</td>
<td>236</td>
<td>342</td>
<td>48</td>
<td>234</td>
<td>345</td>
<td>235</td>
<td>344</td>
<td></td>
<td></td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>48</td>
<td>1391</td>
<td>135</td>
<td>1395</td>
<td>134</td>
<td>1395</td>
<td>134</td>
<td>48</td>
<td>1391</td>
<td>135</td>
<td>1395</td>
<td>134</td>
<td></td>
<td></td>
</tr>
<tr>
<td>554.roms_r</td>
<td>48</td>
<td>722</td>
<td>106</td>
<td>722</td>
<td>106</td>
<td>722</td>
<td>106</td>
<td>24</td>
<td>313</td>
<td>122</td>
<td>313</td>
<td>122</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

**Compiler Notes**

The inconsistent Compiler version information under Compiler Version section is due to a discrepancy in Intel Compiler. The correct version of C/C++ compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux. The correct version of Fortran compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux.

**Submit Notes**

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"

**Environment Variables Notes**

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/speccpu/lib/intel64:/home/speccpu/je5.0.1-64"
MALLOC_CONF = "retain:true"
**SPEC CPU®2017 Floating Point Rate Result**

New H3C Technologies Co., Ltd.  
H3C UniServer R4900 G3 (Intel Xeon Gold 6256)

<table>
<thead>
<tr>
<th>SPECrate®2017_fp_base = 210</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate®2017_fp_peak = 217</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 9066  
**Test Date:** Nov-2020  
**Test Sponsor:** New H3C Technologies Co., Ltd.  
**Tested by:** New H3C Technologies Co., Ltd.  
**Hardware Availability:** Mar-2020  
**Software Availability:** Apr-2020

---

**General Notes**

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0  
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop_caches  
runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>  
jemalloc, a general purpose malloc implementation  
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  

---

**Platform Notes**

BIOS settings:  
Set SNC to Enabled  
Set IMC Interleaving to 1-way Interleave  
Set Patrol Scrub to Disabled

Sysinfo program /home/speccpu/bin/sysinfo  
Rev: r6365 of 2019-08-21 295195f8883a3d7ed9be646e485a0011  
running on localhost.localdomain Tue Nov 10 16:55:35 2020

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) Gold 6256 CPU @ 3.60GHz  
2 "physical id"s (chips)  
48 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 12  
siblings : 24  
physical 0: cores 0 1 2 5 11 13 16 20 21 25 26 29  
physical 1: cores 1 2 4 5 9 10 11 12 16 21 24 29

From lscpu:  
Architecture: x86_64

(Continued on next page)
New H3C Technologies Co., Ltd.  
H3C UniServer R4900 G3 (Intel Xeon Gold 6256)

SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

SPECrate®2017_fp_base = 210
SPECrate®2017_fp_peak = 217

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Tested by: New H3C Technologies Co., Ltd.

Test Date: Nov-2020
Hardware Availability: Mar-2020
Software Availability: Apr-2020

Platform Notes (Continued)

CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 48
On-line CPU(s) list: 0-47
Thread(s) per core: 2
Core(s) per socket: 12
Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6256 CPU @ 3.60GHz
Stepping: 7
CPU MHz: 4335.351
CPU max MHz: 4500.0000
CPU min MHz: 1200.0000
BogoMIPS: 7200.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 33792K
NUMA node0 CPU(s): 0-2, 6, 9, 10, 24-26, 30, 33, 34
NUMA node1 CPU(s): 3-5, 7, 8, 11, 27-29, 31, 32, 35
NUMA node2 CPU(s): 12, 13, 16, 17, 20, 22, 36, 37, 40, 41, 44, 46
NUMA node3 CPU(s): 14, 15, 18, 19, 21, 23, 38, 39, 42, 43, 45, 47
Flags: fpu vme de pse tsc msr pae mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc c pud aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3nowprefetch cpuid_fault epb cat_l3 cdp_l3 invpcid_single intel_pppin ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erm invpcid rtm cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd avx512bw avx512vl xsaveopt xsaves xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local dtherm ida arat pln pts pku ospke avx512_vnni md_clear flush_l1d arch_capabilities

/proc/cpuinfo cache data
cache size : 33792 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0 1 2 6 9 10 24 25 26 30 33 34
node 0 size: 46665 MB

(Continued on next page)
New H3C Technologies Co., Ltd.

H3C UniServer R4900 G3 (Intel Xeon Gold 6256)

SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

SPECrate®2017_fp_base = 210
SPECrate®2017_fp_peak = 217

Platform Notes (Continued)

node 0 free: 37743 MB
node 1 cpus: 3 4 5 7 8 11 27 28 29 31 32 35
node 1 size: 48381 MB
node 1 free: 42326 MB
node 2 cpus: 12 13 16 17 20 22 36 37 40 41 44 46
node 2 size: 48381 MB
node 2 free: 42781 MB
node 3 cpus: 14 15 18 19 21 23 38 39 42 43 45 47
node 3 size: 48380 MB
node 3 free: 42923 MB
node distances:
  node   0   1   2   3
  0:  10  11  21  21
  1:  11  10  21  21
  2:  21  21  10  11
  3:  21  21  11  10

From /proc/meminfo
  MemTotal:       196412248 kB
  HugePages_Total:       0
  Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
  os-release:
    NAME="Red Hat Enterprise Linux"
    VERSION="8.2 (Ootpa)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="8.2"
    PLATFORM_ID="platform:el8"
    PRETTY_NAME="Red Hat Enterprise Linux 8.2 (Ootpa)"
    ANSI_COLOR="0;31"
  redhat-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
  system-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
  system-release-cpe: cpe:/o:redhat:enterprise_linux:8.2:ga

uname -a:
  Linux localhost.localdomain 4.18.0-193.el8.x86_64 #1 SMP Fri Mar 27 14:35:58 UTC 2020
  x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
  itlb_multihit:                        KVM: Mitigation: Split huge pages
  CVE-2018-3620 (L1 Terminal Fault):   Not affected
  Microarchitectural Data Sampling:    Not affected
  CVE-2017-5754 (Meltdown):           Not affected
  CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled

(Continued on next page)
New H3C Technologies Co., Ltd.  
H3C UniServer R4900 G3 (Intel Xeon Gold 6256)

**SPEC CPU®2017 Floating Point Rate Result**

**SPECrate®2017_fp_base = 210**  
**SPECrate®2017_fp_peak = 217**

---

**CPU2017 License:** 9066  
**Test Sponsor:** New H3C Technologies Co., Ltd.  
**Test Date:** Nov-2020  
**Hardware Availability:** Mar-2020  
**Tested by:** New H3C Technologies Co., Ltd.  
**Software Availability:** Apr-2020

---

**Platform Notes (Continued)**

- **CVE-2017-5753 (Spectre variant 1):** Mitigation: usercopy/swapgs barriers and __user pointer sanitization
- **CVE-2017-5715 (Spectre variant 2):** Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
- **tsx_async_abort:** Mitigation: Clear CPU buffers; SMT vulnerable

**run-level 3 Nov 10 10:45**

**SPEC is set to:** /home/speccpu  
**Filesystem**

<table>
<thead>
<tr>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>xfs</td>
<td>503G</td>
<td>64G</td>
<td>439G</td>
<td>13%</td>
<td>/home</td>
</tr>
</tbody>
</table>

**From /sys/devices/virtual/dmi/id**

- **BIOS:** American Megatrends Inc. 2.00.33 08/22/2019  
- **Vendor:** H3C  
- **Product:** RS33M2C9S  
- **Product Family:** Rack

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

**Memory:**

- 12x NO DIMM NO DIMM  
- 12x Samsung M393A2K43CB2-CVF 16 GB 2 rank 2933

(End of data from sysinfo program)

---

**Compiler Version Notes**

```
C               | 519.lbm_r(base, peak) 538.imagick_r(base, peak)  
| 544.nab_r(base, peak)

Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1  
NextGen Build 20200304  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
```

```
C++            | 508.namd_r(base, peak) 510.parest_r(base, peak)

Intel(R) C++ Compiler for applications running on Intel(R) 64, Version 2021.1  
NextGen Build 20200304  
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```

(Continued on next page)
New H3C Technologies Co., Ltd.

H3C UniServer R4900 G3 (Intel Xeon Gold 6256)

CPU2017 License: 9066
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Tested by: New H3C Technologies Co., Ltd.

Test Date: Nov-2020
Hardware Availability: Mar-2020
Software Availability: Apr-2020

Compiler Version Notes (Continued)

==============================================================================
C++, C | 511.povray_r(base) 526.blender_r(base, peak)
==============================================================================
Intel(R) C++ Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
C++, C | 511.povray_r(peak)
==============================================================================
Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
C++, C | 511.povray_r(base) 526.blender_r(base, peak)
==============================================================================
Intel(R) C++ Compiler for applications running on Intel(R) 64, Version 2021.1
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==============================================================================
C++, C | 511.povray_r(peak)
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Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
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Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
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Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
New H3C Technologies Co., Ltd.  
H3C UniServer R4900 G3 (Intel Xeon Gold 6256)  

**SPECrate®2017_fp_base = 210**  
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**CPU2017 License:** 9066  
**Test Sponsor:** New H3C Technologies Co., Ltd.  
**Tested by:** New H3C Technologies Co., Ltd.  
**Test Date:** Nov-2020  
**Hardware Availability:** Mar-2020  
**Software Availability:** Apr-2020

---

### Compiler Version Notes (Continued)

**C++, C, Fortran | 507.cactuBSSN_r(base, peak)**

---

**Intel(R) C++ Compiler for applications running on Intel(R) 64, Version 2021.1  
NextGen Build 20200304**

Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

---

**Fortran | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base, peak)**

---

**Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
64, Version 19.1.1.217 Build 20200306**

Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

---

**Fortran, C | 521.wrf_r(base) 527.cam4_r(base, peak)**

---

**Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
64, Version 19.1.1.217 Build 20200306**

Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

---

**Fortran, C | 521.wrf_r(peak)**

---

**Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
64, Version 19.1.1.217 Build 20200306**

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---

**Fortran, C | 521.wrf_r(base) 527.cam4_r(base, peak)**

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(Continued on next page)
### New H3C Technologies Co., Ltd.

**H3C UniServer R4900 G3 (Intel Xeon Gold 6256)**

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<tr>
<td>SPECrate®2017_fp_peak = 217</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 9066

**Test Sponsor:** New H3C Technologies Co., Ltd.

**Tested by:** New H3C Technologies Co., Ltd.

**Hardware Availability:** Mar-2020

**Software Availability:** Apr-2020

**Test Date:** Nov-2020

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### Compiler Version Notes (Continued)

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R) 64, Version 19.1.1.217 Build 20200306

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Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1

NextGen Build 20200304

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#### Base Compiler Invocation

- **C benchmarks:**
  - icc

- **C++ benchmarks:**
  - icpc

- **Fortran benchmarks:**
  - ifort

- **Benchmarks using both Fortran and C:**
  - ifort icc

- **Benchmarks using both C and C++:**
  - icpc icc

- **Benchmarks using Fortran, C, and C++:**
  - icpc icc ifort

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#### Base Portability Flags

- 503.bwaves_r: -DSPEC_LP64
- 507.cactuBSSN_r: -DSPEC_LP64

(Continued on next page)
**Base Portability Flags (Continued)**

508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

**Base Optimization Flags**

C benchmarks:
```
-m64 -qnextgen -std=c11
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs
-fuse-ld=gold -xCORE-AVX512 -Ofast -ffast-math -flto -mfpmath=sse
-funroll-loops -gopt-mem-layout-trans=4
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

C++ benchmarks:
```
-m64 -qnextgen -Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -fuse-ld=gold -xCORE-AVX512 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -gopt-mem-layout-trans=4
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Fortran benchmarks:
```
-m64 -Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs
-fuse-ld=gold -xCORE-AVX512 -O3 -ipo -no-prec-div -gopt-prefetch
-ffinite-math-only -gopt-multiple-gather-scatter-by-shuffles
-gopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
-auto -mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using both Fortran and C:
```
-m64 -qnextgen -std=c11
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs
-fuse-ld=gold -xCORE-AVX512 -Ofast -ffast-math -flto -mfpmath=sse
-funroll-loops -gopt-mem-layout-trans=4 -O3 -ipo -no-prec-div
-gopt-prefetch -ffinite-math-only
-gopt-multiple-gather-scatter-by-shuffles -nostandard-realloc-lhs
-align array32byte -auto -mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```
New H3C Technologies Co., Ltd. | SPEC CPU®2017 Floating Point Rate Result
H3C UniServer R4900 G3 (Intel Xeon Gold 6256) | SPECrate®2017_fp_base = 210
| SPECrate®2017_fp_peak = 217

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Tested by: New H3C Technologies Co., Ltd.

Test Date: Nov-2020
Hardware Availability: Mar-2020
Software Availability: Apr-2020

Base Optimization Flags (Continued)

Benchmarks using both C and C++:
-m64 -qnextgen -std=c11
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs
-fuse-ld=gold -xCORE-AVX512 -Ofast -ffast-math -flto -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Benchmarks using Fortran, C, and C++:
-m64 -qnextgen -std=c11
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs
-fuse-ld=gold -xCORE-AVX512 -Ofast -ffast-math -flto -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4 -O3 -ipo -no-prec-div
-qopt-prefetch -ffinite-math-only
-qopt-multiple-gather-scatter-by-shuffles -nostandard-realloc-lhs
-align array32byte -auto -mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Peak Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using both C and C++:
icpc icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort
New H3C Technologies Co., Ltd.
H3C UniServer R4900 G3 (Intel Xeon Gold 6256)

| SPECrate®2017_fp_base = 210 |
| SPECrate®2017_fp_peak = 217 |

CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Tested by: New H3C Technologies Co., Ltd.
Test Date: Nov-2020
Hardware Availability: Mar-2020
Software Availability: Apr-2020

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
519.lbm_r: basepeak = yes
538.imagick_r: basepeak = yes
544.nab_r: basepeak = yes

C++ benchmarks:
508.namd_r: basepeak = yes
510.parest_r -m64 -gnextgen
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -fuse-ld=gold -xCORE-AVX512 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -L/usr/local/jemalloc64-5.0.1/lib
-jemalloc

Fortran benchmarks:
503.bwaves_r: -m64 -Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -fuse-ld=gold -xCORE-AVX512 -O3 -ipo
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-multiple-gather-scatter-by-shuffles
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs
-align array32byte -auto -mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

549.fotonik3d_r: basepeak = yes
554.roms_r: Same as 503.bwaves_r

Benchmarks using both Fortran and C:
521.wrf_r: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512 -O3
-ipo -no-prec-div -qopt-prefetch -fffinite-math-only
-qopt-multiple-gather-scatter-by-shuffles
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
-nostandard-realloc-lhs -align array32byte -auto

(Continued on next page)
New H3C Technologies Co., Ltd.
H3C UniServer R4900 G3 (Intel Xeon Gold 6256)

**SPECrate®2017_fp_peak = 217**

**SPECrate®2017_fp_base = 210**

**CPU2017 License:** 9066  
**Test Date:** Nov-2020  
**Test Sponsor:** New H3C Technologies Co., Ltd.  
**Hardware Availability:** Mar-2020  
**Tested by:** New H3C Technologies Co., Ltd.  
**Software Availability:** Apr-2020

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**Peak Optimization Flags (Continued)**

521.wrf_r (continued):
- L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

527.cam4_r: basepeak = yes

Benchmarks using both C and C++:

511.povray_r: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512 -O3  
-ipo -no-prec-div -qopt-prefetch -ffinite-math-only  
-qopt-multiple-gather-scatter-by-shuffles  
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

526.blender_r: basepeak = yes

Benchmarks using Fortran, C, and C++:

507.cactuBSSN_r: basepeak = yes

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The flags files that were used to format this result can be browsed at


http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.4-CLX-RevB.html

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2017/flags/Intel-ic19.1u1-official-linux64_revA.xml

http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.4-CLX-RevB.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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