# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

## Fujitsu

(Test Sponsor: Fujitsu)

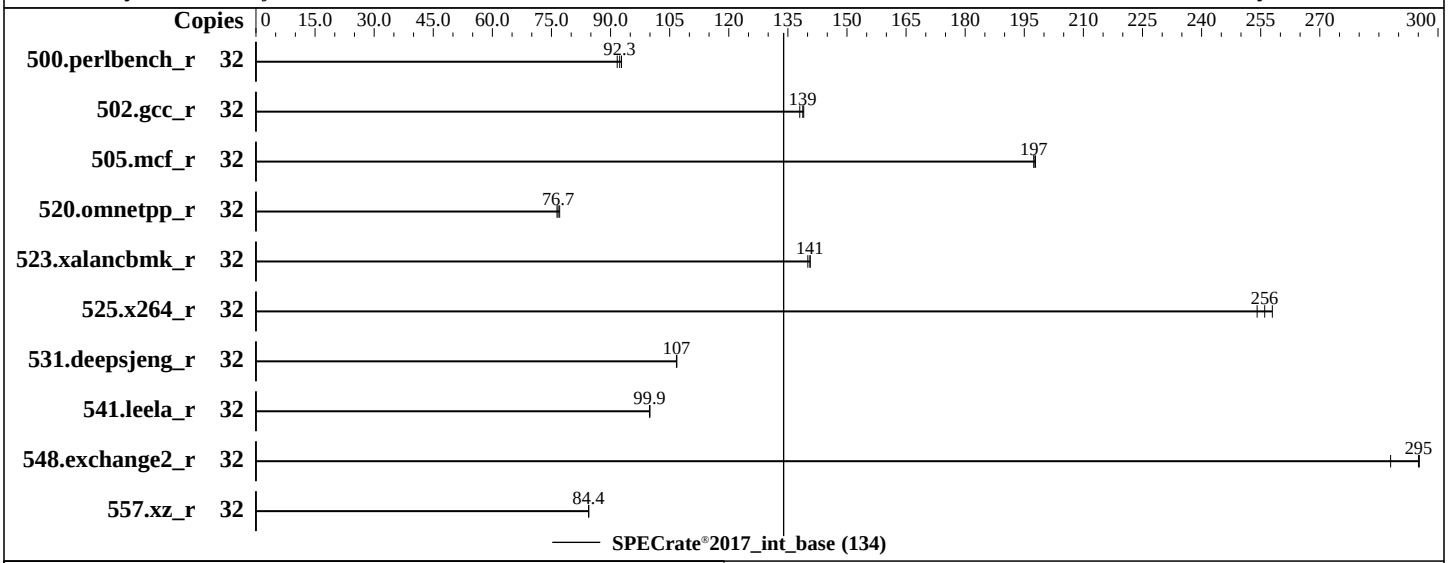
PRIMERGY GX2460 M1, AMD EPYC 7262, 3.20 GHz

SPECrate®2017_int_base = 134

SPECrate®2017_int_peak = Not Run

**CPU2017 License:** 19
**Test Sponsor:** Fujitsu
**Tested by:** Fujitsu

**Test Date:** Dec-2020
**Hardware Availability:** Oct-2020
**Software Availability:** Jul-2020




## Hardware

| | |
|---|---|
| CPU Name: | AMD EPYC 7262 |
| Max MHz: | 3400 |
| Nominal: | 3200 |
| Enabled: | 16 cores, 2 chips, 2 threads/core |
| Orderable: | 2 chips |
| Cache L1: | 32 KB I + 32 KB D on chip per core |
| L2: | 512 KB I+D on chip per core |
| L3: | 128 MB I+D on chip per chip, 16 MB per core |
| Other: | None |
| Memory: | 512 GB (16 x 32 GB 2Rx4 PC4-3200AA-L) |
| Storage: | 1 x PCIe SSD, 2TB |
| Other: | None |

## Software

| | |
|---|---|
| OS: | SUSE Linux Enterprise Server 15 SP2 (x86_64) kernel version 5.3.18-22-default |
| Compiler: | C/C++/Fortran: Version 2.0.0 of AOCC |
| Parallel: | No |
| Firmware: | Fujitsu BIOS Version R07.104. Released Oct-2020 tested as V7.104 Aug-2020 |
| File System: | xfs |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 64-bit |
| Peak Pointers: | Not Applicable |
| Other: | jemalloc: jemalloc memory allocator library v5.2.0 |
| Power Management: | BIOS set to prefer performance at the cost of additional power usage |

# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Fujitsu
(Test Sponsor: Fujitsu)
PRIMERGY GX2460 M1, AMD EPYC 7262, 3.20 GHz

SPECrate®2017_int_base = 134

SPECrate®2017_int_peak = Not Run

**CPU2017 License:** 19
**Test Sponsor:** Fujitsu
**Tested by:** Fujitsu

**Test Date:** Dec-2020
**Hardware Availability:** Oct-2020
**Software Availability:** Jul-2020

## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 500.perlbench_r | 32 | 549 | 92.8 | 556 | 91.7 | **552** | **92.3** | | | | | | | |
| 502.gcc_r | 32 | 326 | 139 | 328 | 138 | **327** | **139** | | | | | | | |
| 505.mcf_r | 32 | **262** | **197** | 261 | 198 | 262 | 197 | | | | | | | |
| 520.omnetpp_r | 32 | 549 | 76.4 | **547** | **76.7** | 545 | 77.1 | | | | | | | |
| 523.xalancbmk_r | 32 | 241 | 140 | 240 | 141 | **240** | **141** | | | | | | | |
| 525.x264_r | 32 | 217 | 258 | 221 | 254 | **219** | **256** | | | | | | | |
| 531.deepsjeng_r | 32 | 343 | 107 | 344 | 107 | **343** | **107** | | | | | | | |
| 541.leela_r | 32 | **530** | **99.9** | 530 | 99.9 | 530 | 99.9 | | | | | | | |
| 548.exchange2_r | 32 | 284 | 295 | **284** | **295** | 291 | 288 | | | | | | | |
| 557.xz_r | 32 | **409** | **84.4** | 409 | 84.4 | 409 | 84.4 | | | | | | | |

**SPECrate®2017_int_base = 134**

**SPECrate®2017_int_peak = Not Run**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

```
The AMD64 AOCC Compiler Suite is available at
http://developer.amd.com/amd-aocc/
```

## Submit Notes

```
The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.
```

## Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

Set dirty_ratio=8 to limit dirty cache to 8% of memory
Set swappiness=1 to swap only if necessary
Set zone_reclaim_mode=1 to free local node memory and avoid remote memory
sync then drop_caches=3 to reset caches before invoking runcpu

dirty_ratio, swappiness, zone_reclaim_mode and drop_caches were
all set using privileged echo (e.g. echo 1 > /proc/sys/vm/swappiness).
```

**(Continued on next page)**

## Operating System Notes (Continued)

```
Transparent huge pages set to 'always' for this run (OS default)
```

## Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH =
     "/home/benchmark/speccpu/amd_rate_aocc200_rome_C_lib/64;/home/benchmark/
     speccpu/amd_rate_aocc200_rome_C_lib/32:"
MALLOC_CONF = "retain:true"
```

## General Notes

```
Binaries were compiled on a system with 2x AMD EPYC 7601 CPU + 512GB Memory using Fedora 26

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

jemalloc: configured and built with GCC v9.1.0 in Ubuntu 19.04 with -O3 -znver2 -flto
jemalloc 5.2.0 is available here:
https://github.com/jemalloc/jemalloc/releases/download/5.2.0/jemalloc-5.2.0.tar.bz2
```

## Platform Notes

```
 BIOS configuration:
 cTDP = 180
 Determinism Slider = Power
 Package Power Limit = 180
 SVM Mode = Disabled

 Sysinfo program /home/benchmark/speccpu/bin/sysinfo
 Rev: r6365 of 2019-08-21 295195f888a3d7edb1e6e46a485a0011
 running on localhost Thu Aug 20 13:57:35 2020

 SUT (System Under Test) info as seen by some common utilities.
 For more information on this section, see
    https://www.spec.org/cpu2017/Docs/config.html#sysinfo

 From /proc/cpuinfo
    model name : AMD EPYC 7262 8-Core Processor
```

**(Continued on next page)**

## Platform Notes (Continued)

```
      2  "physical id"s (chips)
      32 "processors"
   cores, siblings (Caution: counting these is hw and system dependent. The following
   excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
      cpu cores : 8
      siblings  : 16
      physical 0: cores 0 4 8 12 16 20 24 28
      physical 1: cores 0 4 8 12 16 20 24 28

 From lscpu:
      Architecture:        x86_64
      CPU op-mode(s):      32-bit, 64-bit
      Byte Order:          Little Endian
      Address sizes:       43 bits physical, 48 bits virtual
      CPU(s):              32
      On-line CPU(s) list: 0-31
      Thread(s) per core:  2
      Core(s) per socket:  8
      Socket(s):           2
      NUMA node(s):        8
      Vendor ID:           AuthenticAMD
      CPU family:          23
      Model:               49
      Model name:          AMD EPYC 7262 8-Core Processor
      Stepping:            0
      CPU MHz:             1796.196
      CPU max MHz:         3200.0000
      CPU min MHz:         1500.0000
      BogoMIPS:            6387.75
      Virtualization:      AMD-V
      L1d cache:           32K
      L1i cache:           32K
      L2 cache:            512K
      L3 cache:            16384K
      NUMA node0 CPU(s):   0,1,16,17
      NUMA node1 CPU(s):   2,3,18,19
      NUMA node2 CPU(s):   4,5,20,21
      NUMA node3 CPU(s):   6,7,22,23
      NUMA node4 CPU(s):   8,9,24,25
      NUMA node5 CPU(s):   10,11,26,27
      NUMA node6 CPU(s):   12,13,28,29
      NUMA node7 CPU(s):   14,15,30,31
      Flags:               fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
     pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm
     constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid aperfmperf pni pclmulqdq
     monitor ssse3 fma cx16 sse4_1 sse4_2 movbe popcnt aes xsave avx f16c rdrand lahf_lm
     cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch osvw ibs
```

**(Continued on next page)**

## Platform Notes (Continued)

```
      skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb cat_l3
      cdp_l3 hw_pstate sme ssbd mba sev ibrs ibpb stibp vmmcall fsgsbase bmi1 avx2 smep
      bmi2 cqm rdt_a rdseed adx smap clflushopt clwb sha_ni xsaveopt xsavec xgetbv1 xsaves
      cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local clzero irperf xsaveerptr wbnoinvd
      arat npt lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists
      pausefilter pfthreshold avic v_vmsave_vmload vgif umip rdpid overflow_recov succor
      smca

  /proc/cpuinfo cache data
     cache size : 512 KB

  From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a
  physical chip.
    available: 8 nodes (0-7)
    node 0 cpus: 0 1 16 17
    node 0 size: 64249 MB
    node 0 free: 63953 MB
    node 1 cpus: 2 3 18 19
    node 1 size: 64509 MB
    node 1 free: 64382 MB
    node 2 cpus: 4 5 20 21
    node 2 size: 64509 MB
    node 2 free: 64398 MB
    node 3 cpus: 6 7 22 23
    node 3 size: 64497 MB
    node 3 free: 64400 MB
    node 4 cpus: 8 9 24 25
    node 4 size: 64509 MB
    node 4 free: 64409 MB
    node 5 cpus: 10 11 26 27
    node 5 size: 64476 MB
    node 5 free: 64218 MB
    node 6 cpus: 12 13 28 29
    node 6 size: 64509 MB
    node 6 free: 64408 MB
    node 7 cpus: 14 15 30 31
    node 7 size: 64269 MB
    node 7 free: 64151 MB
    node distances:
    node   0   1   2   3   4   5   6   7
      0:  10  12  12  12  32  32  32  32
      1:  12  10  12  12  32  32  32  32
      2:  12  12  10  12  32  32  32  32
      3:  12  12  12  10  32  32  32  32
      4:  32  32  32  32  10  12  12  12
      5:  32  32  32  32  12  10  12  12
      6:  32  32  32  32  12  12  10  12
```

**(Continued on next page)**

## Platform Notes (Continued)

```
     7:  32  32  32  32  12  12  12  10

 From /proc/meminfo
    MemTotal:       527904492 kB
    HugePages_Total:       0
    Hugepagesize:       2048 kB

 From /etc/*release* /etc/*version*
    os-release:
       NAME="SLES"
       VERSION="15-SP2"
       VERSION_ID="15.2"
       PRETTY_NAME="SUSE Linux Enterprise Server 15 SP2"
       ID="sles"
       ID_LIKE="suse"
       ANSI_COLOR="0;32"
       CPE_NAME="cpe:/o:suse:sles:15:sp2"

 uname -a:
    Linux localhost 5.3.18-22-default #1 SMP Wed Jun 3 12:16:43 UTC 2020
    (720aeba/lp-1a956f1) x86_64 x86_64 x86_64 GNU/Linux

 Kernel self-reported vulnerability status:

 itlb_multihit:                           Not affected
 CVE-2018-3620 (L1 Terminal Fault):       Not affected
 Microarchitectural Data Sampling:        Not affected
 CVE-2017-5754 (Meltdown):                Not affected
 CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled
                                           via prctl and seccomp
 CVE-2017-5753 (Spectre variant 1):       Mitigation: usercopy/swapgs barriers and __user
                                           pointer sanitization
 CVE-2017-5715 (Spectre variant 2):        Mitigation: Full AMD retpoline, IBPB:
                                           conditional, IBRS_FW, STIBP: conditional, RSB
                                           filling
 srbds:                                    Not affected
 tsx_async_abort:                          Not affected

 run-level 3 Aug 20 13:36

 SPEC is set to: /home/benchmark/speccpu
    Filesystem     Type  Size  Used Avail Use% Mounted on
    /dev/nvme1n1p3 xfs   1.3T  6.0G  1.3T   1% /home

 From /sys/devices/virtual/dmi/id
     BIOS:    American Megatrends Inc. V7.104 08/20/2020
     Vendor:  FUJITSU
```

**(Continued on next page)**

## Platform Notes (Continued)

```
     Product: PRIMERGY GX2460 M1
     Product Family: empty
     Serial:  MACK900001

  Additional information from dmidecode follows.  WARNING: Use caution when you interpret
  this section. The 'dmidecode' program reads system data which is "intended to allow
  hardware to be accurately determined", but the intent may not be met, as there are
  frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
    Memory:
      16x Samsung M393A4K40DB3-CWE 32 kB 2 rank 3200

  (End of data from sysinfo program)
```

## Compiler Version Notes

```
==============================================================================
C       | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base)
        | 525.x264_r(base) 557.xz_r(base)
------------------------------------------------------------------------------
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
  AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin
------------------------------------------------------------------------------

==============================================================================
C++     | 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)
        | 541.leela_r(base)
------------------------------------------------------------------------------
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
  AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin
------------------------------------------------------------------------------

==============================================================================
Fortran | 548.exchange2_r(base)
------------------------------------------------------------------------------
AOCC.LLVM.2.0.0.B191.2019_07_19 clang version 8.0.0 (CLANG: Jenkins
  AOCC_2_0_0-Build#191) (based on LLVM AOCC.LLVM.2.0.0.B191.2019_07_19)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /sppo/dev/compilers/aocc-compiler-2.0.0/bin
------------------------------------------------------------------------------
```

## Base Compiler Invocation

C benchmarks:
clang

C++ benchmarks:
clang++

Fortran benchmarks:
flang

## Base Portability Flags

500.perlbench_r: -DSPEC_LINUX_X64 -DSPEC_LP64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LINUX -DSPEC_LP64
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

## Base Optimization Flags

C benchmarks:
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -ffast-math
-march=znver2 -fstruct-layout=3 -mllvm -unroll-threshold=50
-fremap-arrays -mllvm -function-specialize -mllvm -enable-gvn-hoist
-mllvm -reduce-array-computations=3 -mllvm -global-vectorize-slp
-mllvm -vector-library=LIBMVEC -mllvm -inline-threshold=1000
-flv-function-specialization -z muldefs -lmvec -lamdlibm -ljemalloc
-lflang

C++ benchmarks:
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-suppress-fmas -O3 -ffast-math -march=znver2
-mllvm -loop-unswitch-threshold=200000 -mllvm -vector-library=LIBMVEC
-mllvm -unroll-threshold=100 -flv-function-specialization

**(Continued on next page)**

## Base Optimization Flags (Continued)

C++ benchmarks (continued):
-mllvm -enable-partial-unswitch -z muldefs -lmvec -lamdlibm
-ljemalloc -lflang

Fortran benchmarks:
-flto -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-vector-library=LIBMVEC
-Wl,-mllvm -Wl,-reduce-array-computations=3 -ffast-math
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop
-Wl,-mllvm -Wl,-enable-iv-split -O3 -march=znver2 -funroll-loops
-Mrecursive -mllvm -vector-library=LIBMVEC -z muldefs
-mllvm -disable-indvar-simplify -mllvm -unroll-aggressive
-mllvm -unroll-threshold=150 -lmvec -lamdlibm -ljemalloc -lflang

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/aocc200-flags-C4.html
http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.0-ROME-RevC.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/aocc200-flags-C4.xml
http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.0-ROME-RevC.xml


SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.


For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.0 on 2020-08-20 00:57:35-0400.
Report generated on 2020-12-28 09:45:52 by CPU2017 PS formatter v6255.
Originally published on 2020-12-22.