# SPEC CPU®2017 Integer Speed Result

## Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)

Tyrone Camarero DS400TG-48R  
(3.00 GHz, Intel Xeon Gold 6248R)

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>006042</th>
<th>Test Date:</th>
<th>Feb-2021</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor:</td>
<td>Netweb Pte Ltd</td>
<td>Hardware Availability:</td>
<td>Aug-2020</td>
</tr>
<tr>
<td>Tested by:</td>
<td>Tyrone Systems</td>
<td>Software Availability:</td>
<td>Dec-2020</td>
</tr>
</tbody>
</table>

## SPECspeed®2017 int_base = 10.1

| SPECspeed®2017_int_peak = 10.4 |

<table>
<thead>
<tr>
<th>threads</th>
<th>SPECspeed®2017_int_base (10.1)</th>
<th>SPECspeed®2017_int_peak (10.4)</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>6.19</td>
<td>14.9</td>
</tr>
<tr>
<td>600.perlbench_s 96</td>
<td>7.26</td>
<td>17.5</td>
</tr>
<tr>
<td>602.gcc_s 96</td>
<td>9.62</td>
<td>4.42</td>
</tr>
<tr>
<td>605.mcf_s 96</td>
<td>7.45</td>
<td>5.39</td>
</tr>
<tr>
<td>620.omnetpp_s 96</td>
<td>12.5</td>
<td>13.9</td>
</tr>
<tr>
<td>623.xalancbmk_s 96</td>
<td>17.5</td>
<td>14.9</td>
</tr>
<tr>
<td>625.x264_s 96</td>
<td>23.4</td>
<td>23.4</td>
</tr>
</tbody>
</table>

## Hardware

CPU Name: Intel Xeon Gold 6248R  
Max MHz: 4000  
Nominal: 3000  
Enabled: 48 cores, 2 chips, 2 threads/core  
Orderable: 1.2 (chip)s  
Cache L1: 32 KB I + 32 KB D on chip per core  
L2: 1 MB I+D on chip per core  
L3: 35.75 MB I+D on chip per chip  
Other: None  
Memory: 384 GB (12 x 32 GB 2Rx4 PC4-2933Y-R)  
Storage: 1 x 480 GB SATA SSD  
Other: None

## Software

OS: CentOS Linux release 8.3.2011  
4.18.0-240.el8.x86_64  
Compiler: C/C++: Version 19.1.1.217 of Intel C/C++ Compiler for Linux Build 20200306;  
Fortran: Version 19.1.1.217 of Intel Fortran Compiler for Linux Build 20200306;  
Parallel: Yes  
Firmware: Version 3.3 released Feb-2020  
File System: xfs  
System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 64-bit  
Other: jemalloc memory allocator V5.0.1  
Power Management: BIOS set to prefer performance at the cost of additional power usage
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**SPECspeed®2017_int_base = 10.1**  
**SPECspeed®2017_int_peak = 10.4**

---

### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Base</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Peak</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>96</td>
<td>289</td>
<td>6.15</td>
<td>287</td>
<td>6.19</td>
<td>286</td>
<td>6.20</td>
<td>245</td>
<td>7.25</td>
<td>244</td>
<td>7.26</td>
<td>244</td>
<td>7.27</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>96</td>
<td>269</td>
<td>17.5</td>
<td>271</td>
<td>17.4</td>
<td>268</td>
<td>17.6</td>
<td>269</td>
<td>17.5</td>
<td>271</td>
<td>17.4</td>
<td>268</td>
<td>17.6</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>96</td>
<td>219</td>
<td>7.45</td>
<td>227</td>
<td>7.19</td>
<td>214</td>
<td>7.61</td>
<td>219</td>
<td>7.45</td>
<td>227</td>
<td>7.19</td>
<td>214</td>
<td>7.61</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>623.xalanchmk_s</td>
<td>96</td>
<td>112</td>
<td>12.6</td>
<td>114</td>
<td>12.4</td>
<td>114</td>
<td>12.5</td>
<td>112</td>
<td>12.6</td>
<td>114</td>
<td>12.4</td>
<td>114</td>
<td>12.5</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>625.x264_s</td>
<td>96</td>
<td>127</td>
<td>13.9</td>
<td>127</td>
<td>13.9</td>
<td>122</td>
<td>14.4</td>
<td>118</td>
<td>14.9</td>
<td>120</td>
<td>14.7</td>
<td>119</td>
<td>14.9</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>96</td>
<td>266</td>
<td>5.39</td>
<td>266</td>
<td>5.38</td>
<td>266</td>
<td>5.39</td>
<td>266</td>
<td>5.39</td>
<td>266</td>
<td>5.38</td>
<td>266</td>
<td>5.39</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>641.leela_s</td>
<td>96</td>
<td>386</td>
<td>4.42</td>
<td>386</td>
<td>4.42</td>
<td>386</td>
<td>4.42</td>
<td>386</td>
<td>4.42</td>
<td>386</td>
<td>4.42</td>
<td>386</td>
<td>4.42</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>96</td>
<td>194</td>
<td>15.2</td>
<td>193</td>
<td>15.2</td>
<td>193</td>
<td>15.2</td>
<td>194</td>
<td>15.2</td>
<td>193</td>
<td>15.2</td>
<td>193</td>
<td>15.2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>657.xz_s</td>
<td>96</td>
<td>264</td>
<td>23.4</td>
<td>264</td>
<td>23.4</td>
<td>265</td>
<td>23.3</td>
<td>264</td>
<td>23.4</td>
<td>265</td>
<td>23.3</td>
<td>265</td>
<td>23.3</td>
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<td></td>
<td></td>
<td></td>
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</tbody>
</table>

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### Compiler Notes

The inconsistent Compiler version information under Compiler Version section is due to a discrepancy in Intel Compiler.  
The correct version of C/C++ compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux  
The correct version of Fortran compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
KMP_AFFINITY = "granularity=fine,scatter"  
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"  
MALLOCONF = "retain=true"  
OMP_STACKSIZE = "192M"

### General Notes

Binaries compiled on a system with 2x Intel Cascade Lake CPU + 384GB RAM  
memory using Centos 8.2 x86_64  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3>/proc/sys/vm/drop_caches

---

(Continued on next page)
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General Notes (Continued)

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

ejemalloc, a general purpose malloc implementation built with the Centos 8.2 x86_64, and the system compiler gcc 8.3.1 sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases

Platform Notes

BIOS Settings:
Power Technology = Custom
Power Performance Tuning = BIOS Controls EPB
ENERGY_PERF_BIAS_CFG mode = Maximum Performance
SNC = Enable
Stale AtoS = Disable
IMC Interleaving = 1-way Interleave
Patrol Scrub = Disable

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6538 of 2020-09-24 e8664e66d2d7080afeaa89d4b38e2f1c running on localhost.localdomain Wed Feb 3 14:38:51 2021

SUT (System Under Test) info as seen by some common utilities. For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6248R CPU @ 3.00GHz
2 "physical id"s (chips)
96 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores: 24
siblings: 48
physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 25 26 27 28 29
physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 25 26 27 28 29

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian

(Continued on next page)
Platform Notes (Continued)

CPU(s):              96
On-line CPU(s) list: 0-95
Thread(s) per core:  2
Core(s) per socket:  24
Socket(s):           2
NUMA node(s):        4
Vendor ID:           GenuineIntel
CPU family:          6
Model:               85
Model name:          Intel(R) Xeon(R) Gold 6248R CPU @ 3.00GHz
Stepping:            7
CPU MHz:             3600.036
CPU max MHz:         4000.0000
CPU min MHz:         1200.0000
BogoMIPS:            6000.00
Virtualization:      VT-x
L1d cache:           32K
L1i cache:           32K
L2 cache:            1024K
L3 cache:            36608K
NUMA node0 CPU(s):   0-3,7-9,13-15,19,20,48-51,55-57,61-63,67,68
NUMA node1 CPU(s):   4-6,10-12,16-18,21-23,52-54,58-60,64-66,69-71
NUMA node2 CPU(s):   24-27,31-33,37-39,43,44,72-75,79-81,85-87,91,92
NUMA node3 CPU(s):   28-30,34-36,40-42,45-47,76-78,82-84,88-90,93-95
Flags:               fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmerpf pni pclmulqdq dtes64 ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtrp pdcm
pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c
rdseed rdrand lahf_lm abm 3nowprefetch cpuid_fault epb cat_l3 cdp_l3 invpcid_single
intel_patin ssbd mba ibrs ibpb stibp ibrs_enhanced fsgsbase tsc_adjust bmi1 hle avx2
smep bmi2 brms invpcid cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt
clwb intel_pt avx512cd avx512bw avx512vl xsaveopt xsavec xsavec xsaveopt xsaveopt
avx 1c qm_mbb_total qm_mbb_local dtmcd ida arat pln pts pku ospke
avx512_vnni md_clear flush_l1d arch_capabilities

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a
physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 7 8 9 13 14 15 19 20 48 49 50 51 55 56 57 61 62 63 67 68
node 0 size: 89655 MB
node 0 free: 78459 MB
node 1 cpus: 4 5 6 10 11 12 16 17 18 21 22 23 52 53 54 58 59 60 64 65 66 69 70 71
node 1 size: 91630 MB

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SPEC CPU®2017 Integer Speed Result

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SPECspeed®2017_int_peak = 10.4

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Test Date: Feb-2021
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Software Availability: Dec-2020

Platform Notes (Continued)

node 1 free: 82100 MB
node 2 cpus: 24 25 26 27 31 32 33 37 38 39 43 44 72 73 74 75 79 80 81 85 86 87 91 92
node 2 size: 91928 MB
node 2 free: 82079 MB
node 3 cpus: 28 29 30 34 40 41 42 45 46 47 76 77 78 82 83 84 88 89 90 93 94 95
node 3 size: 91513 MB
node 3 free: 82160 MB
node distances:
0: 10 11 21 21
1: 11 10 21 21
2: 21 21 10 11
3: 21 21 11 10

From /proc/meminfo
MemTotal: 394848500 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
/sbin/tuned-adm active
  Current active profile: throughput-performance
/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has performance

From /etc/*release* /etc/*version*
centos-release: CentOS Linux release 8.3.2011
centos-release-upstream: Derived from Red Hat Enterprise Linux 8.3
os-release:
  NAME="CentOS Linux"
  VERSION="8"
  ID="centos"
  ID_LIKE="rhel fedora"
  VERSION_ID="8"
  PLATFORM_ID="platform:el8"
  PRETTY_NAME="CentOS Linux 8"
  ANSI_COLOR="0;31"
redhat-release: CentOS Linux release 8.3.2011
system-release: CentOS Linux release 8.3.2011
system-release-cpe: cpe:/o:centos:centos:8

uname -a:
Linux localhost.localdomain 4.18.0-240.el8.x86_64 #1 SMP Fri Sep 25 19:48:47 UTC 2020
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

(Continued on next page)
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Platform Notes (Continued)

CVE-2018-12207 (iTLB Multihit): KVM: Mitigation: Split huge pages
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store
Bypass disabled via prctl and
seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swapgs
barriers and __user pointer
sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB:
conditional, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected

run-level 3 Feb 1 13:57
SPEC is set to: /home/cpu2017
From /sys/devices/virtual/dmi/id
Vendor: Tyrone Systems
Product: Tyrone Camarero DS400TG-48R
Serial: 0123456789
Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
4x NO DIMM NO DIMM
12x Samsung M393A4K40CB2-CVF 32 GB 2 rank 2933, configured at 2934
BIOS:
BIOS Vendor: American Megatrends Inc.
BIOS Version: 3.3
BIOS Date: 02/21/2020
BIOS Revision: 5.14

(End of data from sysinfo program)
Sysinfo incorrectly parsed dmidecode output. Configured memory speed is 2933.
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Compiler Version Notes

==============================================================================
C       | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)
       | 625.x264_s(base, peak) 657.xz_s(base, peak)
------------------------------------------------------------------------------
Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
==============================================================================
C       | 600.perlbench_s(peak)
------------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
==============================================================================
C       | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)
       | 625.x264_s(base, peak) 657.xz_s(base, peak)
------------------------------------------------------------------------------
Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
==============================================================================
C       | 600.perlbench_s(peak)
------------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
==============================================================================
C++     | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak)
       | 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)
------------------------------------------------------------------------------
Intel(R) C++ Compiler for applications running on Intel(R) 64, Version 2021.1
NextGen Build 20200304
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
==============================================================================
Fortran | 648.exchange2_s(base, peak)
------------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)

(Continued on next page)
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Compiler Version Notes (Continued)

64, Version 19.1.1.217 Build 20200306
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-m64 -mnextgen -std=c11
-Wl, -plugin-opt=-x86-branches-within-32B-boundaries -Wl, -z, muldefs
-xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse -funroll-loops
-fuse-ld=gold -qopt-mem-layout-trans=4 -fopenmp -DSPEC_OPENMP
-L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:
-m64 -mnextgen -Wl, -plugin-opt=-x86-branches-within-32B-boundaries
-Wl, -z, muldefs -xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse
-funroll-loops -fuse-ld=gold -qopt-mem-layout-trans=4

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Base Optimization Flags (Continued)

C++ benchmarks (continued):
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin
-lqkmalloc

Fortran benchmarks:
-m64 -Wl,-plugin-opt=-x86-branches-within-32B-boundaries -xCORE-AVX512
-O3 -ipo -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte
-mbranches-within-32B-boundaries

Peak Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Peak Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64(*) -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

(*) Indicates a portability flag that was found in a non-portability variable.
### Peak Optimization Flags (Continued)

600.perlbench_s: -Wl, -z, multdefs -prof-gen(pass 1) -prof-use(pass 2)
-xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-strict-overflow
-mbranches-within-32B-boundaries
-L/usr/local/je5.0.1-64/lib -ljemalloc

602.gcc_s: -m64 -qnextgen -std=c11 -fuse-ld=gold
-ML,-plugin-opt=-x86-branches-within-32B-boundaries
-ML,-z, multdefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -flto
-Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4
-L/usr/local/je5.0.1-64/lib -ljemalloc

605.mcf_s: basepeak = yes

C++ benchmarks:

620.omnetpp_s: basepeak = yes

623.xalancbmk_s: basepeak = yes

631.deepsjeng_s: basepeak = yes

641.leela_s: basepeak = yes

Fortran benchmarks:

648.exchange2_s: basepeak = yes

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The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-CLX-revB.html


You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-CLX-revB.xml

http://www.spec.org/cpu2017/flags/Intel-ic19.1u1-official-linux64_revA.xml
### SPEC CPU®2017 Integer Speed Result

Tyrone Systems  
(Test Sponsor: Netweb Pte Ltd)  
Tyrone Camarero DS400TG-48R  
(3.00 GHz, Intel Xeon Gold 6248R)

<table>
<thead>
<tr>
<th>SPECspeed®2017_int_base</th>
<th>SPECspeed®2017_int_peak</th>
</tr>
</thead>
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<tr>
<td>10.1</td>
<td>10.4</td>
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</tbody>
</table>

| CPU2017 License:       | 006042                  |
| Test Sponsor:          | Netweb Pte Ltd          |
| Tested by:             | Tyrone Systems          |
| Test Date:             | Feb-2021                |
| Hardware Availability: | Aug-2020                |
| Software Availability: | Dec-2020                |

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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