SPEC CPU®2017 Integer Rate Result

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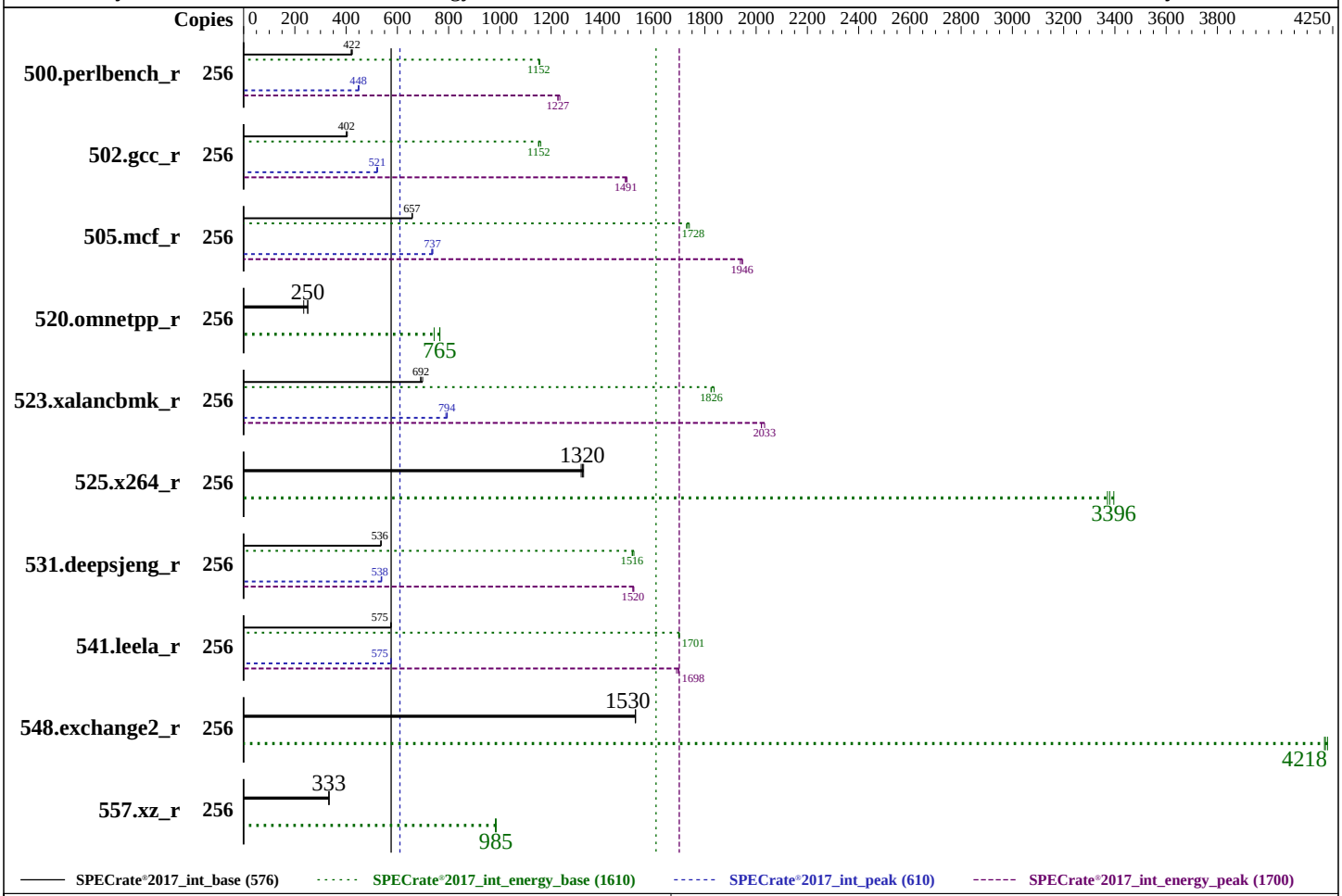
Lenovo Global Technology

ThinkSystem SR645
2.00 GHz, AMD EPYC 7713

SPECrate®2017_int_base =	576
SPECrate®2017_int_energy_base =	1610
SPECrate®2017_int_peak =	610
SPECrate®2017_int_energy_peak =	1700

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Feb-2021
Hardware Availability: Mar-2021
Software Availability: Mar-2021



Hardware

CPU Name:	AMD EPYC 7713
Max MHz:	3675
Nominal:	2000
Enabled:	128 cores, 2 chips, 2 threads/core
Orderable:	1,2 chips
Cache L1:	32 KB I + 32 KB D on chip per core
L2:	512 KB I+D on chip per core
L3:	256 MB I+D on chip per chip, 32 MB shared / 8 cores
Other:	None
Memory:	512 GB (16 x 32 GB 2Rx8 PC4-3200AA-R)
Storage:	1 x 960 GB SATA SSD
Other:	None

Software

OS:	SUSE Linux Enterprise Server 15 SP2 (x86_64) Kernel 5.3.18-22-default
Compiler:	C/C++/Fortran: Version 3.0.0 of AOCC
Parallel:	No
Firmware:	Lenovo BIOS Version D8E115B 2.00 released Feb-2021
File System:	xfs
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	32/64-bit
Other:	jemalloc: jemalloc memory allocator library v5.1.0
Power Management:	BIOS set to balance power and performance

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Power

Max. Power (W): 466.75
Idle Power (W): 162.86
Min. Temperature (C): 23.25
Elevation (m): 43
Line Standard: 220 V / 50 Hz / 1 phase / 3 wires
Provisioning: Line-powered

Power Settings

Management FW: Version 3.00 of D8BT15H
Memory Mode: Normal

Power-Relevant Hardware

Power Supply: 1 x 750 W (non-redundant)
Details: ThinkSystem 750W Titanium Power Supply 4P57A26292
Backplane: 10 x 2.5-inch HDD back plane
Other Storage: None
Storage Model #s: 4XB7A17089
NICs Installed: 1 x ThinkSystem Ethernet 4-port Adaptor @ 1 Gb
NICs Enabled (FW/OS): 4 / 1
NICs Connected/Speed: 1 @ 1 Gb
Other HW Model #s: 8 x High Performance fans

Power Analyzer

Power Analyzer: WIN:9888
Hardware Vendor: YOKOGAWA, Inc.
Model: YokogawaWT310E
Serial Number: C3UD17023E
Input Connection: Default
Metrology Institute: CNAS
Calibration By: GUANG ZHOU GRG METROLOGY & TEST CO.,LTD.
Calibration Label: J202009040176A-0001
Calibration Date: 25-Sep-2020
PTDaemon™ Version: 1.9.1 (a2d19f26; 2019-07-17)
Setup Description: Connected to PSU1
Current Ranges Used: 2.5A
Voltage Range Used: 300V

Temperature Meter

Temperature Meter: WIN:9889
Hardware Vendor: Digi International, Inc.
Model: DigiWATCHPORT_H
Serial Number: W62330940
Input Connection: USB
PTDaemon Version: 1.9.1 (a2d19f26; 2019-07-17)
Setup Description: 50 mm in front of SUT main intake

Base Results Table

Benchmark	Copies	Seconds	Ratio	Energy (kJ)	Energy Ratio	Average Power	Maximum Power	Seconds	Ratio	Energy (kJ)	Energy Ratio	Average Power	Maximum Power	Seconds	Ratio	Energy (kJ)	Energy Ratio	Average Power	Maximum Power
500.perlbench_r	256	971	420	383	1160	394	432	961	424	383	1160	398	432	**965**	**422**	**384**	**1150**	**398**	**429**
502.gcc_r	256	904	401	342	1150	378	416	896	405	340	1160	379	416	**903**	**402**	**342**	**1150**	**379**	**415**
505.mcf_r	256	630	657	260	1740	413	466	**629**	**657**	**262**	**1730**	**416**	**467**	628	659	261	1730	416	456
520.omnetpp_r	256	**1343**	**250**	**475**	**765**	**354**	**377**	1432	235	489	744	342	376	1341	250	475	766	354	380
523.xalancbmk_r	256	387	699	159	1840	412	446	**391**	**692**	**160**	**1830**	**410**	**444**	391	691	161	1820	410	441
525.x264_r	256	338	1330	144	3380	426	456	**339**	**1320**	**143**	**3400**	**422**	**449**	340	1320	144	3370	424	454
531.deepsjeng_r	256	548	535	210	1520	383	397	546	537	209	1520	383	397	**547**	**536**	**210**	**1520**	**384**	**396**
541.leela_r	256	**738**	**575**	**270**	**1700**	**366**	**390**	738	575	270	1700	366	387	737	575	270	1700	366	391
548.exchange2_r	256	439	1530	172	4230	392	403	439	1530	172	4220	392	403	**439**	**1530**	**172**	**4220**	**393**	**404**
557.xz_r	256	**830**	**333**	**305**	**985**	**368**	**396**	830	333	305	985	368	397	830	333	306	983	369	399

SPECrate®2017_int_base = 576

SPECrate®2017_int_energy_base = 1610

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

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CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Feb-2021
Hardware Availability: Mar-2021
Software Availability: Mar-2021

Peak Results Table

Benchmark	Copies	Seconds	Ratio	Energy (kJ)	Energy Ratio	Average Power	Maximum Power	Seconds	Ratio	Energy (kJ)	Energy Ratio	Average Power	Maximum Power	Seconds	Ratio	Energy (kJ)	Energy Ratio	Average Power	Maximum Power
500.perlbench_r	256	906	450	358	1230	396	437	911	448	360	1230	396	445	**909**	**448**	**360**	**1230**	**396**	**445**
502.gcc_r	256	694	522	263	1500	379	428	697	520	264	1490	379	431	**695**	**521**	**264**	**1490**	**380**	**431**
505.mcf_r	256	**561**	**737**	**233**	**1950**	**415**	**457**	560	738	232	1950	415	456	564	734	233	1940	414	458
520.omnetpp_r	256	**1343**	**250**	**475**	**765**	**354**	**377**	1432	235	489	744	342	376	1341	250	475	766	354	380
523.xalancbmk_r	256	**341**	**794**	**144**	**2030**	**423**	**450**	342	789	145	2020	423	452	340	795	145	2020	426	455
525.x264_r	256	338	1330	144	3380	426	456	**339**	**1320**	**143**	**3400**	**422**	**449**	340	1320	144	3370	424	454
531.deepsjeng_r	256	546	537	210	1520	384	396	545	538	209	1520	384	395	**545**	**538**	**210**	**1520**	**385**	**398**
541.leela_r	256	738	574	271	1690	368	396	737	575	270	1700	367	394	**737**	**575**	**270**	**1700**	**366**	**391**
548.exchange2_r	256	439	1530	172	4230	392	403	439	1530	172	4220	392	403	**439**	**1530**	**172**	**4220**	**393**	**404**
557.xz_r	256	**830**	**333**	**305**	**985**	**368**	**396**	830	333	305	985	368	397	830	333	306	983	369	399

SPECrate®2017_int_peak = **610**

SPECrate®2017_int_energy_peak = **1700**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

```
The AMD64 AOCC Compiler Suite is available at
http://developer.amd.com/amd-aocc/
```

Submit Notes

```
The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

'echo 8 > /proc/sys/vm/dirty_ratio' run as root to limit dirty cache to 8% of
memory.
'echo 1 > /proc/sys/vm/swappiness' run as root to limit swap usage to minimum
necessary.
'echo 1 > /proc/sys/vm/zone_reclaim_mode' run as root to free node-local memory
and avoid remote memory usage.
'sync; echo 3 > /proc/sys/vm/drop_caches' run as root to reset filesystem caches.
'sysctl -w kernel.randomize_va_space=0' run as root to disable address space layout
randomization (ASLR) to reduce run-to-run variability.
```

(Continued on next page)

Operating System Notes (Continued)

'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root for peak integer runs and all FP runs to enable Transparent Hugepages (THP).
'echo madvise > /sys/kernel/mm/transparent_hugepage/enabled' run as root for base integer runs to enable THP only on request.

'echo 0 > /proc/sys/kernel/numa_balancing' run as root to eliminate NUMA location probing; the workloads are all bound to specific NUMA nodes.

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH =
"/home/cpu2017-1.1.5-amd-aocc300-milan-A1/amd_rate_aocc300_milan_A_lib/64;/home/cpu2017-1.1.5-amd-aocc300-milan-A1/amd_rate_aocc300_milan_A_lib/32:"
MALLOC_CONF = "retain:true"

Environment variables set by runcpu during the 523.xalancbmk_r peak run:
MALLOC_CONF = "thp:never"

General Notes

Binaries were compiled on a system with 2x AMD EPYC 7742 CPU + 1TiB Memory using openSUSE 15.2

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc: configured and built with GCC v4.8.2 in RHEL 7.4 (No options specified)
jemalloc 5.1.0 is available here:
https://github.com/jemalloc/jemalloc/releases/download/5.1.0/jemalloc-5.1.0.tar.bz2

Platform Notes

BIOS settings:
Operating Mode set to Custom Mode
Core Performance Boost set to Disable
Memory Speed set to 3200MHz

(Continued on next page)

Platform Notes (Continued)

```
SOC P-States set to P3
NUMA nodes per socket set to NPS4
ACPI SRAT L3 Cache as NUMA Domain set to Enable
L2 Stream HW Prefetcher set to Disable
Memory interleaving set to Disabled

Sysinfo program /home/cpu2017-1.1.5-amd-aocc300-milan-A1/bin/sysinfo
Rev: r6538 of 2020-09-24 e8664e66d2d7080afeaa89d4b38e2f1c
running on localhost Fri Feb 26 03:10:31 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
   https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : AMD EPYC 7713 64-Core Processor
      2  "physical id"s (chips)
      256 "processors"
   cores, siblings (Caution: counting these is hw and system dependent. The following
   excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
      cpu cores : 64
      siblings  : 128
      physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
      25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52
      53 54 55 56 57 58 59 60 61 62 63
      physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
      25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52
      53 54 55 56 57 58 59 60 61 62 63

From lscpu:
     Architecture:        x86_64
     CPU op-mode(s):      32-bit, 64-bit
     Byte Order:          Little Endian
     Address sizes:       48 bits physical, 48 bits virtual
     CPU(s):              256
     On-line CPU(s) list: 0-255
     Thread(s) per core:  2
     Core(s) per socket:  64
     Socket(s):           2
     NUMA node(s):        16
     Vendor ID:           AuthenticAMD
     CPU family:          25
     Model:               1
     Model name:          AMD EPYC 7713 64-Core Processor
     Stepping:            1
```

(Continued on next page)

Platform Notes (Continued)

```
      CPU MHz:               1746.090
      CPU max MHz:           2000.0000
      CPU min MHz:           1500.0000
      BogoMIPS:              3992.79
      Virtualization:        AMD-V
      L1d cache:             32K
      L1i cache:             32K
      L2 cache:              512K
      L3 cache:              32768K
      NUMA node0 CPU(s):     0-7,128-135
      NUMA node1 CPU(s):     8-15,136-143
      NUMA node2 CPU(s):     16-23,144-151
      NUMA node3 CPU(s):     24-31,152-159
      NUMA node4 CPU(s):     32-39,160-167
      NUMA node5 CPU(s):     40-47,168-175
      NUMA node6 CPU(s):     48-55,176-183
      NUMA node7 CPU(s):     56-63,184-191
      NUMA node8 CPU(s):     64-71,192-199
      NUMA node9 CPU(s):     72-79,200-207
      NUMA node10 CPU(s):    80-87,208-215
      NUMA node11 CPU(s):    88-95,216-223
      NUMA node12 CPU(s):    96-103,224-231
      NUMA node13 CPU(s):    104-111,232-239
      NUMA node14 CPU(s):    112-119,240-247
      NUMA node15 CPU(s):    120-127,248-255
      Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
     pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm
     constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid aperfmperf pni pclmulqdq
     monitor ssse3 fma cx16 pcid sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c
     rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
     osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb
     cat_l3 cdp_l3 invpcid_single hw_pstate ssbd mba ibrs ibpb stibp vmmcall fsgsbase
     bmi1 avx2 smep bmi2 erms invpcid cqm rdt_a rdseed adx smap clflushopt clwb sha_ni
     xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
     clzero irperf xsaveerptr wbnoinvd arat npt lbrv svm_lock nrip_save tsc_scale
     vmcb_clean flushbyasid decodeassists pausefilter pfthreshold v_vmsave_vmload vgif
     umip pku ospke vaes vpclmulqdq rdpid overflow_recov succor smca

  /proc/cpuinfo cache data
     cache size : 512 KB

  From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a
  physical chip.
    available: 16 nodes (0-15)
    node 0 cpus: 0 1 2 3 4 5 6 7 128 129 130 131 132 133 134 135
```

(Continued on next page)

Platform Notes (Continued)

```
node 0 size: 32064 MB
node 0 free: 31499 MB
node 1 cpus: 8 9 10 11 12 13 14 15 136 137 138 139 140 141 142 143
node 1 size: 32216 MB
node 1 free: 31973 MB
node 2 cpus: 16 17 18 19 20 21 22 23 144 145 146 147 148 149 150 151
node 2 size: 32252 MB
node 2 free: 32074 MB
node 3 cpus: 24 25 26 27 28 29 30 31 152 153 154 155 156 157 158 159
node 3 size: 32250 MB
node 3 free: 32082 MB
node 4 cpus: 32 33 34 35 36 37 38 39 160 161 162 163 164 165 166 167
node 4 size: 32252 MB
node 4 free: 32058 MB
node 5 cpus: 40 41 42 43 44 45 46 47 168 169 170 171 172 173 174 175
node 5 size: 32250 MB
node 5 free: 32083 MB
node 6 cpus: 48 49 50 51 52 53 54 55 176 177 178 179 180 181 182 183
node 6 size: 32252 MB
node 6 free: 32081 MB
node 7 cpus: 56 57 58 59 60 61 62 63 184 185 186 187 188 189 190 191
node 7 size: 32238 MB
node 7 free: 32079 MB
node 8 cpus: 64 65 66 67 68 69 70 71 192 193 194 195 196 197 198 199
node 8 size: 32252 MB
node 8 free: 32103 MB
node 9 cpus: 72 73 74 75 76 77 78 79 200 201 202 203 204 205 206 207
node 9 size: 32250 MB
node 9 free: 32108 MB
node 10 cpus: 80 81 82 83 84 85 86 87 208 209 210 211 212 213 214 215
node 10 size: 32252 MB
node 10 free: 32105 MB
node 11 cpus: 88 89 90 91 92 93 94 95 216 217 218 219 220 221 222 223
node 11 size: 32250 MB
node 11 free: 32111 MB
node 12 cpus: 96 97 98 99 100 101 102 103 224 225 226 227 228 229 230 231
node 12 size: 32252 MB
node 12 free: 32122 MB
node 13 cpus: 104 105 106 107 108 109 110 111 232 233 234 235 236 237 238 239
node 13 size: 32250 MB
node 13 free: 32128 MB
node 14 cpus: 112 113 114 115 116 117 118 119 240 241 242 243 244 245 246 247
node 14 size: 32252 MB
node 14 free: 32130 MB
node 15 cpus: 120 121 122 123 124 125 126 127 248 249 250 251 252 253 254 255
```

(Continued on next page)

Platform Notes (Continued)

```
  node 15 size: 32248 MB
  node 15 free: 32128 MB
  node distances:
  node   0   1   2   3   4   5   6   7   8   9  10  11  12  13  14  15
    0:  10  11  12  12  12  12  12  12  32  32  32  32  32  32  32  32
    1:  11  10  12  12  12  12  12  12  32  32  32  32  32  32  32  32
    2:  12  12  10  11  12  12  12  12  32  32  32  32  32  32  32  32
    3:  12  12  11  10  12  12  12  12  32  32  32  32  32  32  32  32
    4:  12  12  12  12  10  11  12  12  32  32  32  32  32  32  32  32
    5:  12  12  12  12  11  10  12  12  32  32  32  32  32  32  32  32
    6:  12  12  12  12  12  12  10  11  32  32  32  32  32  32  32  32
    7:  12  12  12  12  12  12  11  10  32  32  32  32  32  32  32  32
    8:  32  32  32  32  32  32  32  32  10  11  12  12  12  12  12  12
    9:  32  32  32  32  32  32  32  32  11  10  12  12  12  12  12  12
   10:  32  32  32  32  32  32  32  32  12  12  10  11  12  12  12  12
   11:  32  32  32  32  32  32  32  32  12  12  11  10  12  12  12  12
   12:  32  32  32  32  32  32  32  32  12  12  12  12  10  11  12  12
   13:  32  32  32  32  32  32  32  32  12  12  12  12  11  10  12  12
   14:  32  32  32  32  32  32  32  32  12  12  12  12  12  12  10  11
   15:  32  32  32  32  32  32  32  32  12  12  12  12  12  12  11  10

From /proc/meminfo
   MemTotal:       528162036 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has
   performance

From /etc/*release* /etc/*version*
   os-release:
      NAME="SLES"
      VERSION="15-SP2"
      VERSION_ID="15.2"
      PRETTY_NAME="SUSE Linux Enterprise Server 15 SP2"
      ID="sles"
      ID_LIKE="suse"
      ANSI_COLOR="0;32"
      CPE_NAME="cpe:/o:suse:sles:15:sp2"

uname -a:
   Linux localhost 5.3.18-22-default #1 SMP Wed Jun 3 12:16:43 UTC 2020 (720aeba) x86_64
   x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
```

(Continued on next page)

Platform Notes (Continued)

```
CVE-2018-12207 (iTLB Multihit):                        Not affected
CVE-2018-3620 (L1 Terminal Fault):                     Not affected
Microarchitectural Data Sampling:                      Not affected
CVE-2017-5754 (Meltdown):                              Not affected
CVE-2018-3639 (Speculative Store Bypass):              Mitigation: Speculative Store
                                                       Bypass disabled via prctl and
                                                       seccomp
CVE-2017-5753 (Spectre variant 1):                     Mitigation: usercopy/swapgs
                                                       barriers and __user pointer
                                                       sanitization
CVE-2017-5715 (Spectre variant 2):                     Mitigation: Full AMD retpoline,
                                                      IBPB: conditional, IBRS_FW, STIBP:
                                                       always-on, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort):               Not affected

run-level 3 Feb 26 03:05

SPEC is set to: /home/cpu2017-1.1.5-amd-aocc300-milan-A1
   Filesystem     Type  Size  Used Avail Use% Mounted on
   /dev/sda3      xfs   889G   54G  836G   6% /

From /sys/devices/virtual/dmi/id
    Vendor:         Lenovo
    Product:        ThinkSystem SR645 MB
    Product Family: ThinkSystem
    Serial:         1234567890

Additional information from dmidecode follows.  WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
  Memory:
    16x Samsung M393A4G43AB3-CWE 32 GB 2 rank 3200
    16x Unknown Unknown

BIOS:
   BIOS Vendor:       Lenovo
   BIOS Version:      D8E115B-2.00
   BIOS Date:         02/02/2021
   BIOS Revision:     2.0
   Firmware Revision: 3.0

(End of data from sysinfo program)
```

Compiler Version Notes

```
==============================================================================
C       | 502.gcc_r(peak)
------------------------------------------------------------------------------
AMD clang version 12.0.0 (CLANG: AOCC_3.0.0-Build#78 2020_12_10) (based on
  LLVM Mirror.Version.12.0.0)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.0.0/bin
------------------------------------------------------------------------------

==============================================================================
C       | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak)
        | 525.x264_r(base, peak) 557.xz_r(base, peak)
------------------------------------------------------------------------------
AMD clang version 12.0.0 (CLANG: AOCC_3.0.0-Build#78 2020_12_10) (based on
  LLVM Mirror.Version.12.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.0.0/bin
------------------------------------------------------------------------------

==============================================================================
C       | 502.gcc_r(peak)
------------------------------------------------------------------------------
AMD clang version 12.0.0 (CLANG: AOCC_3.0.0-Build#78 2020_12_10) (based on
  LLVM Mirror.Version.12.0.0)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.0.0/bin
------------------------------------------------------------------------------

==============================================================================
C       | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak)
        | 525.x264_r(base, peak) 557.xz_r(base, peak)
------------------------------------------------------------------------------
AMD clang version 12.0.0 (CLANG: AOCC_3.0.0-Build#78 2020_12_10) (based on
  LLVM Mirror.Version.12.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.0.0/bin
------------------------------------------------------------------------------

==============================================================================
C++     | 523.xalancbmk_r(peak)
------------------------------------------------------------------------------
```

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Compiler Version Notes (Continued)

```
AMD clang version 12.0.0 (CLANG: AOCC_3.0.0-Build#78 2020_12_10) (based on
  LLVM Mirror.Version.12.0.0)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.0.0/bin
------------------------------------------------------------------------------

==============================================================================
C++     | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base)
        | 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)
------------------------------------------------------------------------------
AMD clang version 12.0.0 (CLANG: AOCC_3.0.0-Build#78 2020_12_10) (based on
  LLVM Mirror.Version.12.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.0.0/bin
------------------------------------------------------------------------------

==============================================================================
C++     | 523.xalancbmk_r(peak)
------------------------------------------------------------------------------
AMD clang version 12.0.0 (CLANG: AOCC_3.0.0-Build#78 2020_12_10) (based on
  LLVM Mirror.Version.12.0.0)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.0.0/bin
------------------------------------------------------------------------------

==============================================================================
C++     | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base)
        | 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)
------------------------------------------------------------------------------
AMD clang version 12.0.0 (CLANG: AOCC_3.0.0-Build#78 2020_12_10) (based on
  LLVM Mirror.Version.12.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.0.0/bin
------------------------------------------------------------------------------

==============================================================================
Fortran | 548.exchange2_r(base, peak)
------------------------------------------------------------------------------
AMD clang version 12.0.0 (CLANG: AOCC_3.0.0-Build#78 2020_12_10) (based on
  LLVM Mirror.Version.12.0.0)
Target: x86_64-unknown-linux-gnu
```

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Compiler Version Notes (Continued)

```
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.0.0/bin
------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
clang

C++ benchmarks:
clang++

Fortran benchmarks:
flang

Base Portability Flags

500.perlbench_r: -DSPEC_LINUX_X64 -DSPEC_LP64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LINUX -DSPEC_LP64
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-m64 -Wl,-allow-multiple-definition -Wl,-mllvm -Wl,-enable-licm-vrp
-flto -Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -ffast-math
-march=znver3 -fveclib=AMDLIBM -fstruct-layout=5
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-fremap-arrays -mllvm -function-specialize -flv-function-specialization

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Base Optimization Flags (Continued)

C benchmarks (continued):
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3 -z muldefs
-lamdlibm -ljemalloc -lflang -lflangrti

C++ benchmarks:
-m64 -std=c++98 -Wl,-mllvm -Wl,-do-block-reorder=aggressive -flto
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -ffast-math
-march=znver3 -fveclib=AMDLIBM -mllvm -enable-partial-unswitch
-mllvm -unroll-threshold=100 -finline-aggressive
-flv-function-specialization -mllvm -loop-unswitch-threshold=200000
-mllvm -reroll-loops -mllvm -aggressive-loop-unswitch
-mllvm -extra-vectorizer-passes -mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true -mllvm -convert-pow-exp-to-int=false
-z muldefs -mllvm -do-block-reorder=aggressive
-fvirtual-function-elimination -fvisibility=hidden -lamdlibm
-ljemalloc -lflang -lflangrti

Fortran benchmarks:
-m64 -Wl,-mllvm -Wl,-inline-recursion=4
-Wl,-mllvm -Wl,-lsr-in-nested-loop -Wl,-mllvm -Wl,-enable-iv-split
-flto -Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -ffast-math
-march=znver3 -fveclib=AMDLIBM -z muldefs -mllvm -unroll-aggressive
-mllvm -unroll-threshold=500 -lamdlibm -ljemalloc -lflang -lflangrti

Base Other Flags

C benchmarks:
-Wno-unused-command-line-argument

C++ benchmarks:
-Wno-unused-command-line-argument

Peak Compiler Invocation

C benchmarks:
clang

C++ benchmarks:
clang++

Fortran benchmarks:
flang

Peak Portability Flags

500.perlbench_r: -DSPEC_LINUX_X64 -DSPEC_LP64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LINUX -DSPEC_LP64
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:

500.perlbench_r: -m64 -Wl,-allow-multiple-definition
-Wl,-mllvm -Wl,-enable-licm-vrp -flto
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-fprofile-instr-generate(pass 1)
-fprofile-instr-use(pass 2) -Ofast -march=znver3
-fveclib=AMDLIBM -fstruct-layout=7
-mllvm -unroll-threshold=50 -fremap-arrays
-flv-function-specialization -mllvm -inline-threshold=1000
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=false
-mllvm -function-specialize -mllvm -enable-licm-vrp
-mllvm -reduce-array-computations=3 -lamdlibm -ljemalloc

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Peak Optimization Flags (Continued)

502.gcc_r: -m32 -Wl,-allow-multiple-definition
-Wl,-mllvm -Wl,-enable-licm-vrp -flto
-Wl,-mllvm -Wl,-function-specialize -Ofast -march=znver3
-fveclib=AMDLIBM -fstruct-layout=7
-mllvm -unroll-threshold=50 -fremap-arrays
-flv-function-specialization -mllvm -inline-threshold=1000
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -function-specialize -mllvm -enable-licm-vrp
-mllvm -reduce-array-computations=3 -fgnu89-inline
-ljemalloc

505.mcf_r: -m64 -Wl,-allow-multiple-definition
-Wl,-mllvm -Wl,-enable-licm-vrp -flto
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -fstruct-layout=7
-mllvm -unroll-threshold=50 -fremap-arrays
-flv-function-specialization -mllvm -inline-threshold=1000
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -function-specialize -mllvm -enable-licm-vrp
-mllvm -reduce-array-computations=3 -lamdlibm -ljemalloc

525.x264_r: basepeak = yes

557.xz_r: basepeak = yes

C++ benchmarks:

520.omnetpp_r: basepeak = yes

523.xalancbmk_r: -m32 -Wl,-mllvm -Wl,-do-block-reorder=aggressive -flto
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -finline-aggressive
-mllvm -unroll-threshold=100 -flv-function-specialization
-mllvm -enable-licm-vrp -mllvm -reroll-loops
-mllvm -aggressive-loop-unswitch
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true
-mllvm -do-block-reorder=aggressive

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Peak Optimization Flags (Continued)

523.xalancbmk_r (continued):
-fvirtual-function-elimination -fvisibility=hidden
-ljemalloc

531.deepsjeng_r: -m64 -std=c++98
-Wl,-mllvm -Wl,-do-block-reorder=aggressive -flto
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -finline-aggressive
-mllvm -unroll-threshold=100 -flv-function-specialization
-mllvm -enable-licm-vrp -mllvm -reroll-loops
-mllvm -aggressive-loop-unswitch
-mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true
-mllvm -do-block-reorder=aggressive
-fvirtual-function-elimination -fvisibility=hidden
-lamdlibm -ljemalloc

541.leela_r: Same as 531.deepsjeng_r

Fortran benchmarks:

548.exchange2_r: basepeak = yes

Peak Other Flags

C benchmarks (except as noted below):
-Wno-unused-command-line-argument

502.gcc_r: -L/usr/lib -Wno-unused-command-line-argument
-L/sppo/bin/cpu2017v115aocc3/amd_rate_aocc300_milan_A_lib/32

C++ benchmarks (except as noted below):
-Wno-unused-command-line-argument

523.xalancbmk_r: -L/usr/lib -Wno-unused-command-line-argument
-L/sppo/bin/cpu2017v115aocc3/amd_rate_aocc300_milan_A_lib/32

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Milan-C.html
http://www.spec.org/cpu2017/flags/aocc300-flags-A1.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Milan-C.xml
http://www.spec.org/cpu2017/flags/aocc300-flags-A1.xml

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