Dell Inc.

PowerEdge MX750c (Intel Xeon Gold 6334, 3.60 GHz)

SPECspeed®2017_int_base = 11.8
SPECspeed®2017_int_peak = 12.1

CPU2017 License: 55
Test Date: May-2021
Test Sponsor: Dell Inc.
Tested by: Dell Inc.
Hardware Availability: Apr-2021
Software Availability: Dec-2020

<table>
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</tr>
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<td>11.0</td>
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<td>20.5</td>
</tr>
<tr>
<td>657.xz_s 16</td>
<td></td>
<td>21.0</td>
</tr>
</tbody>
</table>

**Hardware**

CPU Name: Intel Xeon Gold 6334
Max MHz: 3700
Nominal: 3600
Enabled: 16 cores, 2 chips
Orderable: 1.2 chips
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 1.25 MB I+D on chip per core
L3: 18 MB I+D on chip per chip
Other: None
Memory: 512 GB (16 x 32 GB 2Rx8 PC4-3200AA-R)
Storage: 125 GB on tmpfs
Other: None

**Software**

OS: Red Hat Enterprise Linux 8.2 (Ootpa)
Compiler: C/C++: Version 2021.1 of Intel oneAPI DPC++/C++
Compiler Build 20201113 for Linux;
Fortran: Version 2021.1 of Intel Fortran Compiler
Classic Build 20201112 for Linux;
C/C++: Version 2021.1 of Intel C/C++ Compiler
Classic Build 20201112 for Linux
Parallel: Yes
Firmware: Version 1.1.2 released Apr-2021
File System: tmpfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.
## SPEC CPU®2017 Integer Speed Result

**Dell Inc.**
PowerEdge MX750c (Intel Xeon Gold 6334, 3.60 GHz)

<table>
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**Test Date:** May-2021  
**Hardware Availability:** Apr-2021  
**Software Availability:** Dec-2020

### Results Table

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<tr>
<th>Benchmark</th>
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<th>Ratio</th>
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<th>Ratio</th>
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**SPECspeed®2017_int_base =** 11.8  
**SPECspeed®2017_int_peak =** 12.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Environment Variables Notes

Environment variables set by runcpu before the start of the run:

- **KMP_AFFINITY** = "granularity=fine,scatter"
- **LD_LIBRARY_PATH** = "/mnt/ramdisk/cpu2017-1.1.7-ic2021.1/lib/intel64:/mnt/ramdisk/cpu2017-1.1.7-ic2021.1/lib:/mnt/ramdisk/cpu2017-1.1.7-ic2021.1/jre5.0.1-64"
- **MALLOCS_CONF** = "retain:true"
- **OMP_STACKSIZE** = "192M"

### General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
  sync; echo 3> /proc/sys/vm/drop_caches
```

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5


(Continued on next page)
SPEC CPU®2017 Integer Speed Result

Dell Inc.

PowerEdge MX750c (Intel Xeon Gold 6334, 3.60 GHz)

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CPU2017 License: 55
Test Sponsor: Dell Inc.
Test Date: May-2021
Hardware Availability: Apr-2021
Tested by: Dell Inc.
Software Availability: Dec-2020

General Notes (Continued)

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Benchmark run from a 125 GB ramdisk created with the cmd: "mount -t tmpfs -o size=125G tmpfs /mnt/ramdisk"

Platform Notes

BIOS Settings:
- Logical Processor: Disabled
- Virtualization Technology: Disabled
- System Profile: Custom
- CPU Power Management: Maximum Performance
- C1E: Disabled
- C States: Autonomous
- Memory Patrol Scrub: Disabled
- Energy Efficiency Policy: Performance
- CPU Interconnect Bus Link Power Management: Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.7-ic2021.1/bin/sysinfo
Rev: r6538 of 2020-09-24 e8664e66d2d7080afeaa89d4b38e2f1c
running on localhost.localdomain Tue May 4 23:08:32 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
- model name: Intel(R) Xeon(R) Gold 6334 CPU @ 3.60GHz
  - 2 "physical id"s (chips)
  - 16 "processors"
- cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  - cpu cores: 8
  - siblings: 8
  - physical 0: cores 0 1 2 3 4 5 6 7
  - physical 1: cores 0 1 2 3 4 5 6 7

From lscpu:
- Architecture: x86_64

(Continued on next page)
Dell Inc.  
PowerEdge MX750c (Intel Xeon Gold 6334, 3.60 GHz)  

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CPU op-mode(s): 32-bit, 64-bit  
Byte Order: Little Endian

On-line CPU(s) list: 0-15  
Thread(s) per core: 1  
Core(s) per socket: 8  
Socket(s): 2  
NUMA node(s): 2  
Vendor ID: GenuineIntel  
CPU family: 6  
Model: 106  
Model name: Intel(R) Xeon(R) Gold 6334 CPU @ 3.60GHz

CPU MHz: 1295.281  
BogoMIPS: 7200.00  
Virtualization: VT-x  
L1d cache: 48K  
L1i cache: 32K  
L2 cache: 1280K  
L3 cache: 18432K  
NUMA node0 CPU(s): 0,2,4,6,8,10,12,14  
NUMA node1 CPU(s): 1,3,5,7,9,11,13,15

Flags: fpu vme de pse tsc msr pae mce cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vmx flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavevc xckcvb1 xsaves cqm_llc cqm_occupa_llc cqm_mbb_total cqm_mbb_local wbnoinv dtherm ida arat pln pts avx512v bmi umip pku ospe avx512_vbmi12 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid md_clear pconfig flush_l1d arch_capabilities

/proc/cpuinfo cache data  
cache size : 18432 KB

From numactl --hardware  
WARNING: a numactl 'node' might or might not correspond to a physical chip.

available: 2 nodes (0-1)  
node 0 cpus: 0 2 4 6 8 10 12 14  
node 0 size: 257412 MB  
node 0 free: 247554 MB  
node 1 cpus: 1 3 5 7 9 11 13 15  
node 1 size: 258043 MB

(Continued on next page)
SPEC CPU®2017 Integer Speed Result
Copyright 2017-2021 Standard Performance Evaluation Corporation

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CPU2017 License: 55
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Platform Notes (Continued)

node 1 free: 257761 MB
node distances:
node 0 1
0: 10 20
1: 20 10

From /proc/meminfo
MemTotal: 527826616 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/sbin/tuned-adm active
Current active profile: throughput-performance

From /etc/*release* /etc/*version*
  os-release:
    NAME="Red Hat Enterprise Linux"
    VERSION="8.2 (Ootpa)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="8.2"
    PLATFORM_ID="platform:el8"
    PRETTY_NAME="Red Hat Enterprise Linux 8.2 (Ootpa)"
    ANSI_COLOR="0;31"
  redhat-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
  system-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
  system-release-cpe: cpe:/o:redhat:enterprise_linux:8.2:ga

uname -a:
Linux localhost.localdomain 4.18.0-193.el8.x86_64 #1 SMP Fri Mar 27 14:35:58 UTC 2020
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit):
Not affected
CVE-2018-3620 (L1 Terminal Fault):
Not affected
Microarchitectural Data Sampling:
Not affected
CVE-2017-5754 (Meltdown):
Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2018-3639 (Speculative Store Bypass):
Mitigation: usercopy/swapgs barriers and __user pointer sanitization
CVE-2017-5753 (Spectre variant 1):
Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
CVE-2017-5715 (Spectre variant 2):
CVE-2020-0543 (Special Register Buffer Data Sampling): No status reported

(Continued on next page)
SPEC CPU®2017 Integer Speed Result

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CPU2017 License: 55  Test Date: May-2021
Test Sponsor: Dell Inc.  Hardware Availability: Apr-2021
Tested by: Dell Inc.  Software Availability: Dec-2020

Platform Notes (Continued)

CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 May 4 23:05

SPEC is set to: /mnt/ramdisk/cpu2017-1.1.7-ic2021.1

spec

Filesystem Type Size Used Avail Use% Mounted on
tmpfs tmpfs 125G 4.4G 121G 4% /mnt/ramdisk

From /sys/devices/virtual/dmi/id
Vendor: Dell Inc.
Product: PowerEdge MX750c
Product Family: PowerEdge
Serial: 1234567

Additional information from dmidecode follows. WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
1x 002C00B3002C 18ASF4G72PDZ-3G2E1 32 GB 2 rank 3200
15x 00AD063200AD HMAA4GR7AJR8N-XN 32 GB 2 rank 3200
16x Not Specified Not Specified

BIOS:
BIOS Vendor: Dell Inc.
BIOS Version: 1.1.2
BIOS Date: 04/09/2021
BIOS Revision: 1.1

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C | 600.perlbench_s(peak)
-----------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)
64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
-----------------------------------------------------------------------------

==============================================================================
C | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)
 | 625.x264_s(base, peak) 657.xz_s(base, peak)
-----------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Dell Inc.  
PowerEdge MX750c (Intel Xeon Gold 6334, 3.60 GHz)

SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

Dell Inc.  
PowerEdge MX750c (Intel Xeon Gold 6334, 3.60 GHz)

SPECspeed®2017_int_base = 11.8
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CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: May-2021
Hardware Availability: Apr-2021
Software Availability: Dec-2020

Compiler Version Notes (Continued)

Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

______________________________________________________________

C       | 600.perlbench_s(peak)
______________________________________________________________

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

______________________________________________________________

C       | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)
        | 625.x264_s(base, peak) 657.xz_s(base, peak)
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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

______________________________________________________________

C++     | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak)
        | 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)
______________________________________________________________

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

______________________________________________________________

Fortran  | 648.exchange2_s(base, peak)
______________________________________________________________

Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

(Continued on next page)
SPEC CPU®2017 Integer Speed Result

Dell Inc.
PowerEdge MX750c (Intel Xeon Gold 6334, 3.60 GHz)

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Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-DSPEC_OPENMP -std=c11 -m64 -fiopenmp -Wl,-z,muldefs -xCORE-AVX512
-O3 -ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

C++ benchmarks:
-DSPEC_OPENMP -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin/
-llqkmalloc

Fortran benchmarks:
-m64 -xCORE-AVX512 -O3 -ipo -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-mbranches-within-32B-boundaries

Peak Compiler Invocation

C benchmarks (except as noted below):
icx

(Continued on next page)
## Dell Inc. PowerEdge MX750c (Intel Xeon Gold 6334, 3.60 GHz)

<table>
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### Test Date: May-2021

### Hardware Availability: Apr-2021

### Software Availability: Dec-2020

#### Peak Compiler Invocation (Continued)

600.perlbench\textunderscore s: icc

C++ benchmarks:

- icpx

Fortran benchmarks:

- ifort

#### Peak Portability Flags

Same as Base Portability Flags

#### Peak Optimization Flags

C benchmarks:

- 600.perlbench\_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
- \texttt{-xCORE-AVX512 -ipo -O3 -no-prec-div}
- \texttt{-qopt-mem-layout-trans=4 -fno-strict-overflow}
- \texttt{-mbranches\textunderscore within\textunderscore 32B\textunderscore boundaries}
- \texttt{-L/usr/local/jemalloc64\textunderscore 5.0.1/\lib -ljemalloc}

- 602.gcc\_s: -m64 -std=c11 -Wl,-z,muldefs -fprofile\_generate(pass 1)
- \texttt{-fprofile\_use=default.profd\textunderscore ata(pass 2) -xCORE\textunderscore AVX512 -flto}
- \texttt{-O\textunderscore fast(pass 1) -O3 -ffast\textunderscore math -qopt\textunderscore mem\textunderscore layout\textunderscore trans=4}
- \texttt{-mbranches\textunderscore within\textunderscore 32B\textunderscore boundaries}
- \texttt{-L/usr/local/jemalloc64\textunderscore 5.0.1/\lib -ljemalloc}

- 605.mcf\_s: basepeak = yes

- 625.x264\_s: -DSPEC\_OPENMP -fiopenmp -std=c11 -m64 -Wl,-z,muldefs
- \texttt{-xCORE\textunderscore AVX512 -flto -O3 -ffast\textunderscore math}
- \texttt{-qopt\textunderscore mem\textunderscore layout\textunderscore trans=4 -fno-alias}
- \texttt{-mbranches\textunderscore within\textunderscore 32B\textunderscore boundaries}
- \texttt{-L/usr/local/jemalloc64\textunderscore 5.0.1/\lib -ljemalloc}

- 657.xz\_s: basepeak = yes

C++ benchmarks:

(Continued on next page)
## SPEC CPU®2017 Integer Speed Result

**Dell Inc.**

PowerEdge MX750c (Intel Xeon Gold 6334, 3.60 GHz)

<table>
<thead>
<tr>
<th>SPECspeed®2017_int_base = 11.8</th>
<th>SPECspeed®2017_int_peak = 12.1</th>
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<tr>
<td><strong>CPU2017 License:</strong> 55</td>
<td><strong>Test Date:</strong> May-2021</td>
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<td><strong>Test Sponsor:</strong> Dell Inc.</td>
<td><strong>Hardware Availability:</strong> Apr-2021</td>
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<tr>
<td><strong>Tested by:</strong> Dell Inc.</td>
<td><strong>Software Availability:</strong> Dec-2020</td>
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### Peak Optimization Flags (Continued)

- `620.omnetpp_s`: `basepeak = yes`
- `623.xalancbmk_s`: `basepeak = yes`
- `631.deepsjeng_s`: `basepeak = yes`
- `641.leela_s`: `basepeak = yes`

**Fortran benchmarks:**

- `648.exchange2_s`: `basepeak = yes`

The flags files that were used to format this result can be browsed at:


You can also download the XML flags sources by saving the following links:


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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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