### Dell Inc.

**PowerEdge XR11 (Intel Xeon Gold 6354, 3.00 GHz)**

<table>
<thead>
<tr>
<th>CPU2017 License: 55</th>
<th>Test Date: Apr-2021</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor: Dell Inc.</td>
<td>Hardware Availability: Jul-2021</td>
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<tr>
<td>Tested by: Dell Inc.</td>
<td>Software Availability: Feb-2021</td>
</tr>
</tbody>
</table>

**SPECrater®2017_int_base = 155**

**SPECrater®2017_int_peak = 160**

<table>
<thead>
<tr>
<th>SPECrate</th>
<th>2017 Int Peak</th>
<th>SPECrate</th>
<th>2017 Int Base</th>
</tr>
</thead>
<tbody>
<tr>
<td>perlbench_r</td>
<td>36</td>
<td>123</td>
<td>128</td>
</tr>
<tr>
<td>gcc_r</td>
<td>36</td>
<td>129</td>
<td>254</td>
</tr>
<tr>
<td>mcf_r</td>
<td>36</td>
<td>97.9</td>
<td>198</td>
</tr>
<tr>
<td>omnetpp_r</td>
<td>36</td>
<td>254</td>
<td></td>
</tr>
<tr>
<td>xalancbmk_r</td>
<td>36</td>
<td>319</td>
<td></td>
</tr>
<tr>
<td>x264_r</td>
<td>36</td>
<td>333</td>
<td></td>
</tr>
<tr>
<td>deepsjeng_r</td>
<td>36</td>
<td>119</td>
<td></td>
</tr>
<tr>
<td>leela_r</td>
<td>36</td>
<td>117</td>
<td></td>
</tr>
<tr>
<td>exchange2_r</td>
<td>36</td>
<td>323</td>
<td></td>
</tr>
<tr>
<td>xz_r</td>
<td>36</td>
<td>84.9</td>
<td></td>
</tr>
</tbody>
</table>

---

### Hardware

**CPU Name:** Intel Xeon Gold 6354  
**Max MHz:** 3600  
**Nominal:** 3000  
**Enabled:** 18 cores, 1 chip, 2 threads/core  
**Orderable:** 1 chip  
**Cache L1:** 32 KB I + 48 KB D on chip per core  
**L2:** 1.25 MB I+D on chip per core  
**L3:** 39 MB I+D on chip per chip  
**Other:** None  
**Memory:** 512 GB (8 x 64 GB 2Rx4 PC4-3200AA-R)  
**Storage:** 225 GB on tmpfs  
**Other:** None

---

### Software

**OS:** Red Hat Enterprise Linux 8.3 (Ootpa)  
4.18.0-240.15.1.el8_3.x86_64  
**Compiler:** C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux;  
Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux;  
**C/C++: Version 2021.1 of Intel C/C++ Compiler Classic Build 20201112 for Linux**  
**Parallel:** No  
**Firmware:** Version 0.6.2 released Apr-2021  
**File System:** tmpfs  
**System State:** Run level 5 (graphical multi-user)  
**Base Pointers:** 64-bit  
**Peak Pointers:** 32/64-bit  
**Other:** jemalloc memory allocator V5.0.1  
**Power Management:** BIOS and OS set to prefer performance at the cost of additional power usage.
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CPU2017 License: 55
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Tested by: Dell Inc.

SPECrate®2017_int_base = 155
SPECrate®2017_int_peak = 160

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Base Copies</th>
<th>Base Seconds</th>
<th>Base Ratio</th>
<th>Peak Copies</th>
<th>Peak Seconds</th>
<th>Peak Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>36</td>
<td>543</td>
<td>106</td>
<td>36</td>
<td>467</td>
<td>123</td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>36</td>
<td>393</td>
<td>130</td>
<td>36</td>
<td>342</td>
<td>149</td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>36</td>
<td>229</td>
<td>254</td>
<td>36</td>
<td>229</td>
<td>254</td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>36</td>
<td>482</td>
<td>97.9</td>
<td>36</td>
<td>482</td>
<td>97.9</td>
</tr>
<tr>
<td>523.xalancbmk_r</td>
<td>36</td>
<td>191</td>
<td>199</td>
<td>36</td>
<td>191</td>
<td>199</td>
</tr>
<tr>
<td>525.x264_r</td>
<td>36</td>
<td>197</td>
<td>320</td>
<td>36</td>
<td>198</td>
<td>319</td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
<td>36</td>
<td>347</td>
<td>119</td>
<td>36</td>
<td>347</td>
<td>119</td>
</tr>
<tr>
<td>541.leela_r</td>
<td>36</td>
<td>511</td>
<td>117</td>
<td>36</td>
<td>511</td>
<td>117</td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>36</td>
<td>291</td>
<td>324</td>
<td>36</td>
<td>291</td>
<td>324</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>36</td>
<td>457</td>
<td>85.0</td>
<td>36</td>
<td>458</td>
<td>84.9</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes
The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = 
"/mnt/ramdisk/cpu2017-1.1.5-ic2021.1/lib/intel64:/mnt/ramdisk/cpu2017-1.1.5-ic2021.1/lib/ia32:/mnt/ramdisk/cpu2017-1.1.5-ic2021.1/je5.0.1-32"
MALLOCONF = "retain:true"

General Notes
Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Red Hat Enterprise Linux 8.1
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with: sync; echo 3>/proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
SPEC CPU®2017 Integer Rate Result

Dell Inc. PowerEdge XR11 (Intel Xeon Gold 6354, 3.00 GHz)

Listening to: Dell Inc.
Tested by: Dell Inc.

General Notes (Continued)

numactl --interleave=all runcpu <etc>
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Benchmark run from a 225 GB ramdisk created with the cmd: "mount -t tmpfs -o size=225G tmpfs /mnt/ramdisk"

Platform Notes

BIOS Settings:
  Sub NUMA Cluster : 2-Way Clustering
  Virtualization Technology : Disabled

  System Profile : Custom
  CPU Power Management : Maximum Performance
    C1E : Disabled
    C States : Autonomous
    Memory Patrol Scrub : Disabled
    Energy Efficiency Policy : Performance
  CPU Interconnect Bus Link
    Power Management : Disabled

Syseinfo program /mnt/ramdisk/cpu2017-1.1.5-ic2021.1/bin/sysinfo
Rev: r6538 of 2020-09-24 e8664e66d2d7080afeaa89d4b38e2f1c
running on localhost.localdomain Thu Apr 29 03:37:27 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
  https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) Gold 6354 CPU @ 3.00GHz
  1 "physical id"s (chips)
    36 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 18
  siblings : 36

(Continued on next page)
Dell Inc.

PowerEdge XR11 (Intel Xeon Gold 6354, 3.00 GHz)

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Tested by: Dell Inc.

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Platform Notes (Continued)

physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17

From lscpu:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 36
On-line CPU(s) list: 0-35
Thread(s) per core: 2
Core(s) per socket: 18
Socket(s): 1
NUMA node: 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 106
Model name: Intel(R) Xeon(R) Gold 6354 CPU @ 3.00GHz
Stepping: 6
CPU MHz: 3600.000
BogoMIPS: 6000.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 39936K
NUMA node 0 CPU(s): 0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30,32,34
NUMA node 1 CPU(s): 1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31,33,35
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpref pni pclmulqdq dtes64monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtrp pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3nowprefetch cpuid_fault epb cat_l3 invpcid_single
intel_ppin ssbd mba ibrs ibpb stibp ibrs_enhanced fsgsgsbase tsc_adjust bmi1 hle avx2
smep bmi2  erms invpcid cqg rdt_a avx512f avx512dq rdseed adx smap avx512ifma
cflushopt clwb intel_pt avx512cd sha_hl avx512bw avx512vl xsaveopt xsaves xgetbv1
xsaves cmq_l1c cmq_occopus llc cmq_mb_mtotal cmq_mb_mlocal split_lock_detect wbinvd
dtherm ida arat pfn pts avx512vbmi umip pkup ospe avx512_vbmi2 gfni vaes vpclmulqdp
avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid md_clear pconfi flush_l1d
arch_capabilities

/platform/cpuinfo cache data
cache size : 39936 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0 2 4 6 8 10 12 14 16 18 20 22 24 26 28 30 32 34

(Continued on next page)
Dell Inc.
PowerEdge XR11 (Intel Xeon Gold 6354, 3.00 GHz)

SPEC CPU®2017 Integer Rate Result

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Dell Inc.

SPECrate®2017_int_base = 155
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CPU2017 License: 55
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Platform Notes (Continued)

node 0 size: 250442 MB
node 0 free: 255922 MB
node 1 cpus: 1 3 5 7 9 11 13 15 17 19 21 23 25 27 29 31 33 35
node 1 size: 250565 MB
node 1 free: 242339 MB
node distances:
node 0 1
  0: 10 11
  1: 11 10

From /proc/meminfo
  MemTotal: 527814304 kB
  HugePages_Total: 0
  Hugepagesize: 2048 kB

/sbin/tuned-adm active
  Current active profile: throughput-performance

From /etc/*release* /etc/*version*
  os-release:
    NAME="Red Hat Enterprise Linux"
    VERSION="8.3 (Ootpa)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="8.3"
    PLATFORM_ID="platform:el8"
    PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"
    ANSI_COLOR="0;31"
  redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
  system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
  system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga

uname -a:
  Linux localhost.localdomain 4.18.0-240.15.1.el8_3.x86_64 #1 SMP Wed Feb 3 03:12:15 EST 2021 x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2018-3639 (Speculative Store Bypass):
CVE-2017-5753 (Spectre variant 1):
  Mitigation: usercopy/swapgs barriers and __user pointer
Dell Inc.

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**SPECrate®2017_int_base = 155**

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### Platform Notes (Continued)

CVE-2017-5715 (Spectre variant 2): sanitization
Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 5 Apr 29 03:35

SPEC is set to: /mnt/ramdisk/cpu2017-1.1.5-ic2021.1

Filesystem Type Size Used Avail Use% Mounted on
tmpfs tmpfs 225G 7.0G 219G 4% /mnt/ramdisk

From /sys/devices/virtual/dmi/id
Vendor: Dell Inc.
Product: PowerEdge XR11
Product Family: PowerEdge
Serial: 09A000K

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
4x 00AD00B300AD HMAA8GR7AJR4N-XN 64 GB 2 rank 3200
1x 00AD063200AD HMAA8GR7AJR4N-XN 64 GB 2 rank 3200
3x 00AD069D00AD HMAA8GR7AJR4N-XN 64 GB 2 rank 3200

BIOS:
BIOS Vendor: Dell Inc.
BIOS Version: 0.6.2
BIOS Date: 04/12/2021
BIOS Revision: 0.6

(End of data from sysinfo program)

---

### Compiler Version Notes

==============================================================================
C | 500.perlbench_r(peak)
==============================================================================

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)
64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================

(Continued on next page)
Compiler Version Notes (Continued)

C       | 502.gcc_r(peak)
-------------------------------

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
C       | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
| 525.x264_r(base, peak) 557.xz_r(base, peak)
-------------------------------

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
C       | 500.perlbench_r(peak)
-------------------------------

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
C       | 502.gcc_r(peak)
-------------------------------

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
C       | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
| 525.x264_r(base, peak) 557.xz_r(base, peak)
-------------------------------

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
C       | 500.perlbench_r(peak)
-------------------------------

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
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**Dell Inc.**

PowerEdge XR11 (Intel Xeon Gold 6354, 3.00 GHz)

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**Compiler Version Notes (Continued)**

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**Base Compiler Invocation**

C benchmarks:

- icx

C++ benchmarks:

- icpx

Fortran benchmarks:

- ifort
SPEC CPU®2017 Integer Rate Result

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Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
-1qkmalloc

C++ benchmarks:
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
-1qkmalloc

Fortran benchmarks:
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ipo -no-prec-div
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
-auto -mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
-1qkmalloc

Peak Compiler Invocation

C benchmarks (except as noted below):
icx

500.perlbench_r: icx

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Peak Compiler Invocation (Continued)

C++ benchmarks:
icpx

Fortran benchmarks:
ifort

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:

500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-strict-overflow
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
-lqkmalloc

502.gcc_r: -m32
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/ia32_lin
-std=gnu89 -Wl,-z,muldefs -fprofile-generate(pass1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -flto
-Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc32-5.0.1/lib -ljemalloc

505.mcf_r: basepeak = yes

525.x264_r: -w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -flto
-O3 -ffast-math -qopt-mem-layout-trans=4 -fno-alias

(Continued on next page)
Dell Inc.  

PowerEdge XR11 (Intel Xeon Gold 6354, 3.00 GHz)  

**SPEC CPU®2017 Integer Rate Result**  

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</table>

**SPECrate®2017_int_base = 155**  

**SPECrate®2017_int_peak = 160**  

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### Peak Optimization Flags (Continued)

525.x264_r (continued):
- mbranches-within-32B-boundaries
- L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
- Lqkmalloc

557.xz_r: basepeak = yes

C++ benchmarks:

520.omnetpp_r: basepeak = yes
523.xalancbmk_r: basepeak = yes
531.deepsjeng_r: basepeak = yes
541.leela_r: basepeak = yes

Fortran benchmarks:

548.exchange2_r: basepeak = yes

---

The flags files that were used to format this result can be browsed at:


You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml  

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.5 on 2021-04-29 04:37:27-0400.  
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