Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL380 Gen10 Plus
(2.40 GHz, Intel Xeon Platinum 8368)

SPECspeed®2017_fp_base = 227
SPECspeed®2017_fp_peak = 230

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Hardware

CPU Name: Intel Xeon Platinum 8368
Max MHz: 3400
Nominal: 2400
Enabled: 76 cores, 2 chips
Orderable: 1, 2 chip(s)
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 1.25 MB I+D on chip per core
L3: 57 MB I+D on chip per chip
Other: None
Memory: 2 TB (32 x 64 GB 2Rx4 PC4-3200AA-R)
Storage: 1 x 800 GB SAS SSD, RAID 0
Other: None

Software

OS: Red Hat Enterprise Linux 8.3 (Ootpa)
Kernel 4.18.0-240.el8.x86_64
Compiler: C/C++: Version 2021.1 of Intel oneAPI DPC++/C++
Compiler Build 20201113 for Linux;
Fortran: Version 2021.1 of Intel Fortran Compiler
Classic Build 20201112 for Linux;
C/C++: Version 2021.1 of Intel C/C++ Compiler
Classic Build 20201112 for Linux
Parallel: Yes
Firmware: HPE BIOS Version U46 v1.42 05/16/2021 released May-2021
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS set to prefer performance at the cost of additional power usage
**SPEC CPU®2017 Floating Point Speed Result**

**Hewlett Packard Enterprise**
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**SPECspeed®2017_fp_base = 227**

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<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Base</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>76</td>
<td>84.7</td>
<td>696</td>
<td></td>
<td>85.3</td>
<td></td>
<td>85.0</td>
<td></td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>76</td>
<td>57.5</td>
<td>290</td>
<td><strong>20</strong></td>
<td>57.2</td>
<td>291</td>
<td>58.0</td>
<td>288</td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>76</td>
<td>36.4</td>
<td>144</td>
<td></td>
<td>35.7</td>
<td>147</td>
<td>36.0</td>
<td>145</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>76</td>
<td>63.3</td>
<td>209</td>
<td></td>
<td>62.2</td>
<td>213</td>
<td>62.1</td>
<td>213</td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>76</td>
<td>50.9</td>
<td>174</td>
<td><strong>180</strong></td>
<td>49.2</td>
<td>180</td>
<td>49.4</td>
<td>180</td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>76</td>
<td>129</td>
<td>91.9</td>
<td></td>
<td>127</td>
<td>93.4</td>
<td>128</td>
<td>93.1</td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>76</td>
<td>58.7</td>
<td>246</td>
<td></td>
<td>58.9</td>
<td>245</td>
<td>58.7</td>
<td>246</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>76</td>
<td>36.3</td>
<td>483</td>
<td></td>
<td>36.1</td>
<td>483</td>
<td>36.2</td>
<td>482</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>76</td>
<td>78.3</td>
<td>116</td>
<td></td>
<td>78.4</td>
<td>116</td>
<td>78.4</td>
<td>116</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>76</td>
<td>61.2</td>
<td>257</td>
<td><strong>254</strong></td>
<td>63.2</td>
<td>249</td>
<td>61.9</td>
<td>254</td>
</tr>
</tbody>
</table>

Results Table

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

**Operating System Notes**

- Stack size set to unlimited using "ulimit -s unlimited"
- Transparent Huge Pages enabled by default
- Prior to runcpu invocation
- Filesystem page cache synced and cleared with: `sync; echo 3 > /proc/sys/vm/drop_caches`

**Environment Variables Notes**

- Environment variables set by runcpu before the start of the run:
  - KMP_AFFINITY = "granularity=fine,compact"
  - LD_LIBRARY_PATH = "/home/cpu2017_1.1.8/lib/intel64:/home/cpu2017_1.1.8/je5.0.1-64"
  - MALLOC_CONF = "retain:true"
  - OMP_STACKSIZE = "192M"

**General Notes**

- Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0
- NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
- Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
- Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
- jemalloc, a general purpose malloc implementation

(Continued on next page)
General Notes (Continued)

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Submitted by: "Bhatnagar, Prateek" <prateek.bhatnagar@hpe.com>
Submitted: Mon Jun 21 10:33:38 EDT 2021
Submission: cpu2017-20210621-27587.sub

Platform Notes

The system ROM used for this result contains Intel microcode version 0xd0002a0 for
the Intel Xeon Platinum 8368 processor
BIOS Configuration:
Workload Profile set to General Peak Frequency Compute
Intel Hyper-Threading set to Disabled
Thermal Configuration set to Maximum Cooling
Memory Patrol Scrubbing set to Disabled
Advanced Memory Protection set to Advanced ECC
Last Level Cache (LLC) Prefetch set to Enabled
Last Level Cache (LLC) Dead Line Allocation set to Disabled
Enhanced Processor Performance set to Enabled
Workload Profile set to Custom
  Energy/Performance Bias set to Balanced Power
  DCU Stream Prefetcher set to Disabled
  Adjacent Sector Prefetch set to Disabled
  Minimum Processor Idle Power Package C-State set to No Package State
  Numa Group Size Optimization set to Flat

Sysinfo program /home/cpu2017_1.1.8/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b5589ef0e16aca6c64d
running on localhost.localdomain Fri Jun 22 20:41:01 2018

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) Platinum 8368 CPU @ 2.40GHz
  2 "physical id"s (chips)
  76 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 38
siblings : 38
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
  25 26 27 28 29 30 31 32 33 34 35 36 37
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
Hewlett Packard Enterprise
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SPECspeed®2017_fp_base = 227
SPECspeed®2017_fp_peak = 230

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Platform Notes (Continued)

From lscpu from util-linux 2.32.1:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 76
On-line CPU(s) list: 0-75
Thread(s) per core: 1
Core(s) per socket: 38
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 106
Model name: Intel(R) Xeon(R) Platinum 8368 CPU @ 2.40GHz
Stepping: 6
CPU MHz: 3405.537
BogoMIPS: 4800.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 58368K
NUMA node0 CPU(s): 0-37
NUMA node1 CPU(s): 38-75
Flags:

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27

(Continued on next page)
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SPECspeed®2017_fp_peak = 230  
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<td>Software Availability: Jun-2021</td>
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</table>

**Platform Notes (Continued)**

| node 0 size: 971008 MB |
| node 0 free: 1024887 MB |
| node 1 cpus: 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 |
| node 1 size: 970840 MB |
| node 1 free: 1030588 MB |

node distances:  
0: 10 20  
1: 20 10

From `/proc/meminfo`  
MemTotal: 2113485712 kB  
HugePages_Total: 0  
Hugepagesize: 2048 kB

/sbin/tuned-adm active  
Current active profile: throughput-performance

From `/etc/*release*/etc/*version*`  
os-release:  
NAME="Red Hat Enterprise Linux"  
VERSION="8.3 (Ootpa)"  
ID="rhel"  
ID_LIKE="fedora"  
VERSION_ID="8.3"  
PLATFORM_ID="platform:el8"  
PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"  
ANSI_COLOR="0;31"  
redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)  
system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)  
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga

uname -a:  
Linux localhost.localdomain 4.18.0-240.el8.x86_64 #1 SMP Wed Sep 23 05:13:10 EDT 2020  
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:  
CVE-2018-12207 (iTLB Multihit): Not affected  
CVE-2018-3620 (L1 Terminal Fault): Not affected  
Microarchitectural Data Sampling: Not affected  
CVE-2017-5754 (Meltdown): Not affected  
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp

(Continued on next page)
## Platform Notes (Continued)

CVE-2017-5753 (Spectre variant 1):
Mitigation: usercopy/swaps barriers and __user pointer sanitization

CVE-2017-5715 (Spectre variant 2):
Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected

CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Jun 22 16:42

SPEC is set to: /home/cpu2017_1.1.8

Filesystem            Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs   670G  219G  451G  33% /home

From /sys/devices/virtual/dmi/id
Vendor:         HPE
Product:        ProLiant DL380 Gen10 Plus
Product Family: ProLiant
Serial:         CN70490X8B

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard. Memory:
32x Micron 36ASF8G72PZ-3G2B2 64 GB 2 rank 3200

BIOS:
BIOS Vendor:       HPE
BIOS Version:      U46
BIOS Date:         05/16/2021
BIOS Revision:     1.42
Firmware Revision: 2.50

(End of data from sysinfo program)

## Compiler Version Notes

```
| C               | 619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base) |
```

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

(Continued on next page)
Hewlett Packard Enterprise
ProLiant DL380 Gen10 Plus
(2.40 GHz, Intel Xeon Platinum 8368)

SPECspeed\textsuperscript{\textregistered}2017\textsuperscript{\textregistered}\textsubscript{fp}\textsuperscript{peak} = 230
SPECspeed\textsuperscript{\textregistered}2017\textsuperscript{\textregistered}\textsubscript{fp}\textsubscript{base} = 227

CPU2017 License: 3
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Test Date: Jun-2021
Hardware Availability: Jun-2021

Tested by: HPE
Software Availability: Jun-2021

Compiler Version Notes (Continued)

<table>
<thead>
<tr>
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<th>644.nab_s(peak)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,</td>
<td></td>
</tr>
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<td>Version 2021.1 Build 20201113</td>
<td></td>
</tr>
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<td></td>
</tr>
</tbody>
</table>

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</tbody>
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</tr>
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</tr>
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<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>C++, C, Fortran</th>
<th>607.cactuBSSN_s(base, peak)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Intel(R) C++ Intel(R) 64 Compiler Classic for applications running on</td>
<td></td>
</tr>
<tr>
<td>Intel(R) 64, Version 2021.1 Build 20201112_000000</td>
<td></td>
</tr>
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<td></td>
</tr>
<tr>
<td>Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)</td>
<td></td>
</tr>
<tr>
<td>Version 2021.1 Build 20201112_000000</td>
<td></td>
</tr>
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<td></td>
</tr>
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<td></td>
</tr>
<tr>
<td>Intel(R) 64, Version 2021.1 Build 20201112_000000</td>
<td></td>
</tr>
<tr>
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<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Fortran</th>
<th>603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak) 654.roms_s(base, peak)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on</td>
<td></td>
</tr>
<tr>
<td>Intel(R) 64, Version 2021.1 Build 20201112_000000</td>
<td></td>
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**SPEC CPU®2017 Floating Point Speed Result**

Copyright 2017-2021 Standard Performance Evaluation Corporation

### Hewlett Packard Enterprise
(2.40 GHz, Intel Xeon Platinum 8368)

#### SPECspeed®2017 fp_base = 227

#### SPECspeed®2017 fp_peak = 230

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</tr>
</thead>
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</tbody>
</table>

---

### Compiler Version Notes (Continued)

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**Base Compiler Invocation**

- **C benchmarks:**
  
  icc

- **Fortran benchmarks:**
  
  ifort

- **Benchmarks using both Fortran and C:**
  
  ifort icc

- **Benchmarks using Fortran, C, and C++:**
  
  icpc icc ifort

---

### Base Portability Flags

- 603.bwaves_s: -DSPEC_LP64
- 607.cactuBSSN_s: -DSPEC_LP64
- 619.lbm_s: -DSPEC_LP64
- 621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
- 627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
- 628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian -assume byterecl
- 638.imagick_s: -DSPEC_LP64
- 644.nab_s: -DSPEC_LP64
- 649.fotonik3d_s: -DSPEC_LP64
- 654.roms_s: -DSPEC_LP64
Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL380 Gen10 Plus
(2.40 GHz, Intel Xeon Platinum 8368)

SPECspeed®2017_fp_base = 227
SPECspeed®2017_fp_peak = 230

Base Optimization Flags

C benchmarks:
-m64 -std=c11 -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-mbranches-within-32B-boundaries

Fortran benchmarks:
-m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX512 -ipo -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -qopenmp -nostandard-realloc-lhs
-mbranches-within-32B-boundaries -L/usr/local/jemalloc64-5.0.1/lib
-ljemalloc

Benchmarks using both Fortran and C:
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
-DSPEC_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Benchmarks using Fortran, C, and C++:
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp
-DSPEC_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

644.nab_s: icx

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort
Hewlett Packard Enterprise
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| SPECspeed®2017_fp_base = 227 |
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**CPU2017 License:** 3  \hspace{1cm} **Test Date:** \hspace{1cm} Jun-2021
**Test Sponsor:** HPE  \hspace{1cm} **Hardware Availability:** \hspace{1cm} Jun-2021
**Tested by:** HPE  \hspace{1cm} **Software Availability:** Jun-2021

---

### Peak Portability Flags

Same as Base Portability Flags

---

### Peak Optimization Flags

**C benchmarks:**

- 619.lbm_s: basepeak = yes
- 638.imagick_s: basepeak = yes
- 644.nab_s: -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
  -flto -mfpmath=sse -funroll-loops -fiopenmp
  -DSPEC_OPENMP -gopt-mem-layout-trans=4
  -fimf-accuracy-bits=14:sqrt
  -mbranches-within-32B-boundaries
  -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

**Fortran benchmarks:**

- 603.bwaves_s: basepeak = yes
- 649.fotonik3d_s: -m64 -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
  -DSPEC_SUPPRESS_OPENMP -DSPEC_OPENMP -ipo -xCORE-AVX512
  -O3 -no-prec-div -gopt-prefetch -ffinite-math-only
  -gopt-mem-layout-trans=4 -gopenmp -nstandard-realloc-lhs
  -mbranches-within-32B-boundaries
  -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

- 654.roms_s: basepeak = yes

**Benchmarks using both Fortran and C:**

- 621.wrf_s: basepeak = yes
- 627.cam4_s: basepeak = yes
- 628.pop2_s: basepeak = yes

**Benchmarks using Fortran, C, and C++:**

- 607.cactuBSSN_s: basepeak = yes
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**Software Availability:** Jun-2021  

The flags files that were used to format this result can be browsed at:
- [http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.0-ICX-revC.html](http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.0-ICX-revC.html)

You can also download the XML flags sources by saving the following links:
- [http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.0-ICX-revC.xml](http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.0-ICX-revC.xml)

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