## SPEC CPU®2017 Integer Rate Result

**Inspur Corporation**

Inspur NF5266M6 (Intel Xeon Platinum 8352V)

<table>
<thead>
<tr>
<th>Copy (434)</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate\textsuperscript{®}2017_int_peak</td>
</tr>
<tr>
<td>434</td>
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**CPU2017 License:** 3358  
**Test Sponsor:** Inspur Corporation  
**Tested by:** Inspur Corporation  
**Test Date:** Jun-2021  
**Hardware Availability:** May-2021  
**Software Availability:** Dec-2020

### Hardware

| CPU Name: Intel Xeon Platinum 8352V  
| Max MHz: 3500  
| Nominal: 2100  
| Enabled: 72 cores, 2 chips, 2 threads/core  
| Orderable: 1.2 chips  
| Cache L1: 32 KB I + 48 KB D on chip per core  
| L2: 1.25 MB I+D on chip per core  
| L3: 54 MB I+D on chip per chip  
| Other: None  
| Memory: 512 GB (16 x 32 GB 2Rx4 PC4-2933Y-R)  
| Storage: 1 x 1 TB NVME SSD  
| Other: None |

### Software

| OS: Red Hat Enterprise Linux release 8.2 (Ootpa)  
| Compiler: C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux; Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux; C/C++: Version 2021.1 of Intel C/C++ Compiler Classic Build 20201112 for Linux |
| Parallel: No  
| Firmware: Version 05.00.01 released May-2021  
| File System: xfs  
| System State: Run level 3 (multi-user)  
| Base Pointers: 64-bit  
| Peak Pointers: 32/64-bit  
| Other: jemalloc memory allocator V5.0.1  
| Power Management: BIOS and OS set to prefers performance at the cost of additional power usage. |
### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
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<td>239</td>
<td>650</td>
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**SPECrate®2017_int_base = 418**

**SPECrate®2017_int_peak = 434**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

SCALING_GOVERNOR set to Performance

### Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH = 
"/home/CPU2017/lib/intel64:/home/CPU2017/lib/ia32:/home/CPU2017/je5.0.1-32"

MALLOC_CONF = "retain:true"
```

### General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM

memory using Red Hat Enterprise Linux 8.1

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:
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**Test Sponsor:** Inspur Corporation

**Tested by:** Inspur Corporation

**Test Date:** Jun-2021

**Hardware Availability:** May-2021

**Software Availability:** Dec-2020

---

**General Notes (Continued)**

```plaintext
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

```

---

**Platform Notes**

BIOS configuration:
ENERGY_PERF_BIAS_CFG mode set to Performance
Hardware Prefetch set to Disable
VT Support set to Disable
C1E Support set to Disable
Sub NUMA Cluster (SNC) set to Enable
Intel Hyper Threading Technology set to Enable

Sysinfo program /home/CPU2017/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d
running on localhost.localdomain Mon Jun 21 11:24:25 2021

SUT (System Under Test) info as seen by some common utilities. For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
```
model name : Intel(R) Xeon(R) Platinum 8352V CPU @ 2.10GHz
  2 "physical id"s (chips)
  144 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 36
siblings : 72
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
```

(Continued on next page)
## SPEC CPU®2017 Integer Rate Result

### Insper Corporation

**Insper NF5266M6 (Intel Xeon Platinum 8352V)**

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**SPECrate®2017_int_base = 418**

**SPECrate®2017_int_peak = 434**

### Platform Notes (Continued)

From `lscpu` from `util-linux 2.32.1`:

**Architecture:** x86_64  
**CPU op-mode(s):** 32-bit, 64-bit  
**Byte Order:** Little Endian  
**CPU(s):** 144  
**On-line CPU(s) list:** 0-143  
**Thread(s) per core:** 2  
**Core(s) per socket:** 36  
**Socket(s):** 2  
**NUMA node(s):** 4  
**Vendor ID:** GenuineIntel  
**CPU family:** 6  
**Model:** 106  
**Model name:** Intel(R) Xeon(R) Platinum 8352V CPU @ 2.10GHz  
**Stepping:** 6  
**CPU MHz:** 2500.000  
**CPU max MHz:** 3500.0000  
**CPU min MHz:** 800.0000  
**BogoMIPS:** 4200.00  
**Virtualization:** VT-x  
**L1d cache:** 48K  
**L1i cache:** 32K  
**L2 cache:** 1280K  
**L3 cache:** 55296K  
**NUMA node0 CPU(s):** 0-17, 72-89  
**NUMA node1 CPU(s):** 18-35, 90-107  
**NUMA node2 CPU(s):** 36-53, 108-125  
**NUMA node3 CPU(s):** 54-71, 126-143  
**Flags:** fpu vme de pse tsc msr pae mce cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pbebs rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd shani avx512bw avx512v1 xsaveopt xsaves xgetbv1 xsaves cqm_llc cqm_occup llc cqm_mbb_total cqm_mbb_local wbnoinvd dtherm ida arat pln pts avx512vbmib umip pku ospke avx512vbmib2 qfni vaes vpcrldaqd avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid md_clear pconfi flush_lld arch_capabilities

```
/proc/cpuinfo cache data
  cache size : 55296 KB
```

(Continued on next page)
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Platform Notes (Continued)

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 4 nodes (0-3)
  node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 72 73 74 75 76 77 78 79 80 81 82 83 84 85 86 87 88 89
  node 0 size: 128611 MB
  node 0 free: 128271 MB
  node 1 cpus: 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 108 109 110 111 112 113 114 115 116 117 118 119 120 121 122 123 124 125
  node 1 size: 129016 MB
  node 1 free: 128775 MB
  node 2 cpus: 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80 81 82 83 84 85 86 87 88 89
  node 2 size: 129016 MB
  node 2 free: 128775 MB
  node 3 cpus: 86 87 88 89 90 91 92 93 94 95 96 97 98 99 100 101 102 103 104 105 106 107
  node 3 size: 128984 MB
  node 3 free: 128775 MB
  node distances:
  node   0   1   2   3
  0:  10  11  20  20
  1:  11  10  20  20
  2:  20  20  10  11
  3:  20  20  11  10

From /proc/meminfo
MemTotal:       528002748 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

/sbin/tuned-adm active
It seems that tuned daemon is not running, preset profile is not activated.
Preset profile: throughput-performance

/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has performance

From /etc/*release* /etc/*version*
  os-release:
    NAME="Red Hat Enterprise Linux"
    VERSION="8.2 (Ootpa)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="8.2"
    PLATFORM_ID="platform:el8"
    PRETTY_NAME="Red Hat Enterprise Linux 8.2 (Ootpa)"

(Continued on next page)
Specifications:

CPU 2017 License: 3358

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Tested by: Inspur Corporation

Test Date: Jun-2021

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Platform Notes (Continued)

ANSI_COLOR="0;31"

redhat-release: Red Hat Enterprise Linux release 8.2 (Ootpa)

system-release: Red Hat Enterprise Linux release 8.2 (Ootpa)

system-release-cpe: cpe:/o:redhat:enterprise_linux:8.2:ga

uname -a:

Linux localhost.localdomain 4.18.0-193.el8.x86_64 #1 SMP Fri Mar 27 14:35:58 UTC 2020

x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): Not affected

CVE-2018-3620 (L1 Terminal Fault): Not affected

Microarchitectural Data Sampling: Not affected

CVE-2017-5754 (Meltdown): Not affected

CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp

CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swapgs barriers and __user pointer sanitization

CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

CVE-2020-0543 (Special Register Buffer Data Sampling): No status reported

CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Jun 21 11:23

SPEC is set to: /home/CPU2017

Filesystem  Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs   876G   89G  787G  11% /home

From /sys/devices/virtual/dmi/id

Vendor: Inspur

Product: NF5266M6

Product Family: Family

Serial: 312356754

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

16x Micron 18ASF4G72PDZ-2G9E1 32 GB 2 rank 2933

BIOS:

BIOS Vendor: American Megatrends Inc.
Inspur Corporation

Inspur NF5266M6 (Intel Xeon Platinum 8352V)

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Platform Notes (Continued)

- BIOS Version: 05.00.01
- BIOS Date: 05/19/2021
- BIOS Revision: 5.22

(End of data from sysinfo program)

Compiler Version Notes

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<tr>
<td>64, Version 2021.1 Build 20201112_000000</td>
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Compiler Version Notes (Continued)

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------------------------------------------------------------------------------
| C       | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak) |
|         | 525.x264_r(base, peak) 557.xz_r(base) |
------------------------------------------------------------------------------

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

------------------------------------------------------------------------------
| C       | 500.perlbench_r(peak) 557.xz_r(peak) |
------------------------------------------------------------------------------

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

------------------------------------------------------------------------------
| C       | 502.gcc_r(peak) |
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Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

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| C       | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak) |
|         | 525.x264_r(base, peak) 557.xz_r(base) |
------------------------------------------------------------------------------

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
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| C++     | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak) |
|         | 531.deepsjeng_r(base, peak) 541.leela_r(base, peak) |
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**Compiler Version Notes (Continued)**

Fortran | 548.exchange2_r(base, peak)

Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
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---

**Base Compiler Invocation**

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifort

---

**Base Portability Flags**

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

---

**Base Optimization Flags**

C benchmarks:
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
-lqkmalloc

---

(Continued on next page)
Inspur Corporation

Inspur NF5266M6 (Intel Xeon Platinum 8352V)

CPU2017 License: 3358
Test Sponsor: Inspur Corporation
Test Date: Jun-2021
CPU2017 License: 3358
Test Sponsor: Inspur Corporation
Tested by: Inspur Corporation
Hardware Availability: May-2021
Test Date: Jun-2021
Hardware Availability: May-2021
Software Availability: Dec-2020
Software Availability: Dec-2020

### Base Optimization Flags (Continued)

C++ benchmarks:
- `-w`  
- `-m64`  
- `-Wl,-z,muldefs`  
- `-xCORE-AVX512`  
- `-O3`  
- `-ffast-math`  
- `-flto`  
- `-mfpmath=sse`  
- `-funroll-loops`  
- `-qopt-mem-layout-trans=4`  
- `-mbranches-within-32B-boundaries`  
- `-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin`  
- `-Lqkmalloc`

Fortran benchmarks:
- `-w`  
- `-m64`  
- `-z,muldefs`  
- `-xCORE-AVX512`  
- `-O3`  
- `-ipo`  
- `-no-prec-div`  
- `-qopt-mem-layout-trans=4`  
- `-nostandard-realloc-lhs`  
- `-align array32byte`  
- `-mbranches-within-32B-boundaries`  
- `-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin`  
- `-Lqkmalloc`

Peak Compiler Invocation

C benchmarks (except as noted below):
- `icx`
- `500.perlbench_r: icc`
- `557.xz_r: icc`

C++ benchmarks:
- `icpx`

Fortran benchmarks:
- `ifort`

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64  
502.gcc_r: -D_FILE_OFFSET_BITS=64  
505.mcf_r: -DSPEC_LP64  
520.omnetpp_r: -DSPEC_LP64  
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX  
525.x264_r: -DSPEC_LP64  
531.deepsjeng_r: -DSPEC_LP64  
541.leela_r: -DSPEC_LP64  
548.exchange2_r: -DSPEC_LP64  
557.xz_r: -DSPEC_LP64
## SPEC CPU®2017 Integer Rate Result

### Inspur Corporation

**Inspur NF5266M6 (Intel Xeon Platinum 8352V)**

<table>
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<tr>
<th><strong>CPU2017 License</strong>: 3358</th>
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<td><strong>Tested by</strong>: Inspur Corporation</td>
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### SPECrate®2017_int_base = 418

### SPECrate®2017_int_peak = 434

#### Peak Optimization Flags

**C benchmarks:**

- `500.perlbench_r`: `-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)`
- `-xCORE-AVX512 -ipo -O3 -no-prec-div`
- `-qopt-mem-layout-trans=4 -fno-strict-overflow`
- `-mbranches-within-32B-boundaries`
- `-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin -lqkmalloc`

- `502.gcc_r`: `-m32`
- `-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/ia32_lin -std=gnu89 -Wl,-z,muldefs -fprofile-generate(pass 1)`
- `-fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -flto`
- `-Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4`
- `-mbranches-within-32B-boundaries`
- `-L/usr/local/jemalloc32-5.0.1/lib -ljemalloc`

- `505.mcf_r`: `basepeak = yes`

- `525.x264_r`: `-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -flto`
- `-O3 -ffast-math -qopt-mem-layout-trans=4 -fno-alias`
- `-mbranches-within-32B-boundaries`
- `-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin -lqkmalloc`

**C++ benchmarks:**

- `520.omnetpp_r`: `basepeak = yes`

- `523.xalancbmk_r`: `basepeak = yes`

- `531.deepsjeng_r`: `basepeak = yes`

- `541.leela_r`: `basepeak = yes`

**Fortran benchmarks:**

- `548.exchange2_r`: `basepeak = yes`
Inspur Corporation

Inspur NF5266M6 (Intel Xeon Platinum 8352V)

**SPEC CPU®2017 Integer Rate Result**

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</tbody>
</table>

Test Date: Jun-2021
Hardware Availability: May-2021
Software Availability: Dec-2020

The flags files that were used to format this result can be browsed at:
http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-V2.0.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml
http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-V2.0.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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