# Lenovo Global Technology

## ThinkSystem SR630 V2

### (2.30 GHz, Intel Xeon Silver 4310T)

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>9017</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor:</td>
<td>Lenovo Global Technology</td>
</tr>
<tr>
<td>Tested by:</td>
<td>Lenovo Global Technology</td>
</tr>
<tr>
<td>Test Date:</td>
<td>Jun-2021</td>
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<td>Hardware Availability:</td>
<td>Jul-2021</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>Dec-2020</td>
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</table>

### SPEC®2017 Floating Point Rate Result

**SPECrate®2017_fp_base** = **132**  
**SPECrate®2017_fp_peak** = **Not Run**

### Hardware

- **CPU Name:** Intel Xeon Silver 4310T  
- **Max MHz:** 3400  
- **Nominal:** 2300  
- **Enabled:** 20 cores, 2 chips, 2 threads/core  
- **Orderable:** 1,2 chips  
- **Cache L1:** 32 KB I + 48 KB D on chip per core  
- **L2:** 1.25 MB I+D on chip per core  
- **L3:** 15 MB I+D on chip per chip  
- **Memory:** 1 TB (32 x 32 GB 2Rx8 PC4-3200AA-R, running at 2666)  
- **Storage:** 1 x 960 GB SATA SSD  
- **Other:** None  

### Software

- **OS:** SUSE Linux Enterprise Server 15 SP2 (x86_64)  
  Kernel 5.3.18-22-default  
- **Compiler:** C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux;  
  Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux;  
- **Parallel:** No  
- **Firmware:** Lenovo BIOS Version AFE111A 1.02 released May-2021  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** Not Applicable  
- **Other:** jemalloc memory allocator V5.0.1  
- **Power Management:** BIOS and OS set to prefer performance at the cost of additional power usage

### Benchmarks

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>SPECrate®2017_fp_base</th>
<th>SPECrate®2017_fp_peak</th>
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<tr>
<td>503.bwaves_r</td>
<td>40</td>
<td>154</td>
<td>Not Run</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
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<td>508.namd_r</td>
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<tr>
<td>510.parest_r</td>
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<td>511.povray_r</td>
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<tr>
<td>527.cam4_r</td>
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<tr>
<td>538.imagick_r</td>
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<tr>
<td>544.nab_r</td>
<td>40</td>
<td>303</td>
<td>Not Run</td>
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<tr>
<td>549.fotonik3d_r</td>
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<tr>
<td>554.roms_r</td>
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</table>

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Page 1

Standard Performance Evaluation Corporation (info@spec.org)  
https://www.spec.org/
Lenovo Global Technology
ThinkSystem SR630 V2
(2.30 GHz, Intel Xeon Silver 4310T)

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

SPECrater®2017_fp_base = 132
SPECrater®2017_fp_peak = Not Run

Results Table

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<tr>
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</tbody>
</table>

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = 
MALLOCP_CONF = "retain:true"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7940X CPU + 64GB RAM memory using openSUSE Leap 15.2
Transparent Huge Pages enabled by default

(Continued on next page)
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SPECraten®2017 fp_base = 132
SPECraten®2017 fp_peak = Not Run

CPU2017 License: 9017
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General Notes (Continued)

Prior to runcpu invocation
Filesystem page cache synced and cleared with:
  sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numacl1 i.e.:
  numacl1 --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance and then set it to Custom Mode
MONITOR/MWAIT set to Enabled
Intel Virtualization Technology set to Disabled
XPT Prefetcher set to Disabled

Sysinfo program /home/cpu2017-1.1.8-ic2021.1-revA-update1/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafec64d
running on localhost Sat Jun 26 08:57:16 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
  https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) Silver 4310T CPU @ 2.30GHz
  2 "physical id"s (chips)
  40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 10
siblings : 20
physical 0: cores 0 1 2 3 4 5 6 7 8 9
physical 1: cores 0 1 2 3 4 5 6 7 8 9

From lscpu from util-linux 2.33.1:
  Architecture: x86_64
  CPU op-mode(s): 32-bit, 64-bit
  Byte Order: Little Endian

(Continued on next page)
SPEC CPU®2017 Floating Point Rate Result

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SPECRate®2017_fp_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Test Date: Jun-2021
Tested by: Lenovo Global Technology
Hardware Availability: Jul-2021
Software Availability: Dec-2020

Platform Notes (Continued)

Address sizes: 46 bits physical, 57 bits virtual
CPU(s): 40
On-line CPU(s) list: 0-39
Thread(s) per core: 2
Core(s) per socket: 10
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 106
Model name: Intel(R) Xeon(R) Silver 4310T CPU @ 2.30GHz
Stepping: 6
CPU MHz: 2888.492
BogoMIPS: 4600.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 15360K
NUMA node0 CPU(s): 0-9,20-29
NUMA node1 CPU(s): 10-19,30-39
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtsscp
lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpref pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_13 invpcid_single ssbd
mba ibrs ibpb stibp ibrs_enhanced tpr_shadow flexpriority ept vpid ept_ad
fsopcode tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm rdt_a avx512f
avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha ni
avx512bw avx512vl xsaveopt xsaves xsavec xgetbv1 xsaves cqm_llc cqm_occupa_llc cqm_mbmc
local wbnoivnd dtherm ida arat pln pts avx512vbmi unip pku ospke
avx512_vbmi2 gfnl vaes vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq
la57 rdpid md_clear pconfig flush_l1d arch_capabilities

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 20 21 22 23 24 25 26 27 28 29
node 0 size: 515681 MB
node 0 free: 514800 MB
node 1 cpus: 10 11 12 13 14 15 16 17 18 19 30 31 32 33 34 35 36 37 38 39
node 1 size: 516054 MB
node 1 free: 515080 MB

(Continued on next page)
Platform Notes (Continued)

node distances:
node  0  1
  0: 10 20
  1: 20 10

From /proc/meminfo
MemTotal: 1056497224 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 15 SP2

From /etc/*release* /etc/*version*
  os-release:
  NAME="SLES"
  VERSION="15-SP2"
  VERSION_ID="15.2"
  PRETTY_NAME="SUSE Linux Enterprise Server 15 SP2"
  ID="sles"
  ID_LIKE="suse"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:15:sp2"

uname -a:
Linux localhost 5.3.18-22-default #1 SMP Wed Jun 3 12:16:43 UTC 2020 (720aeba) x86_64
x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: userscopy/swapgs barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Jun 26 01:05

(Continued on next page)
**Platform Notes (Continued)**

SPEC is set to: /home/cpu2017-1.1.8-ic2021.1-revA-update1

Filesystem | Type | Size   | Used | Avail | Use% | Mounted on
---|------|-------|------|-------|------|----------------
/dev/sdc3  | xfs  | 891G  | 33G  | 858G  | 4%   | /

From /sys/devices/virtual/dmi/id
Vendor: Lenovo
Product: ThinkSystem SR630 V2 MB
Product Family: ThinkSystem
Serial: 1234567890

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
32x Samsung M393A4G43AB3-CWE 32 GB 2 rank 3200, configured at 2666

BIOS:
BIOS Vendor: Lenovo
BIOS Version: AFE111A-1.02
BIOS Date: 05/07/2021
BIOS Revision: 1.2
Firmware Revision: 1.10

(End of data from sysinfo program)
Lenovo Global Technology
ThinkSystem SR630 V2
(2.30 GHz, Intel Xeon Silver 4310T)
<table>
<thead>
<tr>
<th>Base Compiler Invocation (Continued)</th>
</tr>
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<tbody>
<tr>
<td>C++ benchmarks:</td>
</tr>
<tr>
<td>icpx</td>
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<tr>
<td>Fortran benchmarks:</td>
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<tr>
<td>ifort</td>
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<tr>
<td>Benchmarks using both Fortran and C:</td>
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<td>ifort icx</td>
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<td>Benchmarks using both C and C++:</td>
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<td>Benchmarks using Fortran, C, and C++:</td>
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<td>icpx icx ifort</td>
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<th>Base Portability Flags</th>
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<tbody>
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<tr>
<td>521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian</td>
</tr>
<tr>
<td>526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char</td>
</tr>
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<tbody>
<tr>
<td>C benchmarks:</td>
</tr>
<tr>
<td>-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math</td>
</tr>
<tr>
<td>-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4</td>
</tr>
<tr>
<td>-mbranches-within-32B-boundaries -ljemalloc</td>
</tr>
<tr>
<td>-L/usr/local/jemalloc64-5.0.1/lib</td>
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SPECrerate\textsuperscript{2017\textsubscript{fp}\_base = 132  
SPECrerate\textsuperscript{2017\textsubscript{fp}\_peak = Not Run  

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**Base Optimization Flags (Continued)**

C++ benchmarks:
- \texttt{-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto}
- \texttt{-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4}
- \texttt{-mbranches-within-32B-boundaries -ljemalloc}
- \texttt{-L/usr/local/jemalloc64-5.0.1/lib}

Fortran benchmarks:
- \texttt{-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ipo -no-prec-div}
- \texttt{-qopt-prefetch -ffinite-math-only}
- \texttt{-qopt-multiple-gather-scatter-by-shuffles -qopt-mem-layout-trans=4}
- \texttt{-nostandard-realloc-lhs -align array32byte -auto}
- \texttt{-mbranches-within-32B-boundaries -ljemalloc}
- \texttt{-L/usr/local/jemalloc64-5.0.1/lib}

Benchmarks using both Fortran and C:
- \texttt{-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math}
- \texttt{-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -O3 -ipo}
- \texttt{-no-prec-div -qopt-prefetch -ffinite-math-only}
- \texttt{-qopt-multiple-gather-scatter-by-shuffles}
- \texttt{-mbranches-within-32B-boundaries -nostandard-realloc-lhs}
- \texttt{-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib}

Benchmarks using both C and C++:
- \texttt{-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math}
- \texttt{-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4}
- \texttt{-mbranches-within-32B-boundaries -ljemalloc}
- \texttt{-L/usr/local/jemalloc64-5.0.1/lib}

Benchmarks using Fortran, C, and C++:
- \texttt{-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math}
- \texttt{-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -O3}
- \texttt{-no-prec-div -qopt-prefetch -ffinite-math-only}
- \texttt{-qopt-multiple-gather-scatter-by-shuffles}
- \texttt{-mbranches-within-32B-boundaries -nostandard-realloc-lhs}
- \texttt{-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib}

---

The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-ICElake-E.html


You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-ICElake-E.xml

http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml
### Lenovo Global Technology

**ThinkSystem SR630 V2**  
(2.30 GHz, Intel Xeon Silver 4310T)

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- **Test Sponsor:** Lenovo Global Technology  
- **Tested by:** Lenovo Global Technology  
- **Test Date:** Jun-2021  
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