SPEC CPU®2017 Integer Rate Result

Inspur Corporation

Inspur NF5180M6 (Intel Xeon Gold 6354)

SPECrater®2017_int_base = 319
SPECrater®2017_int_peak = 330

CPU2017 License: 3358
Test Sponsor: Inspur Corporation
Tested by: Inspur Corporation

Test Date: Sep-2021
Hardware Availability: May-2021
Software Availability: Dec-2020

Inspur Corporation

SPECrater®2017_int_base = 319
SPECrater®2017_int_peak = 330

Hardware

CPU Name: Intel Xeon Gold 6354
Max MHz: 3600
Nominal: 3000
Enabled: 36 cores, 2 chips, 2 threads/core
Orderable: 1.2 chips
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 1.25 MB I+D on chip per core
L3: 39 MB I+D on chip per chip
Other: None
Memory: 1 TB (32 x 32 GB 2Rx4 PC4-3200AA-R)
Storage: 1 x 1.6 TB NVME SSD
Other: None

Software

OS: Red Hat Enterprise Linux release 8.2 (Ootpa)
4.18.0-193.el8.x86_64
Compiler: C/C++: Version 2021.1 of Intel oneAPI DPC++/C++
Compiler Build 20201113 for Linux;
C/C++: Version 2021.1 of Intel C/C++
Compiler Classic Build 20201112 for Linux;
Fortran: Version 2021.1 of Intel Fortran
Compiler Classic Build 20201112 for Linux
Parallel: No
Firmware: Version 05.00.02 released May-2021
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.
Inspur Corporation
Inspur NF5180M6 (Intel Xeon Gold 6354)

SPEC CPU®2017 Integer Rate Result

SPECrater®2017_int_base = 319
SPECrater®2017_int_peak = 330

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Base</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Peak</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>72</td>
<td>529</td>
<td>217</td>
<td>530</td>
<td>216</td>
<td>529</td>
<td>217</td>
<td>530</td>
<td>216</td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>72</td>
<td>375</td>
<td>272</td>
<td>378</td>
<td>270</td>
<td>376</td>
<td>271</td>
<td>378</td>
<td>271</td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>72</td>
<td>212</td>
<td>550</td>
<td>212</td>
<td>548</td>
<td>213</td>
<td>545</td>
<td>212</td>
<td>548</td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>72</td>
<td>492</td>
<td>192</td>
<td>490</td>
<td>193</td>
<td>492</td>
<td>192</td>
<td>490</td>
<td>193</td>
</tr>
<tr>
<td>523.xalancbmk_r</td>
<td>72</td>
<td>183</td>
<td>416</td>
<td>184</td>
<td>414</td>
<td>184</td>
<td>413</td>
<td>184</td>
<td>413</td>
</tr>
<tr>
<td>525.x264_r</td>
<td>72</td>
<td>190</td>
<td>663</td>
<td>191</td>
<td>666</td>
<td>191</td>
<td>661</td>
<td>191</td>
<td>661</td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
<td>72</td>
<td>342</td>
<td>242</td>
<td>342</td>
<td>241</td>
<td>342</td>
<td>242</td>
<td>342</td>
<td>242</td>
</tr>
<tr>
<td>541.leela_r</td>
<td>72</td>
<td>504</td>
<td>237</td>
<td>504</td>
<td>237</td>
<td>504</td>
<td>237</td>
<td>504</td>
<td>237</td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>72</td>
<td>288</td>
<td>655</td>
<td>288</td>
<td>655</td>
<td>288</td>
<td>654</td>
<td>288</td>
<td>654</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>72</td>
<td>449</td>
<td>173</td>
<td>450</td>
<td>173</td>
<td>448</td>
<td>174</td>
<td>461</td>
<td>169</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes
The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"
SCALING_GOVERNOR set to Performance

Environment Variables Notes
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/CPU2017/lib/intel64:/home/CPU2017/lib/ia32:/home/CPU2017/je5.0.1-32"

MALLOCONF = "retain: true"

General Notes
Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Red Hat Enterprise Linux 8.1
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
General Notes (Continued)

sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numacl i.e.:
numactl --interleave=all runcpu <etc>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5,
and the system compiler gcc 4.8.5;
sources available from jemalloc.net or

Platform Notes

BIOS configuration:
ENERGY_PERF_BIAS_CFG mode set to Performance
Hardware Prefetch set to Disable
VT Support set to Disable
C1E Support set to Disable
Sub NUMA Cluster (SNC) set to Enable

Sysinfo program /home/CPU2017/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16aca64d
running on localhost.localdomain Fri Sep 3 12:11:13 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6354 CPU @ 3.00GHz
  2  "physical id"s (chips)
  72 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 18
  siblings : 36
  physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17
  physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17

From lscpu from util-linux 2.32.1:

(Continued on next page)
**SPEC CPU®2017 Integer Rate Result**

**Inspur Corporation**

**Inspur NF5180M6 (Intel Xeon Gold 6354)**

**SPECrate®2017_int_base = 319**

**SPECrate®2017_int_peak = 330**

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>3358</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor:</td>
<td>Inspur Corporation</td>
</tr>
<tr>
<td>Tested by:</td>
<td>Inspur Corporation</td>
</tr>
<tr>
<td>Test Date:</td>
<td>Sep-2021</td>
</tr>
<tr>
<td>Hardware Availability:</td>
<td>May-2021</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>Dec-2020</td>
</tr>
</tbody>
</table>

**Platform Notes (Continued)**

```plaintext
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 72
On-line CPU(s) list: 0-71
Thread(s) per core: 2
Core(s) per socket: 18
Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6
Model: 106
Model name: Intel(R) Xeon(R) Gold 6354 CPU @ 3.00GHz
Stepping: 6
CPU MHz: 3600.000
CPU max MHz: 3600.0000
CPU min MHz: 800.0000
BogoMIPS: 6000.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 128K
L3 cache: 39936K
NUMA node0 CPU(s): 0-8,36-44
NUMA node1 CPU(s): 9-17,45-53
NUMA node2 CPU(s): 18-26,54-62
NUMA node3 CPU(s): 27-35,63-71
Flags: fpu vme de pse mce cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpes gb rdtscp lm constant tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop tsc cpuid aperf perf pni pclmulqdq dtes64 ds cpl vmx smx est tm2 ssse3 sde cmx16 xtpr pdcm pcd dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3nowprefetch cpuid fault epb cat_l3 invpcid_single ssbd mba ibrs ibpb stibp ibrs enhanced tpr_shadow vnmiflexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha ni avx512bw avx512vl xsaveopt xsaves vec tb1 xsaves cqm llc cqm_occup_llc cqm_mbm_total cqm_mbm_local wbinvd dtc dtherm ida arat pts avx512vmbi utm pku ospe avx512_vmbi gfn i vaes vpcmldq avx512_vnni avx512_bitalg tme avx512 vpoptdecoded la57 rdpid md_clear pconfig flush l1d arch_capabilities

/proc/cpuinfo cache data
  cache size: 39936 KB

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 4 nodes (0-3)
```

(Continued on next page)
Inspur Corporation
Inspur NF5180M6 (Intel Xeon Gold 6354)

CPU2017 License: 3358
Test Sponsor: Inspur Corporation
Tested by: Inspur Corporation

SPECrate®2017_int_base = 319
SPECrate®2017_int_peak = 330

Test Date: Sep-2021
Hardware Availability: May-2021
Software Availability: Dec-2020

Platform Notes (Continued)

node 0 cpus: 0 1 2 3 4 5 6 7 8 36 37 38 39 40 41 42 43 44
node 0 size: 257636 MB
node 0 free: 257328 MB
node 1 cpus: 9 10 11 12 13 14 15 16 17 45 46 47 48 49 50 51 52 53
node 1 size: 258043 MB
node 1 free: 257596 MB
node 2 cpus: 18 19 20 21 22 23 24 25 26 54 55 56 57 58 59 60 61 62
node 2 size: 258016 MB
node 2 free: 257784 MB
node 3 cpus: 27 28 29 30 31 32 33 34 35 63 64 65 66 67 68 69 70 71
node 3 size: 258041 MB
node 3 free: 257781 MB
node distances:
   node   0   1   2   3
0:  10  11  20  20
1:  11  10  20  20
2:  20  20  10  11
3:  20  20  11  10

From /proc/meminfo
   MemTotal:       1056499724 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/sbin/tuned-adm active
   Current active profile: throughput-performance

/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has performance

From /etc/*release* /etc/*version*
   os-release:
      NAME="Red Hat Enterprise Linux"
      VERSION="8.2 (Ootpa)"
      ID="rhel"
      ID_LIKE="fedora"
      VERSION_ID="8.2"
      PLATFORM_ID="platform:el8"
      PRETTY_NAME="Red Hat Enterprise Linux 8.2 (Ootpa)"
      ANSI_COLOR="0;31"
   redhat-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
   system-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:8.2:ga

uname -a:
   Linux localhost.localdomain 4.18.0-193.el8.x86_64 #1 SMP Fri Mar 27 14:35:58 UTC 2020
   x86_64 x86_64 x86_64 GNU/Linux

(Continued on next page)
Inspur Corporation

Inspur NF5180M6 (Intel Xeon Gold 6354)

SPEC CPU®2017 Integer Rate Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

SPECrate®2017_int_base = 319
SPECrate®2017_int_peak = 330

CPU2017 License: 3358
Test Sponsor: Inspur Corporation
Tested by: Inspur Corporation

Test Date: Sep-2021
Hardware Availability: May-2021
Software Availability: Dec-2020

Platform Notes (Continued)

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2018-3639 (Speculative Store Bypass): Mitigation: usercopy/swapgs barriers and __user pointer sanitization

CVE-2017-5753 (Spectre variant 1): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
CVE-2017-5715 (Spectre variant 2):
CVE-2020-0543 (Special Register Buffer Data Sampling): No status reported
CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Sep 3 11:58

SPEC is set to: /home/CPU2017

Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs 1.5T 86G 1.4T 6% /home

From /sys/devices/virtual/dmi/id
Vendor: Inspur
Product: NF5180M6
Product Family: Family
Serial: 380827124

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
32x Micron 18ASF4G72PDZ-3G2E1 32 GB 2 rank 3200

BIOS:
BIOS Vendor: American Megatrends Inc.
BIOS Version: 05.00.02
BIOS Date: 05/22/2021
BIOS Revision: 5.22

(End of data from sysinfo program)
## Compiler Version Notes

<table>
<thead>
<tr>
<th>Compiler</th>
<th>C</th>
<th>500.perlbench_r(peak) 557.xz_r(peak)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Copyright (C) 1985-2020 Intel Corporation. All rights reserved.</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Compiler</th>
<th>C</th>
<th>502.gcc_r(peak)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2021.1 Build 20201113</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Copyright (C) 1985-2020 Intel Corporation. All rights reserved.</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Compiler</th>
<th>C</th>
<th>500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak) 557.xz_r(base)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Copyright (C) 1985-2020 Intel Corporation. All rights reserved.</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

(Continued on next page)
Inspur Corporation
Inspur NF5180M6 (Intel Xeon Gold 6354)

**SPEC CPU®2017 Integer Rate Result**

Copyright 2017-2021 Standard Performance Evaluation Corporation

---

**Compiler Version Notes (Continued)**

Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

```
==============================================================================
C     | 500.perlbench_r(peak) 557.xz_r(peak)
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
C     | 502.gcc_r(peak)
Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
C     | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak) 557.xz_r(base)
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
C++    | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak) 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

==============================================================================
Fortran | 548.exchange2_r(base, peak)
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
```
SPEC CPU®2017 Integer Rate Result

Inspur Corporation
Inspur NF5180M6 (Intel Xeon Gold 6354)

| SPECrate®2017_int_base = 319 |
| SPECrate®2017_int_peak = 330 |

CPU2017 License: 3358
Test Sponsor: Inspur Corporation
Tested by: Inspur Corporation

Test Date: Sep-2021
Hardware Availability: May-2021
Software Availability: Dec-2020

Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifort

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
-lqkmalloc

C++ benchmarks:
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
-lqkmalloc

Fortran benchmarks:
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ipo -no-prec-div
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
-auto -mbranches-within-32B-boundaries

(Continued on next page)
**SPEC CPU®2017 Integer Rate Result**

**Inspur Corporation**

**Inspur NF5180M6 (Intel Xeon Gold 6354)**

<table>
<thead>
<tr>
<th>SPECrate®2017_int_base</th>
<th>319</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate®2017_int_peak</td>
<td>330</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 3358  
**Test Sponsor:** Inspur Corporation  
**Tested by:** Inspur Corporation  

**Test Date:** Sep-2021  
**Hardware Availability:** May-2021  
**Software Availability:** Dec-2020

---

### Base Optimization Flags (Continued)

Fortran benchmarks (continued):

```
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
-lqkmalloc
```

---

### Peak Compiler Invocation

**C benchmarks (except as noted below):**

- `icx`

- `500.perlbench_r: icc`

- `557.xz_r: icc`

**C++ benchmarks:**

- `icpx`

**Fortran benchmarks:**

- `ifort`

---

### Peak Portability Flags

**500.perlbench_r:** `-DSPEC_LP64` `-DSPEC_LINUX_X64`

**502.gcc_r:** `-D_FILE_OFFSET_BITS=64`

**505.mcf_r:** `-DSPEC_LP64`

**520.omnetpp_r:** `-DSPEC_LP64`

**523.xalancbmk_r:** `-DSPEC_LP64` `-DSPEC_LINUX`

**525.x264_r:** `-DSPEC_LP64`

**531.deepsjeng_r:** `-DSPEC_LP64`

**541.leela_r:** `-DSPEC_LP64`

**548.exchange2_r:** `-DSPEC_LP64`

**557.xz_r:** `-DSPEC_LP64`

---

### Peak Optimization Flags

**C benchmarks:**

```
500.perlbench_r: -Wl, -z, muldefs -prof-gen(pass 1) -prof-use(pass 2)
-xCORE-AVX512 -ipo -03 -no-prec-div
-qopt-mem-layout-trans=4 -fno-strict-overflow
-mbranches-within-32B-boundaries
```

(Continued on next page)
Inspur Corporation

Inspur NF5180M6 (Intel Xeon Gold 6354)

SPECr@te®2017_int_base = 319
SPECr@te®2017_int_peak = 330

CPU2017 License: 3358
Test Sponsor: Inspur Corporation
Test Date: Sep-2021
Hardware Availability: May-2021
Tested by: Inspur Corporation
Software Availability: Dec-2020

Peak Optimization Flags (Continued)

500.perlbench_r (continued):
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
-lqkmalloc

502.gcc_r: -m32
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/ia32_lin
-std=gnu89 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -flto
-Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-LL/usr/local/jemalloc32-5.0.1/lib -ljemalloc

505.mcf_r: basepeak = yes

525.x264_r: -w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -flto
-O3 -ffast-math -qopt-mem-layout-trans=4 -fno-alias
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
-lqkmalloc

C++ benchmarks:

520.omnetpp_r: basepeak = yes

Fortran benchmarks:

548.exchange2_r: basepeak = yes

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml
http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-V2.1.xml
<table>
<thead>
<tr>
<th>SPEC CPU®2017 Integer Rate Result</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Inspur Corporation</strong></td>
</tr>
<tr>
<td><strong>Inspur NF5180M6 (Intel Xeon Gold 6354)</strong></td>
</tr>
<tr>
<td><strong>SPECrate®2017_int_base = 319</strong></td>
</tr>
<tr>
<td><strong>SPECrate®2017_int_peak = 330</strong></td>
</tr>
<tr>
<td>CPU2017 License: 3358</td>
</tr>
<tr>
<td>Test Sponsor: Inspur Corporation</td>
</tr>
<tr>
<td>Tested by: Inspur Corporation</td>
</tr>
<tr>
<td>Test Date: Sep-2021</td>
</tr>
<tr>
<td>Hardware Availability: May-2021</td>
</tr>
<tr>
<td>Software Availability: Dec-2020</td>
</tr>
</tbody>
</table>

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.8 on 2021-09-03 12:11:13-0400.
Originally published on 2021-09-28.