# SPEC CPU®2017 Integer Speed Result

## Cisco Systems
Cisco UCS B200 M6 (Intel Xeon Platinum 8358P, 2.60GHz)

<table>
<thead>
<tr>
<th>Software</th>
<th>Hardware</th>
</tr>
</thead>
<tbody>
<tr>
<td>OS: SUSE Linux Enterprise Server 15 SP2</td>
<td>CPU Name: Intel Xeon Platinum 8358P</td>
</tr>
<tr>
<td>Compiler: C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux; Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux; C/C++: Version 2021.1 of Intel C/C++ Compiler Classic Build 20201112 for Linux</td>
<td>Max MHz: 3400</td>
</tr>
<tr>
<td>Parallel: Yes</td>
<td>Nominal: 2600</td>
</tr>
<tr>
<td>Firmware: Version 4.2.1d released Jul-2021</td>
<td>Enabled: 64 cores, 2 chips</td>
</tr>
<tr>
<td>File System: btrfs</td>
<td>Orderable: 1,2 Chips</td>
</tr>
<tr>
<td>System State: Run level 3 (multi-user)</td>
<td>Cache L1: 32 KB I + 48 KB D on chip per core</td>
</tr>
<tr>
<td>Base Pointers: 64-bit</td>
<td>L2: 1.25 MB I+D on chip per core</td>
</tr>
<tr>
<td>Peak Pointers: 64-bit</td>
<td>L3: 48 MB I+D on chip per core</td>
</tr>
<tr>
<td>Other: jemalloc memory allocator V5.0.1</td>
<td>Other: None</td>
</tr>
<tr>
<td>Power Management: BIOS and OS set to prefer performance at the cost of additional power usage</td>
<td>Memory: 2 TB (32 x 64 GB 2Rx4 PC4-3200V-R)</td>
</tr>
<tr>
<td></td>
<td>Storage: 1 x 960 GB M.2 SSD SATA</td>
</tr>
</tbody>
</table>

## SPECspeed®2017_int_base = 11.5

| SPECspeed®2017_int_peak = 11.7 |

| SPECspeed®2017_int_base = 11.5 |

| SPECspeed®2017_int_peak = 11.7 |

## CPU2017 License: 9019

| Test Sponsor: Cisco Systems |
| Tested by: Cisco Systems |

## Test Date: Sep-2021

| Hardware Availability: Apr-2021 |
| Software Availability: Dec-2020 |

| Tested by: Cisco Systems |

## Software Availability: Dec-2020

| Hardware Availability: Apr-2021 |

## Hardware

<table>
<thead>
<tr>
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<tbody>
<tr>
<td>600.perlbench_s</td>
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<td>648.exchange2_s</td>
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<tr>
<td>657.xz_s</td>
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## Software

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<tr>
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<tr>
<td>6.95</td>
<td>7.97</td>
<td>10.5</td>
<td>10.9</td>
<td>19.0</td>
<td>11.3</td>
</tr>
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| SPECspeed®2017_int_base (11.5) |

| SPECspeed®2017_int_peak (11.7) |

<table>
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### SPEC CPU®2017 Integer Speed Result

**Cisco Systems**

Cisco UCS B200 M6 (Intel Xeon Platinum 8358P, 2.60GHz)

**SPEC**

---

**Test Sponsor:** Cisco Systems  
**Test Date:** Sep-2021  
**Hardware Availability:** Apr-2021  
**Software Availability:** Dec-2020

---

**Results Table**

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<th>Ratio</th>
</tr>
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<tbody>
<tr>
<td>600.perlbench_s</td>
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<td>602.gcc_s</td>
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<tr>
<td>620.omnetpp_s</td>
<td>64</td>
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<td>631.deepsjeng_s</td>
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<td>641.leela_s</td>
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<tr>
<td>648.exchange2_s</td>
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<tr>
<td>657.xz_s</td>
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<td>23.7</td>
<td>263</td>
<td>23.5</td>
<td>263</td>
<td>23.5</td>
</tr>
</tbody>
</table>

---

**Submit Notes**

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

---

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"

---

**Environment Variables Notes**

Environment variables set by runcpu before the start of the run:

- KMP_AFFINITY = "granularity=fine,scatter"
- LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"
- MALLOC_CONF = "retain:true"
- OMP_STACKSIZE = "192M"

---

**General Notes**

Binaries compiled on a system with 1x Intel Core i9-7940X CPU + 64GB RAM memory using openSUSE Leap 15.2

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3 > /proc/sys/vm/drop_caches
```

runcpu command invoked through numactl i.e.:

```
(Continued on next page)```
SPEC CPU®2017 Integer Speed Result

Cisco Systems
Cisco UCS B200 M6 (Intel Xeon Platinum 8358P, 2.60GHz)

SPECspeed®2017_int_base = 11.5
SPECspeed®2017_int_peak = 11.7

CPU2017 License: 9019
Test Sponsor: Cisco Systems
Tested by: Cisco Systems

Test Date: Sep-2021
Hardware Availability: Apr-2021
Software Availability: Dec-2020

General Notes (Continued)

    numactl --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
   jemalloc, a general purpose malloc implementation
   built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

    BIOS Settings:
    Intel Hyper-Threading Technology set to Disabled
    Adjacent Cache Line Prefetcher set to Disabled
    DCU Streamer Prefetch set to Disabled
    Memory Refresh Rate set to 1x Refresh
    ADDDC Sparing set to Disabled
    Patrol Scrub set to Disabled
    Energy Efficient Turbo set to Enabled
    Processor C6 Report set to Enabled
    Processor C1E set to Enabled

    Sysinfo program /home/cpu2017/bin/sysinfo
    Rev: r6622 of 2021-04-07 982a616ec0915b55891ef0e16aca6c4d
    running on install Wed Sep 1 22:21:06 2021

    SUT (System Under Test) info as seen by some common utilities.
    For more information on this section, see
    https://www.spec.org/cpu2017/Docs/config.html#sysinfo

    From /proc/cpuinfo
    model name : Intel(R) Xeon(R) Platinum 8358P CPU @ 2.60GHz
      2 "physical id"s (chips)
      64 "processors"
    cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
      cpu cores : 32
      siblings : 32
      physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31
      physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31

    From lsccpu from util-linux 2.33.1:

    (Continued on next page)
Cisco Systems
Cisco UCS B200 M6 (Intel Xeon Platinum 8358P, 2.60GHz)

CPU2017 License: 9019
Test Sponsor: Cisco Systems
Tested by: Cisco Systems

SPECspeed®2017_int_base = 11.5
SPECspeed®2017_int_peak = 11.7

CPU MHz: 3299.587
CPU max MHz: 3400.0000
CPU min MHz: 800.0000
BogoMIPS: 5200.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 49152K
NUMA node0 CPU(s): 0-31
NUMA node1 CPU(s): 32-63

Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtpsc
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmprf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtrr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_13 invpcid_single ssbd
mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnum flexpriority ept vpid ept_ad
dfgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm rdt_a avx512f
avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_ni
avx512bw avx512vl xsaveopt xsavec xsaveopt xsavec xsavec cqm_llc cqm_occup_llc cqm_mbb_total
cqm_mbb_local wbinvvd dtherm ida arat pln pts hwp hwp_act_window hwp_epp
hwp_pkg_req avx512vBMI umip pku ospke avx512_vBMI2 gfn i vaes vcpulmqdq avx512_vnni
avx512_bitalg tme avx512_vpopcntdq la57 rpdid md_clear pconfig flush_l1d
arch_capabilities

/platform/cpuinfo/cache.data

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 2 nodes (0-1)
Cisco Systems

Cisco UCS B200 M6 (Intel Xeon Platinum 8358P, 2.60GHz)

**SPECspeed®2017_int_base = 11.5**

**SPECspeed®2017_int_peak = 11.7**

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<td>Software Availability: Dec-2020</td>
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**Platform Notes (Continued)**

```plaintext
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31
node 0 size: 1031744 MB
node 0 free: 1031192 MB
node 1 cpus: 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63
node 1 size: 1032180 MB
node 1 free: 1031622 MB
node distances:
node 0 1
0: 10 20
1: 20 10
```

From `/proc/meminfo`

- `MemTotal: 2113458748 kB`
- `HugePages_Total: 0`
- `Hugepagesize: 2048 kB`

From `/etc/*release* /etc/*version*`:

- `NAME="SLES"
  VERSION="15-SP2"
  VERSION_ID="15.2"
  PRETTY_NAME="SUSE Linux Enterprise Server 15 SP2"
  ID="sles"
  ID_LIKE="suse"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:15:sp2"

uname -a:

```plaintext
Linux install 5.3.18-22-default #1 SMP Wed Jun 3 12:16:43 UTC 2020 (720aeba) x86_64
x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

- **CVE-2018-12207 (iTLB Multihit):** Not affected
- **CVE-2018-3620 (L1 Terminal Fault):** Not affected
- **Microarchitectural Data Sampling:** Not affected
- **CVE-2017-5754 (Meltdown):** Not affected
- **CVE-2018-3639 (Speculative Store Bypass):** Mitigation: Speculative Store Bypass disabled via prctl and seccomp
- **CVE-2017-5753 (Spectre variant 1):** Mitigation: usercopy/swaps barriers and __user pointer

(Continued on next page)
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SPECspeed®2017_int_base = 11.5
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CPU2017 License: 9019
Test Sponsor: Cisco Systems
Tested by: Cisco Systems

Platform Notes (Continued)

- CVE-2017-5715 (Spectre variant 2): sanitization
  Mitigation: Enhanced IBRS, IBPB:
  conditional, RSB filling
- CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
- CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Sep 1 22:10

SPEC is set to: /home/cpu2017
Filesistem     Type   Size  Used Avail Use% Mounted on
/dev/sda2      btrfs  892G   26G  863G   3% /home

From /sys/devices/virtual/dmi/id
Vendor:         Cisco Systems Inc
Product:        UCSB-B200-M6
Serial:         FCH24097578

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
  32x 0xCE00 M393A8G40AB2-CWE 64 GB 2 rank 3200

BIOS:
  BIOS Vendor:  Cisco Systems, Inc.
  BIOS Version: B200M6.4.2.id.0.0730210924
  BIOS Date:    07/30/2021
  BIOS Revision: 5.22

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C       | 600.perlbench_s(peak)
       | 602.gcc_s(base, peak) 605.mcf_s(base, peak)
       | 625.x264_s(base, peak) 657.xz_s(base, peak)
==============================================================================

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)
  64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

(Continued on next page)
Cisco Systems
Cisco UCS B200 M6 (Intel Xeon Platinum 8358P, 2.60GHz)

SPEC ® 2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

Compiler Version Notes (Continued)

Intel (R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
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Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

(Continued on next page)
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Cisco UCS B200 M6 (Intel Xeon Platinum 8358P, 2.60GHz)

Baseline Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Baseline Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Baseline Optimization Flags

C benchmarks:
-DSPEC_OPENMP -std=c11 -m64 -fopenmp -Wl,-z,muldefs -xCORE-AVX512
-03 -ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

C++ benchmarks:
-DSPEC_OPENMP -m64 -Wl,-z,muldefs -xCORE-AVX512 -03 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin/
-lqkmalloc

Fortran benchmarks:
-m64 -xCORE-AVX512 -03 -ipo -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-mbranches-within-32B-boundaries

Test Date: Sep-2021
Hardware Availability: Apr-2021
Software Availability: Dec-2020

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SPECspeed®2017_int_peak = 11.7
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Cisco UCS B200 M6 (Intel Xeon Platinum 8358P, 2.60GHz)

SPECspeak®2017_int_base = 11.5
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CPU2017 License: 9019
Test Sponsor: Cisco Systems
Tested by: Cisco Systems

Test Date: Sep-2021
Hardware Availability: Apr-2021
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Peak Compiler Invocation

C benchmarks (except as noted below):
icx

600.perlbench_s: icc

C++ benchmarks:
icpx

Fortran benchmarks:
ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-strict-overflow
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

602.gcc_s: -m64 -std=c11 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -flto
-Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

605.mcf_s: basepeak = yes

625.x264_s: -DSPEC_OPENMP -fiopenmp -std=c11 -m64 -Wl,-z,muldefs
-xCORE-AVX512 -flto -O3 -ffast-math
-qopt-mem-layout-trans=4 -fno-alias
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

657.xz_s: basepeak = yes

(Continued on next page)
Cisco Systems
Cisco UCS B200 M6 (Intel Xeon Platinum 8358P, 2.60GHz)

SPECspeak®2017_int_base = 11.5
SPECspeak®2017_int_peak = 11.7

CPU2017 License: 9019
Test Sponsor: Cisco Systems
Tested by: Cisco Systems

Test Date: Sep-2021
Hardware Availability: Apr-2021
Software Availability: Dec-2020

Peak Optimization Flags (Continued)

C++ benchmarks:
620.omnetpp_s: basepeak = yes
623.xalancbmk_s: basepeak = yes
631.deepsjeng_s: basepeak = yes
641.leela_s: basepeak = yes

Fortran benchmarks:
648.exchange2_s: basepeak = yes

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.8 on 2021-09-02 01:21:06-0400.
Report generated on 2021-09-29 12:26:06 by CPU2017 PDF formatter v6442.
Originally published on 2021-09-28.