# SPEC CPU®2017 Integer Rate Result

## Lenovo Global Technology

**ThinkSystem SR650 V2**

(2.30 GHz, Intel Xeon Silver 4310T)

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>9017</th>
</tr>
</thead>
<tbody>
<tr>
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<td>Hardware Availability:</td>
<td>Jul-2021</td>
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<td>Dec-2020</td>
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</tbody>
</table>

<table>
<thead>
<tr>
<th>Copies</th>
<th>SPECrate®2017_int_base =</th>
<th>144</th>
</tr>
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<tbody>
<tr>
<td>500.perlbench_r</td>
<td>40</td>
<td></td>
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<tr>
<td>502.gcc_r</td>
<td>40</td>
<td></td>
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<tr>
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<td></td>
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<tr>
<td>557.xz_r</td>
<td>40</td>
<td></td>
</tr>
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### Hardware

**CPU Name:** Intel Xeon Silver 4310T  
**Max MHz:** 3400  
**Nominal:** 2300  
**Enabled:** 20 cores, 2 chips, 2 threads/core  
**Orderable:** 1.2 chips  
**Cache L1:** 32 KB I + 48 KB D on chip per core  
**L2:** 1.25 MB I+D on chip per core  
**L3:** 15 MB I+D on chip per chip  
**Other:** None  
**Memory:** 1 TB (32 x 32 GB 2Rx8 PC4-3200AA-R, running at 2666)  
**Storage:** 1 x 960 GB SATA SSD  
**Other:** None

### Software

**OS:** Red Hat Enterprise Linux 8.3 (Ootpa)  
**Kernel:** 4.18.0-240.el8.x86_64  
**Compiler:** C/C++, Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux;  
**Fortran:** Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux  
**Parallel:** No  
**Firmware:** Lenovo BIOS Version AFE113D 1.10 released Sep-2021  
**File System:** xfs  
**System State:** Run level 3 (multi-user)  
**Base Pointers:** 64-bit  
**Peak Pointers:** Not Applicable  
**Other:** None  
**Power Management:** BIOS and OS set to prefer performance at the cost of additional power usage
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SPECraten®2017_int_base = 144
SPECraten®2017_int_peak = Not Run

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Base Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<tbody>
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<td>670</td>
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<td>468</td>
<td>121</td>
<td>471</td>
<td>120</td>
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<td>290</td>
<td>365</td>
<td>287</td>
<td>364</td>
<td>288</td>
</tr>
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<td>557.xz_r</td>
<td>40</td>
<td>539</td>
<td>80.1</td>
<td>540</td>
<td>80.0</td>
<td>539</td>
<td>80.2</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH =
  
MALLOC_CONF = "retain:true"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Red Hat Enterprise Linux 8.1
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
  sync; echo 3>/proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
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**SPECrate®2017_int_base = 144**
**SPECrate®2017_int_peak = Not Run**

**General Notes (Continued)**

- numactl --interleave=all runcpu <etc>
- NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
- Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
- Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

**Platform Notes**

**BIOS configuration:**
Choose Operating Mode set to Maximum Performance and then set it to Custom Mode
C-States set to Legacy
Adjacent Cache Prefetch set to Disabled
DCU Streamer Prefetcher set to Disabled
UPI Link Disable set to Disabled 1 Link

**Sysinfo program** /home/cpu2017-1.1.8-ic2021.1-revB/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16aca64d
running on localhost.localdomain Fri Jun 22 19:12:35 2018

**SUT (System Under Test) info as seen by some common utilities.**
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

**From /proc/cpuinfo**
```
model name : Intel(R) Xeon(R) Silver 4310T CPU @ 2.30GHz
  2 "physical id"s (chips)
  40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 10
siblings : 20
  physical 0: cores 0 1 2 3 4 5 6 7 8 9
  physical 1: cores 0 1 2 3 4 5 6 7 8 9
```

**From lscpu from util-linux 2.32.1:**
```
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 40
On-line CPU(s) list: 0-39
Thread(s) per core: 2
Core(s) per socket: 10
Socket(s): 2
NUMA node(s): 2
```

(Continued on next page)
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**Hardware Availability:** Jul-2021  
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### SPEC CPU®2017 Integer Rate Result

**SPECCrate®2017_int_base =** 144  
**SPECCrate®2017_int_peak =** Not Run

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### Platform Notes (Continued)

Vendor ID: GenuineIntel  
CPU family: 6  
Model: 106  
Model name: Intel(R) Xeon(R) Silver 4310T CPU @ 2.30GHz  
Stepping: 6  
CPU MHz: 2917.064  
BogoMIPS: 4600.00  
Virtualization: VT-x  
L1d cache: 48K  
L1i cache: 32K  
L2 cache: 1280K  
L3 cache: 15360K  
NUMA node0 CPU(s): 0-9, 20-29  
NUMA node1 CPU(s): 10-19, 30-39  
Flags: fpu vme de pse tsc msr pae mce cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single intel_pin ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vmcs flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 3msr invpcid cmqm dcm_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha ni avx512bw avx512vl xsaveopt xsavec xsaveopt xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbb_local split_lock_detect wboinvvd dtherm ida arat pln pts avx512vbmi umip pku ospke avx512_vbmi2 gfn vaes vpcmuleqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid md_clear pconfig flush_l1d arch_capabilities

/proc/cpuinfo cache data  
cache size : 15360 KB

From numactl --hardware  
WARNING: a numactl 'node' might or might not correspond to a physical chip.  
available: 2 nodes (0-1)  
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 20 21 22 23 24 25 26 27 28 29  
node 0 size: 502263 MB  
node 0 free: 515049 MB  
node 1 cpus: 10 11 12 13 14 15 16 17 18 19 30 31 32 33 34 35 36 37 38 39  
node 1 size: 501400 MB  
node 1 free: 515383 MB  
node distances:  
node 0 1  
0: 10 20  
1: 20 10

From /proc/meminfo  
MemTotal: 1056494092 kB

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Platform Notes (Continued)

HugePages_Total:       0
Hugepagesize:       2048 kB

/sbin/tuned-adm active
   Current active profile: throughput-performance

From /etc/*release* /etc/*version*
   os-release:
      NAME="Red Hat Enterprise Linux"
      VERSION="8.3 (Ootpa)"
      ID="rhel"
      ID_LIKE="fedora"
      VERSION_ID="8.3"
      PLATFORM_ID="platform:el8"
      PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"
      ANSI_COLOR="0;31"
   redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
   system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga

uname -a:
   Linux localhost.localdomain 4.18.0-240.el8.x86_64 #1 SMP Wed Sep 23 05:13:10 EDT 2020
   x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multi-hit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2018-3639 (Speculative Store Bypass): Mitigation: usercopy/swaps barriers and __user pointer sanitization
CVE-2017-5753 (Spectre variant 1): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
CVE-2017-5715 (Spectre variant 2):
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Jun 22 19:11

SPEC is set to: /home/cpu2017-1.1.8-ic2021.1-revB
Filesystem Type Size Used Avail Use% Mounted on
/dev/sdb4 xfs 819G 268G 551G 33% /home

(Continued on next page)
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Test Date: Sep-2021
Hardware Availability: Jul-2021
Software Availability: Dec-2020

Platform Notes (Continued)
From /sys/devices/virtual/dmi/id
Vendor: Lenovo
Product: ThinkSystem SR650 V2 MB
Product Family: ThinkSystem
Serial: 1234567890

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
32x Samsung M393A4G43AB3-CWE 32 GB 2 rank 3200, configured at 2666

BIOS:
BIOS Vendor: Lenovo
BIOS Version: AFE113D-1.10
BIOS Date: 09/01/2021
BIOS Revision: 1.10
Firmware Revision: 1.12

(End of data from sysinfo program)

Compiler Version Notes
==============================================================================
| C       | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base) 525.x264_r(base) 557.xz_r(base) |
------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
| C++     | 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base) 541.leela_r(base) |
------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
| Fortran | 548.exchange2_r(base) |
------------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on

(Continued on next page)
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Compiler Version Notes (Continued)

Intel(R) 64, Version 2021.1 Build 20201112_000000
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Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifort

Base Portability Flags

500 perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502 gcc_r: -DSPEC_LP64
505 mcf_r: -DSPEC_LP64
520 omnetpp_r: -DSPEC_LP64
523 xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525 x264_r: -DSPEC_LP64
531 deepsjeng_r: -DSPEC_LP64
541 leela_r: -DSPEC_LP64
548 exchange2_r: -DSPEC_LP64
557 xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
-ff1to -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
-lqkmalloc

C++ benchmarks:
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries

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Base Optimization Flags (Continued)

C++ benchmarks (continued):
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
-lqkmalloc

Fortran benchmarks:
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ipo -no-prec-div
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
-auto -mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin
-lqkmalloc

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-ICElake-F.xml
http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml

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