**Dell Inc.**

PowerEdge T350 (Intel Xeon E-2314, 2.80 GHz)  

<table>
<thead>
<tr>
<th>CPU2017 License</th>
<th>Dell Inc.</th>
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<tr>
<td>Test Date:</td>
<td>Sep-2021</td>
</tr>
<tr>
<td>Hardware Availability</td>
<td>Oct-2021</td>
</tr>
<tr>
<td>Software Availability</td>
<td>May-2021</td>
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**Threads**

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Value</th>
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</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>4</td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>4</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>4</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>4</td>
</tr>
<tr>
<td>623.xalanchmk_s</td>
<td>4</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>4</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>4</td>
</tr>
<tr>
<td>641.leela_s</td>
<td>4</td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>4</td>
</tr>
<tr>
<td>657.xz_s</td>
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**Hardware**

<table>
<thead>
<tr>
<th>CPU Name:</th>
<th>Intel Xeon E-2314</th>
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<tbody>
<tr>
<td>Max MHz:</td>
<td>4500</td>
</tr>
<tr>
<td>Nominal:</td>
<td>2800</td>
</tr>
<tr>
<td>Enabled:</td>
<td>4 cores, 1 chip</td>
</tr>
<tr>
<td>Orderable:</td>
<td>1 chip</td>
</tr>
<tr>
<td>Cache L1:</td>
<td>32 KB I + 48 KB D on chip per core</td>
</tr>
<tr>
<td>L2:</td>
<td>512 KB I+D on chip per core</td>
</tr>
<tr>
<td>L3:</td>
<td>8 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other:</td>
<td>None</td>
</tr>
<tr>
<td>Memory:</td>
<td>64 GB (2 x 32 GB 2Rx8 PC4-3200AA-E)</td>
</tr>
<tr>
<td>Storage:</td>
<td>70 GB on tmpfs</td>
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<tr>
<td>Other:</td>
<td>None</td>
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**Software**

<table>
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<tr>
<th>OS:</th>
<th>Red Hat Enterprise Linux 8.4 (Ootpa)</th>
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<tr>
<td>Compiler:</td>
<td>C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux; Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux; C/C++: Version 2021.1 of Intel C/C++ Compiler Classic Build 20201112 for Linux</td>
</tr>
<tr>
<td>Parallel:</td>
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<tr>
<td>Firmware:</td>
<td>Version 1.0.1 released Aug-2021</td>
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<tr>
<td>File System:</td>
<td>tmpfs</td>
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<tr>
<td>System State:</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers:</td>
<td>64-bit</td>
</tr>
<tr>
<td>Peak Pointers:</td>
<td>64-bit</td>
</tr>
<tr>
<td>Other:</td>
<td>jemalloc memory allocator V5.0.1</td>
</tr>
<tr>
<td>Power Management:</td>
<td>BIOS and OS set to prefer performance at the cost of additional power usage.</td>
</tr>
</tbody>
</table>

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**SPEC CPU®2017 Integer Speed Result**

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed®2017_int_base</td>
<td>10.2</td>
</tr>
<tr>
<td>SPECspeed®2017_int_peak</td>
<td>10.4</td>
</tr>
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**SPEC CPU®2017 Integer Speed Result**

<table>
<thead>
<tr>
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<td>10.2</td>
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Dell Inc.
PowerEdge T350 (Intel Xeon E-2314, 2.80 GHz)

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
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<tr>
<td>600.perlbench_s</td>
<td>4</td>
<td>265</td>
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<td>6.68</td>
<td>231</td>
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<td>10.6</td>
<td>358</td>
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<td>605.mcf_s</td>
<td>4</td>
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<td>20.5</td>
<td>230</td>
<td>20.5</td>
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<td>7.93</td>
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<td>13.9</td>
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<td>13.8</td>
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<td>13.9</td>
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<td>17.3</td>
<td>98.7</td>
<td>17.9</td>
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<td>641.leela_s</td>
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<td>648.exchange2_s</td>
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<td>19.3</td>
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<td>152</td>
<td>19.3</td>
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<tr>
<td>657.xz_s</td>
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<td>7.70</td>
<td>802</td>
<td>7.71</td>
<td>803</td>
<td>7.70</td>
</tr>
</tbody>
</table>

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/mnt/ramdisk/cpu2017-1.1.8-ic2021.1/lib/intel64:/mnt/ramdisk/cpu2017-1.1.8-ic2021.1/je5.0.1-64"
MALLOCONF = "retain:true"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM
memory using Redhat Enterprise Linux 8.0
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>/proc/sys/vm/drop_caches
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

(Continued on next page)
### General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Benchmark run from a 70 GB ramdisk created with the cmd: "mount -t tmpfs -o size=70G tmpfs /mnt/ramdisk"

---

### Platform Notes

**BIOS settings:**
- Virtualization Technology: Disabled
- System Profile: Custom
- CPU Power Management: Maximum Performance
- C1E: Disabled
- C States: Autonomous
- PCI ASPM L1 Link
- Power Management: Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.8-ic2021.1/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acaabf64d
running on localhost.localdomain Mon Sep 6 15:03:04 2021

**SUT (System Under Test) info as seen by some common utilities.**
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

**From /proc/cpuinfo**

```
model name : Intel(R) Xeon(R) E-2314 CPU @ 2.80GHz
  1 "physical id"s (chips)
  4 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 4
siblings : 4
physical 0: cores 0 1 2 3
```

**From lscpu from util-linux 2.32.1:**

```
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 4
On-line CPU(s) list: 0-3
Thread(s) per core: 1
Core(s) per socket: 4
```
## Platform Notes (Continued)

**Socket(s):** 1  
**NUMA node(s):** 1  
**Vendor ID:** GenuineIntel  
**BIOS Vendor ID:** Intel  
**CPU family:** 6  
**Model:** 167  
**Model name:** Intel(R) Xeon(R) E-2314 CPU @ 2.80GHz  
**BIOS Model name:** Intel(R) Xeon(R) E-2314 CPU @ 2.80GHz  
**Stepping:** 1  
**CPU MHz:** 3500.000  
**BogoMIPS:** 5616.00  
**Virtualization:** VT-x  
**L1d cache:** 48K  
**L1i cache:** 32K  
**L2 cache:** 512K  
**L3 cache:** 8192K  
**NUMA node0 CPU(s):** 0-3  
**Flags:** fpu vme de pse tsc msr pae mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrcr lahf_lm abm 3dnowprefetch cpuid_fault invpcid_single ssbd ibrs ibpb stibp ibrs_enhanced fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms invpcid mxp avx512f avx512dq rdseed adx smap avx512ifma clflushopt intel_pt avx512cd sha ni avx512bw avx512vl xsaves dtherm ida arat pln pts avx512vmbmi umip pku ospke avx512_vmbmi gfni vaes vpclmulqdq avx512_vnni avx512_bitalg avx512_vpopcntdq rdpid fsgs md_clear flush_l1d arch_capabilities

/proc/cpuinfo cache data  
cache size : 8192 KB  

From numactl --hardware  
**WARNING:** a numactl 'node' might or might not correspond to a physical chip.  
available: 1 nodes (0)  
node 0 cpus: 0 1 2 3  
node 0 size: 64030 MB  
node 0 free: 42700 MB  
node distances:  
node 0  
0: 10  

From /proc/meminfo  
MemTotal: 65567244 kB  
HugePages_Total: 0  
Hugepagesize: 2048 kB

(Continued on next page)
Dell Inc. PowerEdge T350 (Intel Xeon E-2314, 2.80 GHz)

SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

SPECspeed®2017_int_base = 10.2
SPECspeed®2017_int_peak = 10.4

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Platform Notes (Continued)

/sbin/tuned-adm active
Current active profile: throughput-performance

From /etc/*release* /etc/*version*
    os-release:
        NAME="Red Hat Enterprise Linux"
        VERSION="8.4 (Ootpa)"
        ID="rhel"
        ID_LIKE="fedora"
        VERSION_ID="8.4"
        PLATFORM_ID="platform:el8"
        PRETTY_NAME="Red Hat Enterprise Linux 8.4 (Ootpa)"
        ANSI_COLOR="0;31"
    redhat-release: Red Hat Enterprise Linux release 8.4 (Ootpa)
    system-release: Red Hat Enterprise Linux release 8.4 (Ootpa)
    system-release-cpe: cpe:/o:redhat:enterprise_linux:8.4:ga

uname -a:
Linux localhost.localdomain 4.18.0-305.el8.x86_64 #1 SMP Thu Apr 29 08:54:30 EDT 2021
        x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swaps barriers and __user pointer sanitation
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Sep 6 08:54
SPEC is set to: /mnt/ramdisk/cpu2017-1.1.8-ic2021.1
    Filesystem     Type Size  Used Avail Use% Mounted on
    tmpfs          tmpfs  70G  16G  55G  22% /mnt/ramdisk

From /sys/devices/virtual/dmi/id
    Vendor: Dell Inc.
    Product: PowerEdge T350

(Continued on next page)
SPEC CPU®2017 Integer Speed Result

Dell Inc.

PowerEdge T350 (Intel Xeon E-2314, 2.80 GHz)

SPECspeed®2017_int_base = 10.2
SPECspeed®2017_int_peak = 10.4

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Sep-2021
Hardware Availability: Oct-2021
Software Availability: May-2021

Platform Notes (Continued)

Product Family: PowerEdge
Serial: B50KFF3

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
2x 00CE00000C01 M391A4G43AB1-CWE 32 GB 2 rank 3200

BIOS:
BIOS Vendor: Dell Inc.
BIOS Version: 1.0.1
BIOS Date: 08/18/2021
BIOS Revision: 1.0

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C       | 600.perlbench_s(peak)
---
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
==============================================================================

==============================================================================
C       | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)
 | 625.x264_s(base, peak) 657.xz_s(base, peak)
---
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
==============================================================================

(Continued on next page)
Dell Inc.

PowerEdge T350 (Intel Xeon E-2314, 2.80 GHz)

SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

Dell Inc.

SPECspeed®2017_int_base = 10.2

SPECspeed®2017_int_peak = 10.4

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.
Test Date: Sep-2021
Hardware Availability: Oct-2021
Software Availability: May-2021

Compiler Version Notes (Continued)

<table>
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<tr>
<th>C</th>
<th>600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak) 657.xz_s(base, peak)</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113</td>
</tr>
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<td></td>
<td>Copyright (C) 1985-2020 Intel Corporation. All rights reserved.</td>
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<tr>
<th>C++</th>
<th>620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)</th>
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</tbody>
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<table>
<thead>
<tr>
<th>Fortran</th>
<th>648.exchange2_s(base, peak)</th>
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<tbody>
<tr>
<td></td>
<td>Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000</td>
</tr>
<tr>
<td></td>
<td>Copyright (C) 1985-2020 Intel Corporation. All rights reserved.</td>
</tr>
</tbody>
</table>

Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifort

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX

(Continued on next page)
SPEC CPU®2017 Integer Speed Result

Dell Inc.
PowerEdge T350 (Intel Xeon E-2314, 2.80 GHz)

SPECspeed®2017_int_base = 10.2
SPECspeed®2017_int_peak = 10.4

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.
Test Date: Sep-2021
Hardware Availability: Oct-2021
Software Availability: May-2021

Base Portability Flags (Continued)

625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-DSPEC_OPENMP -std=c11 -m64 -fopenmp -Wl,-z,muldefs -xCORE-AVX512
-O3 -ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

C++ benchmarks:
-DSPEC_OPENMP -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin/
-1qkmalloc

Fortran benchmarks:
-m64 -xCORE-AVX512 -O3 -ipo -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-mbranches-within-32B-boundaries

Peak Compiler Invocation

C benchmarks (except as noted below):
icx

600.perlbench_s: icc

C++ benchmarks:
icpx

Fortran benchmarks:
ifort
SPEC CPU®2017 Integer Speed Result

Dell Inc.
PowerEdge T350 (Intel Xeon E-2314, 2.80 GHz)

SPECspeed®2017_int_base = 10.2
SPECspeed®2017_int_peak = 10.4

Peak Portability Flags
Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-strict-overflow
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

602.gcc_s: -m64 -std=c11 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdump(pass 2) -xCORE-AVX512 -flto
-Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

605.mcf_s: basepeak = yes

625.x264_s: -DSPEC_OPENMP -fiopenmp -std=c11 -m64 -Wl,-z,muldefs
-xCORE-AVX512 -flto -O3 -ffast-math
-qopt-mem-layout-trans=4 -fno-alias
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

657.xz_s: basepeak = yes

C++ benchmarks:

620.omnetpp_s: basepeak = yes

623.xalancbmk_s: basepeak = yes

631.deepsjeng_s: basepeak = yes

641.leela_s: basepeak = yes

Fortran benchmarks:

648.exchange2_s: basepeak = yes
### SPEC CPU®2017 Integer Speed Result

**Dell Inc.**

PowerEdge T350 (Intel Xeon E-2314, 2.80 GHz)

| SPECspeed®2017_int_base = 10.2 |
| SPECspeed®2017_int_peak = 10.4 |

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<th>CPU2017 License: 55</th>
<th>Test Date: Sep-2021</th>
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<tr>
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<td>Tested by: Dell Inc.</td>
<td>Software Availability: May-2021</td>
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The flags files that were used to format this result can be browsed at:


You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml

http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-ICX-rev1.4.xml

---

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.8 on 2021-09-06 16:03:03-0400.


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