## Dell Inc.

**PowerEdge R650xs (Intel Xeon Gold 5315Y, 3.20 GHz)**

<table>
<thead>
<tr>
<th>SPECspeed®2017_fp_base</th>
<th>104</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed®2017_fp_peak</td>
<td>107</td>
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### CPU2017 License: 55

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<td>Tested by:</td>
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<tr>
<th>Test Date:</th>
<th>Sep-2021</th>
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<tbody>
<tr>
<td>Hardware Availability:</td>
<td>Jul-2021</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>Dec-2020</td>
</tr>
</tbody>
</table>

### Hardware

| Thread  | 0 | 20 | 40 | 60 | 80 | 100 | 120 | 140 | 160 | 180 | 200 | 220 | 240 | 260 | 280 | 300 | 320 | 340 | 360 | 380 | 400 | 415 |
|---------|---|----|----|----|----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| 603.bwaves_s | 16 | 124 |
| 607.cactuBSSN_s | 16 | 84.0 |
| 619.ibm_s | 16 | 97.6 |
| 621.wrf_s | 16 | 59.1 |
| 627.cam4_s | 16 | 71.3 |
| 628.pop2_s | 16 | 69.6 |
| 638.imagick_s | 16 | 144 |
| 644.nab_s | 16 | 83.7 |
| 649.fotonik3d_s | 16 | 83.8 |
| 654.roms_s | 16 | 102 |

### Software

<table>
<thead>
<tr>
<th>OS:</th>
<th>Red Hat Enterprise Linux 8.3 (Ootpa)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Firmware:</td>
<td>Version 1.2.1 released May-2021</td>
</tr>
<tr>
<td>File System:</td>
<td>tmpfs</td>
</tr>
<tr>
<td>System State:</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers:</td>
<td>64-bit</td>
</tr>
<tr>
<td>Peak Pointers:</td>
<td>64-bit</td>
</tr>
<tr>
<td>Power Management:</td>
<td>BIOS and OS set to prefer performance at the cost of additional power usage.</td>
</tr>
</tbody>
</table>

### Notes

- **CPU Name:** Intel Xeon Gold 5315Y
- **Max MHz:** 3600
- **Nominal:** 3200
- **Enabled:** 16 cores, 2 chips
- **Orderable:** 1.2 chips
- **Cache L1:** 32 KB I + 48 KB D on chip per core
- **L2:** 1.25 MB I+D on chip per core
- **L3:** 12 MB I+D on chip per chip
- **Memory:** 512 GB (16 x 32 GB 2Rx4 PC4-3200AA-R, running at 2933)
- **Storage:** 225 GB on tmpfs
- **Other:** jemalloc memory allocator V5.0.1
Dell Inc. PowerEdge R650xs (Intel Xeon Gold 5315Y, 3.20 GHz)

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Base</th>
<th></th>
<th></th>
<th>Peak</th>
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<tbody>
<tr>
<td></td>
<td></td>
<td>Seconds</td>
<td>Ratio</td>
<td>Seconds</td>
<td>Ratio</td>
<td>Seconds</td>
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<tr>
<td>603.bwaves_s</td>
<td>16</td>
<td>144</td>
<td>409</td>
<td>144</td>
<td>411</td>
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<tr>
<td>607.cactuBSSN_s</td>
<td>16</td>
<td>128</td>
<td>130</td>
<td>135</td>
<td>124</td>
<td></td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>16</td>
<td>62.3</td>
<td>84.1</td>
<td>62.4</td>
<td>84.0</td>
<td></td>
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<tr>
<td>621.wrf_s</td>
<td>16</td>
<td>135</td>
<td>97.6</td>
<td>130</td>
<td>102</td>
<td></td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>16</td>
<td>150</td>
<td>59.1</td>
<td>149</td>
<td>59.3</td>
<td></td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>16</td>
<td>165</td>
<td>71.9</td>
<td>167</td>
<td>71.3</td>
<td></td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>16</td>
<td>207</td>
<td>69.6</td>
<td>207</td>
<td>69.6</td>
<td></td>
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<tr>
<td>644.nab_s</td>
<td>16</td>
<td>122</td>
<td>144</td>
<td>121</td>
<td>144</td>
<td></td>
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<tr>
<td>649.fotonik3d_s</td>
<td>16</td>
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<td>83.7</td>
<td>108</td>
<td>84.6</td>
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<tr>
<td>654.roms_s</td>
<td>16</td>
<td>152</td>
<td>103</td>
<td>154</td>
<td>102</td>
<td></td>
</tr>
</tbody>
</table>

SPECspeed®2017_fp_base = 104
SPECspeed®2017_fp_peak = 107

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes
Environment variables set by runcpu before the start of the run:
- KMP_AFFINITY = "granularity=fine,compact"
- LD_LIBRARY_PATH = "/mnt/ramdisk/cpu2017-1.1.8-ic2021.1/lib/intel64:/mnt/ramdisk/cpu2017-1.1.8-ic2021.1/je5.0.1-64"
- MALLOC_CONF = "retain:true"
- OMP_STACKSIZE = "192M"

General Notes
Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
  sync; echo 3> /proc/sys/vm/drop_caches
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

(Continued on next page)
SPEC CPU®2017 Floating Point Speed Result

Dell Inc.
PowerEdge R650xs (Intel Xeon Gold 5315Y, 3.20 GHz)

SPECspeed®2017_fp_base = 104
SPECspeed®2017_fp_peak = 107

CPU2017 License: 55  Test Date:  Sep-2021
Test Sponsor:  Dell Inc.  Hardware Availability:  Jul-2021
Tested by:  Dell Inc.  Software Availability:  Dec-2020

General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Benchmark run from a 225 GB ramdisk created with the cmd: "mount -t tmpfs -o size=225G tmpfs /mnt/ramdisk"

Platform Notes

BIOS settings:
  Logical Processor : Disabled
  Virtualization Technology : Disabled

  System Profile : Custom
  CPU Power Management : Maximum Performance
  C1E : Disabled
  C States : Autonomous
  Memory Patrol Scrub : Disabled
  Energy Efficiency Policy : Performance
  CPU Interconnect Bus Link
    Power Management : Disabled
  PCI ASPM L1 Link
    Power Management : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.8-ic2021.1/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec915b55891ef0e16a6ac64d
running on r650xs.1b86kd3.inside.dell.com Thu Sep  9 21:33:51 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
  https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) Gold 5315Y CPU @ 3.20GHz
  2 "physical id"s (chips)
  16 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
    cpu cores : 8
    siblings : 8
    physical 0: cores 0 1 2 3 4 5 6 7
    physical 1: cores 0 1 2 3 4 5 6 7

From lscpu from util-linux 2.32.1:
  Architecture:  x86_64

(Continued on next page)
### Platform Notes (Continued)

- **CPU op-mode(s):** 32-bit, 64-bit
- **Byte Order:** Little Endian
- **CPU(s):** 16
- **On-line CPU(s) list:** 0-15
- **Thread(s) per core:** 1
- **Core(s) per socket:** 8
- **Socket(s):** 2
- **NUMA node(s):** 2
- **Vendor ID:** GenuineIntel
- **CPU family:** 6
- **Model:** 106
- **Model name:** Intel(R) Xeon(R) Gold 5315Y CPU @ 3.20GHz
- **Stepping:** 6
- **CPU MHz:** 917.270
- **BogoMIPS:** 6400.00
- **Virtualization:** VT-x
- **L1d cache:** 48K
- **L1i cache:** 32K
- **L2 cache:** 1280K
- **L3 cache:** 12288K
- **NUMA node0 CPU(s):** 0,2,4,6,8,10,12,14
- **NUMA node1 CPU(s):** 1,3,5,7,9,11,13,15
- **Flags:** fpu vme de pse tsc msr pae mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nop_msr xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single intel_pmm ssbd mba ibrs ibpb stibp ibrs_original fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_hni avx512bw avx512vl xsaveopt xsaveprec xgetbv1 xsavegs qcm_l1c qcm_l1d qcm_mmu_data qcm_mmu_code split_lock_detect wbinvd dtcpli arat pnr ptxt avx512vmbidi unip pku ospe avx512vp vpf vpf_vmbidi gfini vaes vpclmulqdq avx512_vnni avx512_vbigbitalg tme avx512vp vpopcntdq lasm rdseed md_clear pconfig flush_l1d arch_capabilities

From `numactl --hardware`

**WARNING:** a numactl 'node' might or might not correspond to a physical chip.

- **available:** 2 nodes (0-1)
- **node 0 cpus:** 0 2 4 6 8 10 12 14
- **node 0 size:** 255012 MB
- **node 0 free:** 241876 MB
- **node 1 cpus:** 1 3 5 7 9 11 13 15
- **node 1 size:** 255693 MB

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(Continued on next page)
Dell Inc. PowerEdge R650xs (Intel Xeon Gold 5315Y, 3.20 GHz)

SPECspeed®2017_fp_base = 104
SPECspeed®2017_fp_peak = 107

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Platform Notes (Continued)

node 1 free: 256887 MB
node distances:
   node  0  1
      0:  10  20
      1:  20  10

From /proc/meminfo
   MemTotal:       527554532 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/sbin/tuned-adm active
   Current active profile: throughput-performance

From /etc/*release* /etc/*version*
   os-release:
      NAME="Red Hat Enterprise Linux"
      VERSION="8.3 (Ootpa)"
      ID="rhel"
      ID_LIKE="fedora"
      VERSION_ID="8.3"
      PLATFORM_ID="platform:el8"
      PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"
      ANSI_COLOR="0;31"
   redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
   system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga

uname -a:
   Linux r650xs.1b86kd3.inside.dell.com 4.18.0-240.el8.x86_64 #1 SMP Wed Sep 23 05:13:10 EDT 2020 x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2018-3639 (Speculative Store Bypass): Mitigation: usercopy/swapgs barriers and __user pointer sanitization
CVE-2017-5753 (Spectre variant 1): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
CVE-2017-5715 (Spectre variant 2):
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected

(Continued on next page)
Platform Notes (Continued)

CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Sep 9 18:40

SPEC is set to: /mnt/ramdisk/cpu2017-1.1.8-ic2021.1

Filesystem Type Size Used Avail Use% Mounted on
tmpfs tmpfs 225G 11G 215G 5% /mnt/ramdisk

From /sys/devices/virtual/dmi/id
Vendor: Dell Inc.
Product: PowerEdge R650xs
Product Family: PowerEdge
Serial: 1B86KD3

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
16x 00AD063200AD HMA84GR7DJR4N-XN 32 GB 2 rank 3200, configured at 2933

BIOS:
BIOS Vendor: Dell Inc.
BIOS Version: 1.2.1
BIOS Date: 05/28/2021
BIOS Revision: 1.2

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C | 619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base)
==============================================================================
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
==============================================================================
C | 644.nab_s(peak)
-----------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
(Continued on next page)
Compiler Version Notes (Continued)

C               | 619.lbm_s(base, peak) 638.imagick_s(base, peak)  
                | 644.nab_s(base)

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

C               | 644.nab_s(peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

C++, C, Fortran | 607.cactuBSSN_s(base, peak)

Intel(R) C++ Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

Fortran         | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak)  
                | 654.roms_s(base, peak)

Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

Fortran, C       | 621.wrf_s(base, peak) 627.cam4_s(base, peak)  
                | 628.pop2_s(base, peak)

Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on

(Continued on next page)
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Compiler Version Notes (Continued)

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Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)
64, Version 2021.1 Build 20201112_000000
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Base Compiler Invocation

C benchmarks:
icc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icc

Benchmarks using Fortran, C, and C++:
icpc icc ifort

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-m64 -std=c11 -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP
-mbranches-within-32B-boundaries

(Continued on next page)
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**Base Optimization Flags (Continued)**

Fortran benchmarks:
- `-m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX512 -ipo -O3`
- `-no-prec-div -qopt-prefetch -ffinite-math-only`
- `-qopt-mem-layout-trans=4 -qopenmp -nostandard-realloc-lhs`
- `-mbranches-within-32B-boundaries -L/usr/local/jemalloc64-5.0.1/lib`
- `-ljemalloc`

Benchmarks using both Fortran and C:
- `-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
- `-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp`
- `-DSPEC_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs`
- `-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

Benchmarks using Fortran, C, and C++:
- `-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
- `-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp`
- `-DSPEC_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs`
- `-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

**Peak Compiler Invocation**

C benchmarks (except as noted below):
- `icc`
- `644.nab_s: icx`

Fortran benchmarks:
- `ifort`

Benchmarks using both Fortran and C:
- `ifort icc`

Benchmarks using Fortran, C, and C++:
- `icpc icc ifort`

**Peak Portability Flags**

Same as Base Portability Flags
**SPEC CPU®2017 Floating Point Speed Result**

**Dell Inc.**

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### Peak Optimization Flags

**C benchmarks:**

619.lbm_s: basepeak = yes

638.imagick_s: basepeak = yes


**Fortran benchmarks:**

603.bwaves_s: basepeak = yes

649.fotonik3d_s: -m64 -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -DSPEC_SUPPRESS_OPENMP -DSPEC_OPENMP -ipo -xCORE-AVX512 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -gopenmp -nostandard-realloc-lhs -mbranches-within-32B-boundaries -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

654.roms_s: basepeak = yes

**Benchmarks using both Fortran and C:**

621.wrf_s: -m64 -std=c11 -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX512 -O3 -no-prec-div -qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

627.cam4_s: basepeak = yes

628.pop2_s: basepeak = yes

**Benchmarks using Fortran, C, and C++:**

607.cactuBSSN_s: basepeak = yes
Dell Inc.

PowerEdge R650xs (Intel Xeon Gold 5315Y, 3.20 GHz)

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The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml
http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-ICX-rev1.4.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.8 on 2021-09-09 22:33:50-0400.
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