Dell Inc.  
PowerEdge C6420 (Intel Xeon Gold 6238R, 2.20 GHz)  

**SPECspeed\textsuperscript{®}2017_fp_base = 142**  
**SPECspeed\textsuperscript{®}2017_fp_peak = 144**

---

### Hardware

- **CPU Name:** Intel Xeon Gold 6238R
- **Max MHz:** 4000
- **Nominal:** 2200
- **Enabled:** 56 cores, 2 chips
- **Orderable:** 1.2 chips
- **Cache L1:** 32 KB I + 32 KB D on chip per core
- **L2:** 1 MB I+D on chip per core
- **L3:** 38.5 MB I+D on chip per chip
- **Other:** None
- **Memory:** 384 GB (12 x 32 GB 2Rx4 PC4-2933Y-R)
- **Storage:** 1 x 1.6 TB SATA SSD
- **Other:** None

---

### Software

- **OS:** Red Hat Enterprise Linux 8.3 (Ootpa)  
  4.18.0-240.el8.x86_64
- **Compiler:**  
  C/C++: Version 2021.1 of Intel oneAPI DPC++/C++  
  Compiler Build 20201113 for Linux;  
  Fortran: Version 2021.1 of Intel Fortran Compiler  
  Classic Build 20201112 for Linux;  
  C/C++: Version 2021.1 of Intel C/C++ Compiler  
  Classic Build 20201112 for Linux
- **Parallel:** Yes
- **Firmware:** Version 2.13.1 released Nov-2021
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 64-bit
- **Other:** jemalloc memory allocator V5.0.1
- **Power Management:** BIOS and OS set to prefer performance at the cost of additional power usage.
Dell Inc. PowerEdge C6420 (Intel Xeon Gold 6238R, 2.20 GHz)   SPECspeed®2017_fp_base = 142
SPECspeed®2017_fp_peak = 144

Results Table

<table>
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<th>Seconds</th>
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<th>Threads</th>
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<td>498</td>
<td>119</td>
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<td>498</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>56</td>
<td>88.9</td>
<td>187</td>
<td>89.6</td>
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<td>117</td>
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<td>117</td>
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<td>628.pop2_s</td>
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<td>117</td>
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<td>56</td>
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<tr>
<td>644.nab_s</td>
<td>56</td>
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<td>293</td>
<td>59.6</td>
<td>56</td>
<td>59.6</td>
<td>293</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>56</td>
<td>105</td>
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<td>102</td>
<td>154</td>
<td>103</td>
<td>56</td>
<td>102</td>
<td>154</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2017-1.1.8-ic2021.1/lib/intel64:/home/cpu2017-1.1.8-ic2021.1/j e5.0.1-64"
MALLOCONF = "retain:true"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>/proc/sys/vm/drop_caches
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
SPEC CPU®2017 Floating Point Speed Result
Copyright 2017-2021 Standard Performance Evaluation Corporation

Dell Inc. PowerEdge C6420 (Intel Xeon Gold 6238R, 2.20 GHz)

SPECspeed®2017_fp_base = 142
SPECspeed®2017_fp_peak = 144

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.
Test Date: Nov-2021
Hardware Availability: Apr-2019
Software Availability: Dec-2020

General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS settings:
   Logical Processor : Disabled
   Virtualization Technology : Disabled
   System Profile : Custom
   CPU Power Management : Maximum Performance
      C1E : Disabled
      C States : Autonomous
   Memory Patrol Scrub : Disabled
   Energy Efficiency Policy : Performance
   CPU Interconnect Bus Link
   Power Management : Disabled
   PCI ASPM L1 Link
   Power Management : Disabled

Sysinfo program /home/cpu2017-1.1.8-ic2021.1/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acaf64d
running on localhost.localdomain Wed Nov 10 14:58:59 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6238R CPU @ 2.20GHz
2 "physical id"s (chips)
56 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 28
siblings : 28
physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27 28 29 30
physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27 28 29 30

From lscpu from util-linux 2.32.1:
Architecture: x86_64

(Continued on next page)
Dell Inc.

PowerEdge C6420 (Intel Xeon Gold 6238R, 2.20 GHz)

SPECspeed®2017_fp_base = 142
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CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Platform Notes (Continued)

CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 56
On-line CPU(s) list: 0-55
Thread(s) per core: 1
Core(s) per socket: 28
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 85
Model name: Intel(R) Xeon(R) Gold 6238R CPU @ 2.20GHz
Stepping: 7
CPU MHz: 1835.537
CPU max MHz: 4000.0000
CPU min MHz: 1000.0000
BogoMIPS: 4400.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 39424K
NUMA node0 CPU(s):
0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30,32,34,36,38,40,42,44,46,48,50,52,54
NUMA node1 CPU(s):
1,3,5,7,9,11,13,15,17,19,21,23,25,27,29,31,33,35,37,39,41,43,45,47,49,51,53,55
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtsscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xptr pdcm pcdi dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3nowprefetch cpuid_fault epb cat_l3 cdp_l3
invpcid_single intel_ppn ssbd mba ibrs ibpb stibp ibrs_enabled fsgbase tsc_adjust
bm1 hle avx2 smep bmi2 ets invpcid cmx mpx rdt_a avx512f avx512dq rdseed adx smap
cflushopt clwb intel_pt avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves
cmq_llc cmq_occup_llc cmq_mbm_total cmq_mbm_local dtherm ida arat pln pts pku ospe
avx512_vnni md_clear flush_l1d arch_capabilities

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0 2 4 6 8 10 12 14 16 18 20 22 24 26 28 30 32 34 36 38 40 42 44 46 48 50
52 54
node 0 size: 181012 MB

(Continued on next page)
Platform Notes (Continued)

node 0 free: 185713 MB
node 1 cpus: 1 3 5 7 9 11 13 15 17 19 21 23 25 27 29 31 33 35 37 39 41 43 45 47 49 51
  53 55
node 1 size: 184469 MB
node 1 free: 183264 MB
node distances:
node 0 1
  0: 10 21
  1: 21 10

From /proc/meminfo
MemTotal: 394815752 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/sbin/tuned-adm active
Current active profile: throughput-performance

/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has
performance

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux"
VERSION="8.3 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.3"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"
ANSI_COLOR="0;31"

redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga

uname -a:
Linux localhost.localdomain 4.18.0-240.el8.x86_64 #1 SMP Wed Sep 23 05:13:10 EDT 2020
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2018-12207 (iTLB Multihit):
CVE-2018-3620 (L1 Terminal Fault):
Microarchitectural Data Sampling:
CVE-2017-5754 (Meltdown):
CVE-2018-3639 (Speculative Store Bypass):

KVM: Mitigation: Split huge pages
Not affected
Not affected
Not affected
Mitigation: Speculative Store
Bypass disabled via prctl and
Dell Inc.  
PowerEdge C6420 (Intel Xeon Gold 6238R, 2.20 GHz)

<table>
<thead>
<tr>
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<td>SPECspeed®2017_fp_peak = 144</td>
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CPU2017 License: 55  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.  
Test Date: Nov-2021  
Hardware Availability: Apr-2019  
Software Availability: Dec-2020

Platform Notes (Continued)

CVE-2017-5753 (Spectre variant 1):  
Mitigation: uscopyswapgs barriers and __user pointer sanitization

CVE-2017-5715 (Spectre variant 2):  
Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected

CVE-2019-11135 (TSX Asynchronous Abort):  
Mitigation: TSX disabled

run-level 3 Nov 10 12:11

SPEC is set to: /home/cpu2017-1.1.8-ic2021.1

Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs 390G 16G 375G 4% /home

From /sys/devices/virtual/dmi/id
Vendor: Dell Inc.  
Product: PowerEdge C6420  
Product Family: PowerEdge

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
7x 00AD00B300AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933
2x 00AD063200AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933
3x 00AD069D00AD HMA84GR7CJR4N-WM 32 GB 2 rank 2933
4x Not Specified Not Specified

BIOS:
BIOS Vendor: Dell Inc.  
BIOS Version: 2.13.1  
BIOS Date: 11/03/2021  
BIOS Revision: 2.13

(End of data from sysinfo program)

Compiler Version Notes

====================================================================================================
C | 619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base)
====================================================================================================

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000

(Continued on next page)
Dell Inc. PowerEdge C6420 (Intel Xeon Gold 6238R, 2.20 GHz)

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Nov-2021
Hardware Availability: Apr-2019
Software Availability: Dec-2020

SPECspeed®2017_fp_base = 142
SPECspeed®2017_fp_peak = 144

Compiler Version Notes (Continued)

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==============================================================================
C | 644.nab_s(peak)
-----------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
-----------------------------------------------------------------------------
C | 619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base)
-----------------------------------------------------------------------------
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64,
Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
-----------------------------------------------------------------------------
C | 644.nab_s(peak)
-----------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
-----------------------------------------------------------------------------
C++, C, Fortran | 607.cactuBSSN_s(base, peak)
-----------------------------------------------------------------------------
Intel(R) C++ Intel(R) 64 Compiler Classic for applications running on
Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64,
Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on
Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
-----------------------------------------------------------------------------
Fortran | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak) 654.roms_s(base, peak)
-----------------------------------------------------------------------------
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on

(Continued on next page)
Dell Inc.  
PowerEdge C6420 (Intel Xeon Gold 6238R, 2.20 GHz)

SPECspeed®2017_fp_base = 142
SPECspeed®2017_fp_peak = 144

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

**Compiler Version Notes (Continued)**

Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on
Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)
64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

**Base Compiler Invocation**

C benchmarks:
- icc

Fortran benchmarks:
- ifort

Benchmarks using both Fortran and C:
- ifort icc

Benchmarks using Fortran, C, and C++:
- icpc icc ifort

**Base Portability Flags**

- 603.bwaves_s: -DSPEC_LP64
- 607.cactuBSSN_s: -DSPEC_LP64
- 619.lbm_s: -DSPEC_LP64
- 621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
- 627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
- 628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
  -assume byterecl
- 638.imagick_s: -DSPEC_LP64
- 644.nab_s: -DSPEC_LP64
- 649.fotonik3d_s: -DSPEC_LP64
- 654.roms_s: -DSPEC_LP64
Dell Inc.  
PowerEdge C6420 (Intel Xeon Gold 6238R, 2.20 GHz)  

| SPECspeed®2017_fp_base = 142 |
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| CPU2017 License: 55 | Test Date: Nov-2021 |
| Test Sponsor: Dell Inc. | Hardware Availability: Apr-2019 |
| Tested by: Dell Inc. | Software Availability: Dec-2020 |

**Base Optimization Flags**

C benchmarks:
- `m64 -std=c11 -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch`
- `ffinite-math-only -qopt-mem-layout-trans=4 -gopenmp -DSPEC_OPENMP`
- `mbranches-within-32B-boundaries`

Fortran benchmarks:
- `m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX512 -ipo -O3`
- `no-prec-div -qopt-prefetch -ffinite-math-only`
- `qopt-mem-layout-trans=4 -gopenmp -nostandard-realloc-lhs`
- `mbranches-within-32B-boundaries -L/usr/local/jemalloc64-5.0.1/lib`
- `-ljemalloc`

Benchmarks using both Fortran and C:
- `m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
- `qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -gopenmp`
- `DSPEC_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs`
- `-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

Benchmarks using Fortran, C, and C++:
- `m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div`
- `qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4 -gopenmp`
- `DSPEC_OPENMP -mbranches-within-32B-boundaries -nostandard-realloc-lhs`
- `-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

**Peak Compiler Invocation**

C benchmarks (except as noted below):
- `icc`
- `644.nab_s: icx`

Fortran benchmarks:
- `ifort`

Benchmarks using both Fortran and C:
- `ifort icc`

Benchmarks using Fortran, C, and C++:
- `icpc icc ifort`
Dell Inc. PowerEdge C6420 (Intel Xeon Gold 6238R, 2.20 GHz) SPECspeed®2017_fp_base = 142
SPECspeed®2017_fp_peak = 144

CPU2017 License: 55
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Nov-2021
Hardware Availability: Apr-2019
Software Availability: Dec-2020

Peak Portability Flags
Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

619.lbm_s: basepeak = yes
638.imagick_s: basepeak = yes
644.nab_s: -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-f1to -mfpmath=sse -funroll-loops -fiopenmp
-DSPEC_OPENMP -qopt-mem-layout-trans=4
-fimf-accuracy-bits=14:sqrt
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Fortran benchmarks:

603.bwaves_s: basepeak = yes
649.fotonik3d_s: -m64 -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-DSPEC_SUPPRESS_OPENMP -DSPEC_OPENMP -ipo -xCORE-AVX512
-03 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=4 -qopenmp -nostandard-realloc-lhs
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

654.roms_s: basepeak = yes

Benchmarks using both Fortran and C:

621.wrf_s: -m64 -std=c11 -Wl,-z,muldefs -prof-gen(pass 1)
-prof-use(pass 2) -ipo -xCORE-AVX512 -03 -no-prec-div
-qopt-prefetch -ffinite-math-only -qopt-mem-layout-trans=4
-DSPEC_SUPPRESS_OPENMP -qopenmp -DSPEC_OPENMP
-mbranches-within-32B-boundaries -nostandard-realloc-lhs
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

627.cam4_s: basepeak = yes
628.pop2_s: basepeak = yes

(Continued on next page)
Dell Inc.
PowerEdge C6420 (Intel Xeon Gold 6238R, 2.20 GHz)

SPECspeed®2017_fp_base = 142
SPECspeed®2017_fp_peak = 144

CPU2017 License: 55
Test Sponsor: Dell Inc.
Test Date: Nov-2021
Tested by: Dell Inc.
Hardware Availability: Apr-2019
Software Availability: Dec-2020

Peak Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++:

607.cactuBSSN_s: basepeak = yes

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml
http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-ICX-rev1.4.xml

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Tested with SPEC CPU®2017 v1.1.8 on 2021-11-10 15:58:58-0500.
Report generated on 2021-12-07 17:00:58 by CPU2017 PDF formatter v6442.
Originally published on 2021-12-07.