## SPEC CPU®2017 Floating Point Rate Result

**New H3C Technologies Co., Ltd.**

H3C UniServer R4300 G5 (Intel Xeon Gold 5315Y)

<table>
<thead>
<tr>
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<th>SPECrate®2017_fp_base = 164</th>
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<tr>
<td>554.roms_r</td>
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</table>

### Hardware
- **CPU Name:** Intel Xeon Gold 5315Y
  - **Max MHz:** 3600
  - **Nominal:** 3200
  - **Enabled:** 16 cores, 2 chips, 2 threads/core
  - **Orderable:** 1,2 chips
  - **Cache L1:** 32 KB I + 48 KB D on chip per core
  - **L2:** 1.25 MB I+D on chip per core
  - **L3:** 12 MB I+D on chip per chip
  - **Memory:** 512 GB (16 x 32 GB 2Rx4 PC4-3200AA-R, running at 2933)
  - **Storage:** 1 x 600GB 10000RPM SAS HDD
  - **Other:** None

### Software
- **OS:** Red Hat Enterprise Linux release 8.2 (Ootpa) 4.18.0-193.el8.x86_64
- **Compiler:** C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux; Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux;
- **Firmware:** Version 5.39 released Nov-2021 BIOS
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** Not Applicable
- **Other:** jemalloc memory allocator V5.0.1
- **Power Management:** BIOS and OS set to prefer performance at the cost of additional power usage.

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**Test Sponsor:** New H3C Technologies Co., Ltd.  
**Test Date:** Dec-2021  
**Hardware Availability:** Jun-2021  
**Software Availability:** Dec-2020  
**CPU2017 License:** 9066  
**Tested by:** New H3C Technologies Co., Ltd.
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Results Table

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SPECrate®2017_fp_base = 164
SPECrate®2017_fp_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes
The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/speccpu/lib/intel64:/home/speccpu/je5.0.1-64"
MALLOC_CONF = "retain:true"

General Notes
Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Red Hat Enterprise Linux 8.1
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
General Notes (Continued)

is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>/proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

BIOS Settings:
Set SNC (Sub NUMA) to Enabled
Set Power Performance Tuning to BIOS Controls EPB
Set Energy Performance BIAS to Performance
Set XPT Prefetch to Enabled

Sysinfo program /home/speccpu/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16aaca65d
running on localhost.localdomain Mon Dec 13 13:30:44 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 5315Y CPU @ 3.20GHz
  2 "physical id"s (chips)
  32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 8
siblings : 16
physical 0: cores 0 1 2 3 4 5 6 7
physical 1: cores 0 1 2 3 4 5 6 7

From lscpu from util-linux 2.32.1:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 32
On-line CPU(s) list: 0-31

(Continued on next page)
New H3C Technologies Co., Ltd.

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CPU2017 License: 9066
Test Sponsor: New H3C Technologies Co., Ltd.
Tested by: New H3C Technologies Co., Ltd.

Thread(s) per core: 2
Core(s) per socket: 8
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 106
Model name: Intel(R) Xeon(R) Gold 5315Y CPU @ 3.20GHz
Stepping: 6
CPU MHz: 3500.000
CPU max MHz: 3600.0000
CPU min MHz: 800.0000
BogoMIPS: 6400.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 12288K
NUMA node0 CPU(s): 0-7,16-23
NUMA node1 CPU(s): 8-15,24-31
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmprefx pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtrar pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_13 invpecid_single ssbd
mba ibpb stibp ibrs enhanced tpr_shadow vmbi flexpriority ept vpid fsgsbase
tsc_adjust bmi1 hle avx2 smep bmi2 erts invpcid rtm cqm rdt_a avx512f avx512dq
rdsed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha ni avx512bw
avx512vl xsaveopt xsaves xgetbv1 xsaves cqm_llc cqm_occapsus_11c cqm_mbms_total
numa_mems_local wblock dtherm ida arat pni pts hwp hwp_act_window hwp_epp
hwp pkg req avx512bfvmi umip pku ospe avx512_vbmi2 gfnf vaes vpcmldqavx512_vnni
avx512_3dnowvavx512_bitalg tme avx512_vpopcntdq la57 rdpid md_clear pconfig flush_lid
arch_capabilities

From numactl --hardware

WARNING: a numactl 'node' might or might not correspond to a physical chip.

available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7 16 17 18 19 20 21 22 23
node 0 size: 257192 MB
node 0 free: 249435 MB
node 1 cpus: 8 9 10 11 12 13 14 15 24 25 26 27 28 29 30 31
node 1 size: 258041 MB
node 1 free: 253418 MB

(Continued on next page)
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Test Date: Dec-2021
Hardware Availability: Jun-2021
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Platform Notes (Continued)

node distances:
node 0 1
0: 10 20
1: 20 10

From /proc/meminfo
MemTotal: 527600172 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/sbin/tuned-adm active
Current active profile: throughput-performance

/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has performance

From /etc/*release* /etc/*version*
    os-release:
      NAME="Red Hat Enterprise Linux"
      VERSION="8.2 (Ootpa)"
      ID="rhel"
      ID_LIKE="fedora"
      VERSION_ID="8.2"
      PLATFORM_ID="platform:el8"
      PRETTY_NAME="Red Hat Enterprise Linux 8.2 (Ootpa)"
      ANSI_COLOR="0;31"
    redhat-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
    system-release: Red Hat Enterprise Linux release 8.2 (Ootpa)
    system-release-cpe: cpe:/o:redhat:enterprise_linux:8.2:ga

uname -a:
    Linux localhost.localdomain 4.18.0-193.el8.x86_64 #1 SMP Fri Mar 27 14:35:58 UTC 2020
    x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit):
    Not affected
CVE-2018-3620 (L1 Terminal Fault):
    Not affected
Microarchitectural Data Sampling:
    Not affected
CVE-2017-5754 (Meltdown):
    Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2018-3639 (Speculative Store Bypass):
    Mitigation: usercopy/swapsgs barriers and __user pointer sanitization
CVE-2017-5753 (Spectre variant 1):
    Mitigation: Enhanced IBRS, IBPB:
CVE-2017-5715 (Spectre variant 2):

(Continued on next page)
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Platform Notes (Continued)

conditional, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling): No status reported
CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Dec 13 10:11
SPEC is set to: /home/speccpu
Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs 504G 146G 358G 29% /home

From /sys/devices/virtual/dmi/id
Product Family: Rack

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
16x Micron 36ASF4G72PZ-3G2E7 32 GB 2 rank 3200, configured at 2933
16x NO DIMM NO DIMM

BIOS:
  BIOS Vendor: American Megatrends International, LLC.
  BIOS Version: 5.39
  BIOS Date: 11/17/2021
  BIOS Revision: 5.22

(End of data from sysinfo program)

Compiler Version Notes

C                                             | 519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)
-----------------------------------------------|----------------------------------------------------
Intel(R) oneAPI DPC+/C++ Compiler for applications running on Intel(R) 64,
  Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

C++                                           | 508.namd_r(base) 510.parest_r(base)
-----------------------------------------------|----------------------------------------------------
Intel(R) oneAPI DPC+/C++ Compiler for applications running on Intel(R) 64,
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### Compiler Version Notes (Continued)

C++, C  | 511.povray_r(base) 526.blender_r(base)

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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

---

C++, C, Fortran | 507.cactuBSSN_r(base)

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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

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Fortran | 503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)

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Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on  
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Fortran, C | 521.wrf_r(base) 527.cam4_r(base)

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## Base Compiler Invocation

**C benchmarks:**
- icx

**C++ benchmarks:**
- icpx

**Fortran benchmarks:**
- ifort

**Benchmarks using both Fortran and C:**
- ifort icx

**Benchmarks using both C and C++:**
- icpx icx

**Benchmarks using Fortran, C, and C++:**
- icpx icx ifort

---

## Base Portability Flags

- 503.bwaves_r: -DSPEC_LP64
- 507.cactuBSSN_r: -DSPEC_LP64
- 508.namd_r: -DSPEC_LP64
- 510.parest_r: -DSPEC_LP64
- 511.povray_r: -DSPEC_LP64
- 519.lbm_r: -DSPEC_LP64
- 521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
- 526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
- 527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
- 538.imagick_r: -DSPEC_LP64
- 544.nab_r: -DSPEC_LP64
- 549.fotonik3d_r: -DSPEC_LP64
- 554.roms_r: -DSPEC_LP64

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## Base Optimization Flags

**C benchmarks:**
- -w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
- -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
- -mbranches-within-32B-boundaries -ljemalloc
- -L/usr/local/jemalloc64-5.0.1/lib

*(Continued on next page)*
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Base Optimization Flags (Continued)

C++ benchmarks:
-\( -w \) -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto
-\( -mfpmath=sse \) -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries -ljemalloc
-\( -L/usr/local/jemalloc64-5.0.1/lib \)

Fortran benchmarks:
-\( -w \) -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ipo -no-prec-div
-qopt-prefetch -ffinite-math-only
-qopt-multiple-gather-scatter-by-shuffles -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-mbranches-within-32B-boundaries -ljemalloc
-\( -L/usr/local/jemalloc64-5.0.1/lib \)

Benchmarks using both Fortran and C:
-\( -w \) -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-\( -flto \) -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -O3 -ipo
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-multiple-gather-scatter-by-shuffles
-mbranches-within-32B-boundaries -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both C and C++:
-\( -w \) -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-\( -flto \) -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries -ljemalloc
-\( -L/usr/local/jemalloc64-5.0.1/lib \)

Benchmarks using Fortran, C, and C++:
-\( -w \) -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-\( -flto \) -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-multiple-gather-scatter-by-shuffles
-mbranches-within-32B-boundaries -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml
http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.0-CPX-RevD.xml
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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.8 on 2021-12-13 00:30:44-0500.
Report generated on 2022-01-10 11:02:10 by CPU2017 PDF formatter v6442.
Originally published on 2022-01-07.