**SPEC CPU®2017 Floating Point Rate Result**

xFusion

xFusion 1288H V6 (Intel Xeon Gold 5318Y)  

<table>
<thead>
<tr>
<th><strong>CPU2017 License</strong></th>
<th>6488</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Test Sponsor</strong></td>
<td>xFusion</td>
</tr>
<tr>
<td><strong>Tested by</strong></td>
<td>xFusion</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>CPU Name: Intel Xeon Gold 5318Y</th>
</tr>
</thead>
<tbody>
<tr>
<td>Max MHz: 3400</td>
</tr>
<tr>
<td>Nominal: 2100</td>
</tr>
<tr>
<td>Enabled: 48 cores, 2 chips, 2 threads/core</td>
</tr>
<tr>
<td>Orderable: 1,2 chips</td>
</tr>
<tr>
<td>Cache L1: 32 KB I + 48 KB D on chip per core</td>
</tr>
<tr>
<td>L2: 1.25 MB I+D on chip per core</td>
</tr>
<tr>
<td>L3: 36 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Memory: 512 GB (16 x 32 GB 2Rx4 PC4-3200AA-R, running at 2933)</td>
</tr>
<tr>
<td>Storage: 1 x 960 GB SATA SSD</td>
</tr>
<tr>
<td>Other: None</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Software</th>
</tr>
</thead>
<tbody>
<tr>
<td>OS: Red Hat Enterprise Linux release 8.4 (Ootpa) 4.18.0-305.el8.x86_64</td>
</tr>
<tr>
<td>Compiler: C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux; Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux</td>
</tr>
<tr>
<td>Parallel: No</td>
</tr>
<tr>
<td>Firmware: Version 0.95 Released Dec-2021</td>
</tr>
<tr>
<td>File System: xfs</td>
</tr>
<tr>
<td>System State: Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers: 64-bit</td>
</tr>
<tr>
<td>Peak Pointers: Not Applicable</td>
</tr>
<tr>
<td>Other: jemalloc memory allocator V5.0.1</td>
</tr>
<tr>
<td>Power Management: BIOS and OS set to prefer performance at the cost of additional power usage</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Copies</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r 96</td>
</tr>
<tr>
<td>507.cactuBSSN_r 96</td>
</tr>
<tr>
<td>508.namd_r 96</td>
</tr>
<tr>
<td>510.parest_r 96</td>
</tr>
<tr>
<td>511.povray_r 96</td>
</tr>
<tr>
<td>519.lbm_r 96</td>
</tr>
<tr>
<td>521.wrf_r 96</td>
</tr>
<tr>
<td>526.blender_r 96</td>
</tr>
<tr>
<td>527.cam4_r 96</td>
</tr>
<tr>
<td>538.imagick_r 96</td>
</tr>
<tr>
<td>544.nab_r 96</td>
</tr>
<tr>
<td>549.fotonik3d_r 96</td>
</tr>
<tr>
<td>554.roms_r 96</td>
</tr>
</tbody>
</table>

| SPECrate®2017_fp_base = 340 |

**Not Run**

| Test Date: Apr-2022 |
| Hardware Availability: Apr-2021 |
| Software Availability: May-2021 |

**SPECrate®2017_fp_base = 340**
Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<tbody>
<tr>
<td></td>
<td>Base</td>
<td>Peak</td>
<td>Base</td>
<td>Peak</td>
<td>Base</td>
<td>Peak</td>
<td></td>
</tr>
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<td>507.cactuBSSN_r</td>
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<td>451</td>
<td>268</td>
<td><strong>454</strong></td>
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<tr>
<td>508.namd_r</td>
<td>96</td>
<td>372</td>
<td>245</td>
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<td>370</td>
<td>247</td>
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<tr>
<td>510.parest_r</td>
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<td><strong>1348</strong></td>
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<tr>
<td>511.povray_r</td>
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<td>635</td>
<td>353</td>
<td><strong>633</strong></td>
<td><strong>354</strong></td>
<td>629</td>
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<tr>
<td>519.lbm_r</td>
<td>96</td>
<td>398</td>
<td>254</td>
<td><strong>397</strong></td>
<td>255</td>
<td>396</td>
<td>255</td>
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<tr>
<td>521.wrf_r</td>
<td>96</td>
<td><strong>666</strong></td>
<td><strong>323</strong></td>
<td>668</td>
<td>322</td>
<td>660</td>
<td>326</td>
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<tr>
<td>526.blender_r</td>
<td>96</td>
<td>435</td>
<td>336</td>
<td><strong>433</strong></td>
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<td>338</td>
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<td>527.cam4_r</td>
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<td>494</td>
<td>340</td>
<td><strong>494</strong></td>
<td><strong>340</strong></td>
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<td>538.imagick_r</td>
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<td><strong>918</strong></td>
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<tr>
<td>544.nab_r</td>
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<td>551</td>
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<td><strong>553</strong></td>
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<tr>
<td>549.fotonik3d_r</td>
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<td><strong>221</strong></td>
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<tr>
<td>554.roms_r</td>
<td>96</td>
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<td>147</td>
<td>1033</td>
<td>148</td>
<td><strong>1033</strong></td>
<td><strong>148</strong></td>
</tr>
</tbody>
</table>

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/spec2017/lib/intel64:/spec2017/je5.0.1-64"
MALLOC_CONF = "retain:true"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Red Hat Enterprise Linux 8.1
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:

(Continued on next page)
General Notes (Continued)

sync; echo 3 > /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

BIOS configuration:
Performance Profile Set to Performance
SNC Set to Enabled SNC2 (2-clusters)

Sysinfo program /spec2017/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acfc64d
running on localhost.localdomain Tue Apr 12 15:37:38 2022

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

  model name : Intel(R) Xeon(R) Gold 5318Y CPU @ 2.10GHz
  2 "physical id"'s (chips)
  96 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 24
  siblings : 48
  physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23
  physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23

From lscpu from util-linux 2.32.1:
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Byte Order:            Little Endian
CPU(s):                96
On-line CPU(s) list:   0–95
Thread(s) per core:    2
Core(s) per socket:    24

(Continued on next page)
Platform Notes (Continued)

Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel
BIOS Vendor ID: Intel(R) Corporation
CPU family: 6
Model: 106
Model name: Intel(R) Xeon(R) Gold 5318Y CPU @ 2.10GHz
BIOS Model name: Intel(R) Xeon(R) Gold 5318Y CPU @ 2.10GHz
Stepping: 6
CPU MHz: 2599.884
BogoMIPS: 4200.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 36864K
NUMA node0 CPU(s): 0-11,48-59
NUMA node1 CPU(s): 12-23,60-71
NUMA node2 CPU(s): 24-35,72-83
NUMA node3 CPU(s): 36-47,84-95
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmprf pni pclmulqdq dtes64 ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm
pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c
rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single ssbd mba ibrs
ibpb stibp ibrs enhanced tpr_shadow vni1 flexpriority ept vpid ept_ad fsgsbase
tsc_adjust bmi1 hle avx2 smep bmi2 erness invpcid cmq rdt_a avx512f avx512dq rdseed
adx smash avx512ifma clflushopt clwb intel_pt avx512cd sha ni avx512bw avx512vl
xsaveopt xsaves xsavec xgetbv1 xsave xsaves cmq llc cmq_occupy llc cmq mbm total cmq mbm local
split_lock_detect wbnoinvd dtherm ida arat pln pts hwp epp avx512vbm1 umip pku ospke
avx512_vbmi2 gfni vaes vpcmullqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq
la57 rdpid fsrmd mclear pconfi flush_lld arch_capabilities

/proc/cpuinfo cache data
  cache size : 36864 KB

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
  available: 4 nodes (0-3)
    node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 48 49 50 51 52 53 54 55 56 57 58 59
    node 0 size: 128116 MB
    node 0 free: 121119 MB
    node 1 cpus: 12 13 14 15 16 17 18 19 20 21 22 23 26 60 61 62 63 64 65 66 67 68 69 70 71
    node 1 size: 129018 MB
    node 1 free: 123772 MB
    node 2 cpus: 24 25 26 27 28 29 30 31 32 33 34 35 72 73 74 75 76 77 78 79 80 81 82 83

(Continued on next page)
xFusion

xFusion 1288H V6 (Intel Xeon Gold 5318Y)

SPECrate®2017_fp_base = 340
SPECrate®2017_fp_peak = Not Run

CPU2017 License: 6488
Test Sponsor: xFusion
Tested by: xFusion

Test Date: Apr-2022
Hardware Availability: Apr-2021
Software Availability: May-2021

Platform Notes (Continued)

node 2 size: 129018 MB
node 2 free: 123642 MB
node 3 cpus: 36 37 38 39 40 41 42 43 44 45 46 47 84 85 86 87 88 89 90 91 92 93 94 95
node 3 size: 129016 MB
node 3 free: 123606 MB
node distances:
node 0 1 2 3
  0: 10 11 20 20
  1: 11 10 20 20
  2: 20 20 10 11
  3: 20 20 11 10

From /proc/meminfo
MemTotal: 527534160 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
/sbin/tuned-adm active
  Current active profile: throughput-performance

From /etc/*release* /etc/*version*
o-release:
  NAME="Red Hat Enterprise Linux"
  VERSION="8.4 (Ootpa)"
  ID="rhel"
  ID_LIKE="fedora"
  VERSION_ID="8.4"
  PLATFORM_ID="platform:el8"
  PRETTY_NAME="Red Hat Enterprise Linux 8.4 (Ootpa)"
  ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.4 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.4 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.4:ga

uname -a:
Linux localhost.localdomain 4.18.0-305.el8.x86_64 #1 SMP Thu Apr 29 08:54:30 EDT 2021
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp

(Continued on next page)
**SPEC CPU®2017 Floating Point Rate Result**

**xFusion**

**xFusion 1288H V6 (Intel Xeon Gold 5318Y)**

<table>
<thead>
<tr>
<th>SPEC®2017_fp_base</th>
<th>340</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPEC®2017_fp_peak</td>
<td>Not Run</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 6488  
**Test Sponsor:** xFusion  
**Tested by:** xFusion  
**Test Date:** Apr-2022  
**Hardware Availability:** Apr-2021  
**Software Availability:** May-2021

---

### Platform Notes (Continued)

- **CVE-2017-5753 (Spectre variant 1):** Mitigation: usercopy/swapgs barriers and __user pointer sanitization
- **CVE-2017-5715 (Spectre variant 2):** Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
- **CVE-2020-0543 (Special Register Buffer Data Sampling):** Not affected
- **CVE-2019-11135 (TSX Asynchronous Abort):** Not affected

---

**run-level:** 3 Apr 12 11:37

**SPEC is set to:** /spec2017

**Filesystem**

<table>
<thead>
<tr>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>xfs</td>
<td>420G</td>
<td>38G</td>
<td>383G</td>
<td>9%</td>
<td>/</td>
</tr>
</tbody>
</table>

From /sys/devices/virtual/dmi/id

- **Vendor:** XFUSION
- **Product:** 1288H V6
- **Product Family:** Whitley
- **Serial:** 123456

---

**Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.**

**Memory:**

- 16x Samsung M393A4K40DB3-CWE 32 GB 2 rank 3200, configured at 2933

**BIOS:**

- **BIOS Vendor:** INSYDE Corp.
- **BIOS Version:** 0.95
- **BIOS Date:** 12/22/2021
- **BIOS Revision:** 0.95

---

**Compiler Version Notes**

```
C | 519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)
```

**Intel(R) oneAPI DPC+/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113**

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SPEC CPU®2017 Floating Point Rate Result

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xFusion 1288H V6 (Intel Xeon Gold 5318Y)

SPECrater®2017_fp_base = 340
SPECrater®2017_fp_peak = Not Run

CPU2017 License: 6488
Test Sponsor: xFusion
Tested by: xFusion

Compiler Version Notes (Continued)

<table>
<thead>
<tr>
<th>C++</th>
<th>508.namd_r(base) 510.parest_r(base)</th>
</tr>
</thead>
</table>
| Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
| Copyright (C) 1985-2020 Intel Corporation. All rights reserved. |

<table>
<thead>
<tr>
<th>C++, C</th>
<th>511.povray_r(base) 526.blender_r(base)</th>
</tr>
</thead>
</table>
| Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
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<table>
<thead>
<tr>
<th>C++, C, Fortran</th>
<th>507.cactuBSSN_r(base)</th>
</tr>
</thead>
</table>
| Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113
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<table>
<thead>
<tr>
<th>Fortran</th>
<th>503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)</th>
</tr>
</thead>
</table>
| Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
| Copyright (C) 1985-2020 Intel Corporation. All rights reserved. |

<table>
<thead>
<tr>
<th>Fortran, C</th>
<th>521.wrf_r(base) 527.cam4_r(base)</th>
</tr>
</thead>
</table>
| Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000
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Test Date: Apr-2022
Hardware Availability: Apr-2021
Software Availability: May-2021

Compiler Version Notes (Continued)

Version 2021.1 Build 20201113
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-----------------------------------------------

Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icx

Benchmarks using both C and C++:
icpx icx

Benchmarks using Fortran, C, and C++:
icpx icx ifort

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64
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**Base Optimization Flags**

C benchmarks:
- `-w`  
- `-std=c11`  
- `-m64`  
- `-Wl,-z,muldefs`  
- `-xCORE-AVX512`  
- `-Ofast`  
- `-ffast-math`  
- `-flto`  
- `-mfpmath=sse`  
- `-funroll-loops`  
- `-qopt-mem-layout-trans=4`  
- `-mbranches-within-32B-boundaries`  
- `-ljemalloc`  
- `-L/usr/local/jemalloc64-5.0.1/lib`

C++ benchmarks:
- `-w`  
- `-m64`  
- `-Wl,-z,muldefs`  
- `-xCORE-AVX512`  
- `-Ofast`  
- `-ffast-math`  
- `-flto`  
- `-mfpmath=sse`  
- `-funroll-loops`  
- `-qopt-mem-layout-trans=4`  
- `-mbranches-within-32B-boundaries`  
- `-ljemalloc`  
- `-L/usr/local/jemalloc64-5.0.1/lib`

Fortran benchmarks:
- `-w`  
- `-m64`  
- `-Wl,-z,muldefs`  
- `-xCORE-AVX512`  
- `-O3`  
- `-ipo`  
- `-no-prec-div`  
- `-qopt-prefetch`  
- `-ffinite-math-only`  
- `-qopt-multiple-gather-scatter-by-shuffles`  
- `-qopt-mem-layout-trans=4`  
- `-nostandard-realloc-lhs`  
- `-align array32byte`  
- `-auto`  
- `-L/usr/local/jemalloc64-5.0.1/lib`

Benchmarks using both Fortran and C:
- `-w`  
- `-m64`  
- `-Wl,-z,muldefs`  
- `-xCORE-AVX512`  
- `-Ofast`  
- `-ffast-math`  
- `-flto`  
- `-mfpmath=sse`  
- `-funroll-loops`  
- `-qopt-mem-layout-trans=4`  
- `-O3`  
- `-ipo`  
- `-no-prec-div`  
- `-qopt-prefetch`  
- `-ffinite-math-only`  
- `-qopt-multiple-gather-scatter-by-shuffles`  
- `-mbranches-within-32B-boundaries`  
- `-L/usr/local/jemalloc64-5.0.1/lib`

Benchmarks using both C and C++:
- `-w`  
- `-m64`  
- `-std=c11`  
- `-Wl,-z,muldefs`  
- `-xCORE-AVX512`  
- `-Ofast`  
- `-ffast-math`  
- `-flto`  
- `-mfpmath=sse`  
- `-funroll-loops`  
- `-qopt-mem-layout-trans=4`  
- `-L/usr/local/jemalloc64-5.0.1/lib`

Benchmarks using Fortran, C, and C++:
- `-w`  
- `-m64`  
- `-std=c11`  
- `-Wl,-z,muldefs`  
- `-xCORE-AVX512`  
- `-Ofast`  
- `-ffast-math`  
- `-flto`  
- `-mfpmath=sse`  
- `-funroll-loops`  
- `-qopt-mem-layout-trans=4`  
- `-O3`  
- `-ipo`  
- `-no-prec-div`  
- `-qopt-prefetch`  
- `-ffinite-math-only`  
- `-qopt-multiple-gather-scatter-by-shuffles`  
- `-mbranches-within-32B-boundaries`  
- `-nostandard-realloc-lhs`  
- `-align array32byte`  
- `-auto`  
- `-ljemalloc`  
- `-L/usr/local/jemalloc64-5.0.1/lib`

The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2017/flags/xFusion-Platform-Settings-ICX-V1.1.html
xFusion

xFusion 1288H V6 (Intel Xeon Gold 5318Y)

SPECrate®2017_fp_base = 340

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 6488
Test Sponsor: xFusion
Tested by: xFusion

Test Date: Apr-2022
Hardware Availability: Apr-2021
Software Availability: May-2021

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml
http://www.spec.org/cpu2017/flags/xFusion-Platform-Settings-ICX-V1.1.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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