# SPEC CPU®2017 Floating Point Rate Result

## xFusion

**xFusion 2288H V6 (Intel Xeon Gold 6314U)**

<table>
<thead>
<tr>
<th>SPECrate®2017_fp_base =</th>
<th>219</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate®2017_fp_peak =</td>
<td>Not Run</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 6488  
**Test Sponsor:** xFusion  
**Tested by:** xFusion  
**Test Date:** May-2022  
**Hardware Availability:** Apr-2021  
**Software Availability:** Sep-2021

### Hardware

- **CPU Name:** Intel Xeon Gold 6314U  
- **Max MHz:** 3400  
- **Nominal:** 2300  
- **Enabled:** 32 cores, 1 chip, 2 threads/core  
- **Orderable:** 1 chip  
- **Cache L1:** 32 KB I + 48 KB D on chip per core  
- **L2:** 1.25 MB I+D on chip per core  
- **L3:** 48 MB I+D on chip per chip  
- **Other:** None  
- **Memory:** 256 GB (8 x 32 GB 2Rx8 PC4-3200AA-R)  
- **Storage:** 1 x 960 GB SATA SSD  
- **Other:** None

### Software

- **OS:** Red Hat Enterprise Linux release 8.4 (Ootpa) 4.18.0-305.el8.x86_64  
- **Compiler:** C/C++: Version 2021.4.0 of Intel oneAPI DPC++/C++ Compiler Build 20210924 for Linux; Fortran: Version 2021.4.0 of Intel Fortran Compiler Classic Build 20210910 for Linux  
- **Parallel:** No  
- **Firmware:** Version 0.95 Released Dec-2021  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** Not Applicable  
- **Other:** jemalloc memory allocator V5.0.1  
- **Power Management:** BIOS and OS set to prefer performance at the cost of additional power usage
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### Results Table

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</tr>
</tbody>
</table>

SPECRate®2017_fp_base = 219

SPECRate®2017_fp_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH = "/spec2017/lib/intel64:/spec2017/je5.0.1-64"
MALLOC_CONF = "retain:true"
```

### General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
xFusion

xFusion 2288H V6 (Intel Xeon Gold 6314U)

| SPECrate®2017_fp_base = | 219 |
| SPECrate®2017_fp_peak = | Not Run |

CPU2017 License: 6488
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Tested by: xFusion
Test Date: May-2022
Hardware Availability: Apr-2021
Software Availability: Sep-2021

General Notes (Continued)

- `sync; echo 3> /proc/sys/vm/drop_caches`
- `runcpu` command invoked through `numactl` i.e.
  - `numactl --interleave=all runcpu <etc>`

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.


Platform Notes

- BIOS configuration:
  - Performance Profile Set to Performance
  - SNC Set to Enabled SNC2 (2-clusters)

- Sysinfo program /spec2017/bin/sysinfo
  - Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16aca6c6d4
  - running on localhost.localdomain Thu May 12 10:06:54 2022

- SUT (System Under Test) info as seen by some common utilities.
  - For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

- From /proc/cpuinfo
  - `model name`: Intel(R) Xeon(R) Gold 6314U CPU @ 2.30GHz
  - 1 "physical id"s (chips)
  - 64 "processors"
  - cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  - cpu cores : 32
  - siblings : 64
  - physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31

- From lscpu from util-linux 2.32.1:
  - Architecture: x86_64
  - CPU op-mode(s): 32-bit, 64-bit
  - Byte Order: Little Endian
  - CPU(s): 64
  - On–line CPU(s) list: 0–63
  - Thread(s) per core: 2
  - Core(s) per socket: 32

(Continued on next page)
xFusion

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SPECraten®2017_fp_base = 219
SPECraten®2017_fp_peak = Not Run

Test Date: May-2022
Hardware Availability: Apr-2021
Software Availability: Sep-2021

Platform Notes (Continued)

Socket(s): 1
NUMA node(s): 2
Vendor ID: GenuineIntel
BIOS Vendor ID: Intel(R) Corporation
CPU family: 6
Model: 106
Model name: Intel(R) Xeon(R) Gold 6314U CPU @ 2.30GHz
BIOS Model name: Intel(R) Xeon(R) Gold 6314U CPU @ 2.30GHz
Stepping: 6
CPU MHz: 2900.310
BogoMIPS: 4600.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 49152K
NUMA node0 CPU(s): 0-15,32-47
NUMA node1 CPU(s): 16-31,48-63

Flags:

fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtsscp
lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpref pni pclmulqdq dtes64 ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm
pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c
rdseed rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single ssbd mba ibrs
ibpb stibp ibrs_enhanced tpr_shadow vmmi flexpriority ept vpid ept_ad fsgsbase
tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid cqm rdt_a ept vpid v plausible
tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed
adx smap avx512ifma clflushopt clwb intel_pt avx512cd shani avx512bw avx512vl
xsaveopt xsave xgetbv1 xsaveas cqm_l1c cqm_occup_llc cqm_mbll_total cqm_mbll_local
split_lock_detect wboinvd dtherm ida arat pln pts hwp_epp avx512vbm1 umip pku ospke
avx512_vbm12 gfni vaes vpcmnlqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq
la57 rdpid fsrm md_clear pconfig flush_lld arch_capabilities

/proc/cpuinfo cache data

cache size: 49152 KB

From numactl --hardware

WARNING: a numactl 'node' might or might not correspond to a physical chip.

available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 32 33 34 35 36 37 38 39 40 41 42 43
44 45 46 47
node 0 size: 128150 MB
node 0 free: 119152 MB
node 1 cpus: 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 48 49 50 51 52 53 54 55 56
57 58 59 60 61 62 63
node 1 size: 128977 MB
node 1 free: 121182 MB
node distances:

(Continued on next page)
Platform Notes (Continued)

node  0  1
  0:  10  11
  1:  11  10

From /proc/meminfo
   MemTotal:       263299256 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/sbin/tuned-adm active
   Current active profile: throughput-performance

From /etc/*release* /etc/*version*
   os-release:
      NAME="Red Hat Enterprise Linux"
      VERSION="8.4 (Ootpa)"
      ID="rhel"
      ID_LIKE="fedora"
      VERSION_ID="8.4"
      PLATFORM_ID="platform:el8"
      PRETTY_NAME="Red Hat Enterprise Linux 8.4 (Ootpa)"
      ANSI_COLOR="0;31"
   redhat-release: Red Hat Enterprise Linux release 8.4 (Ootpa)
   system-release: Red Hat Enterprise Linux release 8.4 (Ootpa)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:8.4:ga

uname -a:
   Linux localhost.localdomain 4.18.0-305.el8.x86_64 #1 SMP Thu Apr 29 08:54:30 EDT 2021
   x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2018-3639 (Speculative Store Bypass): Mitigation: usercopy/swapgs barriers and __user pointer sanitization
CVE-2017-5753 (Spectre variant 1): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
CVE-2017-5715 (Spectre variant 2): Not affected
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort): Not affected
spec

SPEC CPU®2017 Floating Point Rate Result

xFusion

xFusion 2288H V6 (Intel Xeon Gold 6314U)

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SPECrage®2017_fp_peak = Not Run

CPU2017 License: 6488
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Test Date: May-2022
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Platform Notes (Continued)

rn-level 3 May 12 06:19

SPEC is set to: /spec2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda3 xfs 420G 125G 296G 30% /

From /sys/devices/virtual/dmi/id
Vendor: XFUSION
Product: 2288H V6
Product Family: Whiteley
Serial: 1234567

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
8x Samsung M393A4G43AB3-CWE 32 GB 2 rank 3200

BIOS:
BIOS Vendor: INSYDE Corp.
BIOS Version: 0.95
BIOS Date: 12/22/2021
BIOS Revision: 0.95

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
 C               | 519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)
==============================================================================
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.4.0 Build 20210924
Copyright (C) 1985-2021 Intel Corporation. All rights reserved.
==============================================================================

==============================================================================
 C++            | 508.namd_r(base) 510.parest_r(base)
==============================================================================
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.4.0 Build 20210924
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==============================================================================

(Continued on next page)
**Compiler Version Notes (Continued)**

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### Base Compiler Invocation

- **C benchmarks:** icx
- **C++ benchmarks:** icpx
- **Fortran benchmarks:** ifort
- **Benchmarks using both Fortran and C:** ifort icx
- **Benchmarks using both C and C++:** icpx icx
- **Benchmarks using Fortran, C, and C++:** icpx icx ifort

### Base Portability Flags

- 503.bwaves_r: -DSPEC_LP64
- 507.cactuBSSN_r: -DSPEC_LP64
- 508.namd_r: -DSPEC_LP64
- 510.parest_r: -DSPEC_LP64
- 511.povray_r: -DSPEC_LP64
- 519.lbm_r: -DSPEC_LP64
- 521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
- 526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
- 527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
- 538.imagick_r: -DSPEC_LP64
- 544.nab_r: -DSPEC_LP64
- 549.fotonik3d_r: -DSPEC_LP64
- 554.roms_r: -DSPEC_LP64

### Base Optimization Flags

- **C benchmarks:**
  - -w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
  - -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
  - -mbranches-within-32B-boundaries -ljemalloc
  - -L/usr/local/jemalloc64-5.0.1/lib

*(Continued on next page)*
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Base Optimization Flags (Continued)

C++ benchmarks:
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ipo -no-prec-div
-qopt-prefetch -ffinite-math-only
-qopt-multiple-gather-scatter-by-shuffles -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-mbranches-within-32B-boundaries -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both Fortran and C:
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -O3 -ipo
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-multiple-gather-scatter-by-shuffles
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both C and C++:
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using Fortran, C, and C++:
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-multiple-gather-scatter-by-shuffles
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/xFusion-Platform-Settings-ICX-V1.1.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml
http://www.spec.org/cpu2017/flags/xFusion-Platform-Settings-ICX-V1.1.xml
## SPEC CPU®2017 Floating Point Rate Result

**xFusion**

**xFusion 2288H V6 (Intel Xeon Gold 6314U)**

<table>
<thead>
<tr>
<th>SPECrate®2017_fp_base = 219</th>
</tr>
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<tbody>
<tr>
<td>SPECrate®2017_fp_peak = Not Run</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 6488  
**Test Sponsor:** xFusion  
**Tested by:** xFusion  

**Test Date:** May-2022  
**Hardware Availability:** Apr-2021  
**Software Availability:** Sep-2021  

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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