## SPEC CPU®2017 Floating Point Rate Result

**xFusion XH321 V6 (Intel Xeon Platinum 8352Y)**

![SPEC Logo][1]

**SPECrate®2017_fp_base = 432**

**SPECrate®2017_fp_peak = Not Run**

<table>
<thead>
<tr>
<th><strong>Test Date:</strong></th>
<th>Apr-2022</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Hardware Availability:</strong></td>
<td>Apr-2021</td>
</tr>
<tr>
<td><strong>Software Availability:</strong></td>
<td>Sep-2021</td>
</tr>
</tbody>
</table>

### CPU2017 License: 6488

**Test Sponsor:** xFusion

**Test Date:** Apr-2022

**Hardware Availability:** Apr-2021

**Software Availability:** Sep-2021

### Hardware

**CPU Name:** Intel Xeon Platinum 8352Y

- **Max MHz:** 3400
- **Nominal:** 2200
- **Enabled:** 64 cores, 2 chips, 2 threads/core
- **Orderable:** 1,2 chips
- **Cache L1:** 32 KB I + 48 KB D on chip per core
- **L2:** 1.25 MB I+D on chip per core
- **L3:** 48 MB I+D on chip per chip
- **Other:** None
- **Memory:** 512 GB (16 x 32 GB 2Rx8 PC4-3200AA-R)
- **Storage:** 1 x 960 GB SATA SSD
- **Other:** None

### Software

**OS:** Red Hat Enterprise Linux release 8.4 (Ootpa)

- **4.18.0-305.el8.x86_64**

**Compiler:**

- **C/C++:** Version 2021.4.0 of Intel oneAPI DPC++/C++ Compiler Build 20210924 for Linux;
- **Fortran:** Version 2021.4.0 of Intel Fortran Compiler Classic Build 20210910 for Linux

**Parallel:** No

**Firmware:** Version 1.05 Released May-2022

**File System:** xfs

**System State:** Run level 3 (multi-user)

**Base Pointers:** 64-bit

**Peak Pointers:** Not Applicable

**Other:** jemalloc memory allocator V5.0.1

**Power Management:** BIOS and OS set to prefer performance at the cost of additional power usage

### Copies

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>128</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>128</td>
</tr>
<tr>
<td>508.namd_r</td>
<td>128</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>128</td>
</tr>
<tr>
<td>511.povray_r</td>
<td>128</td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>128</td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>128</td>
</tr>
<tr>
<td>526.blender_r</td>
<td>128</td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>128</td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>128</td>
</tr>
<tr>
<td>544.nab_r</td>
<td>128</td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>128</td>
</tr>
<tr>
<td>554.roms_r</td>
<td>128</td>
</tr>
</tbody>
</table>

![Graph](https://www.spec.org/)
## SPEC CPU®2017 Floating Point Rate Result

xFusion

xFusion XH321 V6 (Intel Xeon Platinum 8352Y)

---

**SPECrater®2017_fp_base =** 432

**SPECrater®2017_fp_peak =** Not Run

---

### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Base</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Peak</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>128</td>
<td>1719</td>
<td>747</td>
<td>1719</td>
<td>747</td>
<td>1718</td>
<td>747</td>
<td></td>
<td></td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>128</td>
<td>276</td>
<td>587</td>
<td>278</td>
<td>583</td>
<td>278</td>
<td>584</td>
<td></td>
<td></td>
</tr>
<tr>
<td>508.namd_r</td>
<td>128</td>
<td>347</td>
<td>351</td>
<td>349</td>
<td>348</td>
<td>349</td>
<td>348</td>
<td></td>
<td></td>
</tr>
<tr>
<td>510.parest_r</td>
<td>128</td>
<td>1567</td>
<td>214</td>
<td>1572</td>
<td>213</td>
<td>1580</td>
<td>212</td>
<td></td>
<td></td>
</tr>
<tr>
<td>511.povray_r</td>
<td>128</td>
<td>582</td>
<td>514</td>
<td>583</td>
<td>513</td>
<td>583</td>
<td>512</td>
<td></td>
<td></td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>128</td>
<td>490</td>
<td>275</td>
<td>491</td>
<td>275</td>
<td>492</td>
<td>274</td>
<td></td>
<td></td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>128</td>
<td>822</td>
<td>349</td>
<td>824</td>
<td>348</td>
<td>824</td>
<td>348</td>
<td></td>
<td></td>
</tr>
<tr>
<td>526.blender_r</td>
<td>128</td>
<td>369</td>
<td>529</td>
<td>370</td>
<td>527</td>
<td>369</td>
<td>528</td>
<td></td>
<td></td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>128</td>
<td>497</td>
<td>450</td>
<td>501</td>
<td>447</td>
<td>501</td>
<td>447</td>
<td></td>
<td></td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>128</td>
<td>222</td>
<td>1430</td>
<td>222</td>
<td>1430</td>
<td>220</td>
<td>1450</td>
<td></td>
<td></td>
</tr>
<tr>
<td>544.nab_r</td>
<td>128</td>
<td>229</td>
<td>940</td>
<td>231</td>
<td>934</td>
<td>231</td>
<td>934</td>
<td></td>
<td></td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>128</td>
<td>2178</td>
<td>229</td>
<td>2176</td>
<td>229</td>
<td>2177</td>
<td>229</td>
<td></td>
<td></td>
</tr>
<tr>
<td>554.roms_r</td>
<td>128</td>
<td>1281</td>
<td>159</td>
<td>1283</td>
<td>159</td>
<td>1284</td>
<td>158</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

---

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Environment Variables Notes

Environment variables set by runcpu before the start of the run:

LD_LIBRARY_PATH = "/spec2017/lib/intel64:/spec2017/je5.0.1-64"
MALLOC_CONF = "retain:true"

### General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:

---

(Continued on next page)
General Notes (Continued)

sync; echo 3 > /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
Performance Profile Set to Performance
SNC Set to Enabled SNC2 (2-clusters)

Sysinfo program /spec2017/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16aca6f4d running on localhost.localdomain Fri Apr 1 08:25:36 2022

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Platinum 8352Y CPU @ 2.20GHz
  2 "physical id"s (chips)
  128 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 32
siblings : 64
  physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
  25 26 27 28 29 30 31
  physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
  25 26 27 28 29 30 31

From lscpu from util-linux 2.32.1:
Architecture:    x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order:      Little Endian
CPU(s):          128
On-line CPU(s) list: 0-127

(Continued on next page)
Platform Notes (Continued)

Thread(s) per core: 2
Core(s) per socket: 32
Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel
BIOS Vendor ID: Intel(R) Corporation
CPU family: 6
Model: 106
Model name: Intel(R) Xeon(R) Platinum 8352Y CPU @ 2.20GHz
BIOS Model name: Intel(R) Xeon(R) Platinum 8352Y CPU @ 2.20GHz
Stepping: 6
CPU MHz: 2800.154
BogoMIPS: 4400.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 49152K
NUMA node0 CPU(s): 0-15,64-79
NUMA node1 CPU(s): 16-31,80-95
NUMA node2 CPU(s): 32-47,96-111
NUMA node3 CPU(s): 48-63,112-127
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc arch_perfmon pebs bts rep_good ncpu xtology nonstop_tsc cpuid
aperfmpref pni pclmulqdq dtets64 ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm
pcid dca ssse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c
rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single ssbd mba ibrs
ibpb stibp tpr_shadow vmmi flexpriority ept vpid ept_ad fsbgbase
tsc_adjust bml1 hle avx2 smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed
adx smap avx512ifma clflushopt clwb intel_pt avx512d sha ni avx512bw avx512vl
xsaveopt xsave cxiw xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbb_total cqm_mbb_local
split_lock_detect wbinvd dtherm ida arat pln pts hwp epp avx512vbm unip pkd ospe
avx512_vbmi2 gfnai vaes vpcmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq
la57 rdpid fsrcm md_clear pconfig flush_l1d arch_capabilities

/proc/cpuinfo cache data
cache size : 49152 KB

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 64 65 66 67 68 69 70 71 72 73 74 75
76 77 78 79
node 0 size: 128183 MB
node 0 free: 117645 MB
node 1 cpus: 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 80 81 82 83 84 85 86 87 88

(Continued on next page)
SPEC CPU®2017 Floating Point Rate Result

xFusion

xFusion XH321 V6 (Intel Xeon Platinum 8352Y)

<table>
<thead>
<tr>
<th>SPECrate®2017_fp_base</th>
<th>432</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate®2017_fp_peak</td>
<td>Not Run</td>
</tr>
</tbody>
</table>

CPU2017 License: 6488
Test Sponsor: xFusion
Test Date: Apr-2022
Hardware Availability: Apr-2021
Tested by: xFusion
Software Availability: Sep-2021

Platform Notes (Continued)

<table>
<thead>
<tr>
<th>node</th>
<th>size</th>
<th>free</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>129017 MB</td>
<td>121567 MB</td>
</tr>
<tr>
<td>1</td>
<td>129017 MB</td>
<td>122044 MB</td>
</tr>
<tr>
<td>2</td>
<td>128977 MB</td>
<td>121995 MB</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>node</th>
<th>cpus</th>
<th>size</th>
<th>free</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>1</td>
<td>2</td>
<td>3</td>
</tr>
<tr>
<td>1</td>
<td>10</td>
<td>11</td>
<td>20</td>
</tr>
<tr>
<td>2</td>
<td>11</td>
<td>10</td>
<td>20</td>
</tr>
<tr>
<td>3</td>
<td>20</td>
<td>20</td>
<td>11</td>
</tr>
</tbody>
</table>

node distances:
0: 10 11 20 20
1: 11 10 20 20
2: 20 20 10 11
3: 20 20 11 10

From /proc/meminfo
MemTotal: 527559832 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
/sbin/tuned-adm active
Current active profile: throughput-performance

From /etc/*release* /etc/*version*

os-release:
NAME="Red Hat Enterprise Linux"
VERSION="8.4 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.4"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.4 (Ootpa)"
ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.4 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.4 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.4:ga

uname -a:
Linux localhost.localdomain 4.18.0-305.el8.x86_64 #1 SMP Thu Apr 29 08:54:30 EDT 2021
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2018-12207 (iTLB Multihit): Not affected

(Continued on next page)
xFusion

xFusion XH321 V6 (Intel Xeon Platinum 8352Y)

 SPECrate®2017_fp_base = 432
 SPECrate®2017_fp_peak = Not Run

CPU2017 License: 6488
Test Sponsor: xFusion
Tested by: xFusion

Test Date: Apr-2022
Hardware Availability: Apr-2021
Software Availability: Sep-2021

Platform Notes (Continued)

CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2018-3639 (Speculative Store Bypass): Mitigation: usercopy/swaps barriers and __user pointer sanitization
CVE-2017-5753 (Spectre variant 1): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
CVE-2017-5715 (Spectre variant 2): Not affected
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Mar 31 07:42

SPEC is set to: /spec2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda3 xfs 420G 60G 360G 15% /

From /sys/devices/virtual/dmi/id
Vendor: XFUSION
Product: XH321 V6
Product Family: Whitley
Serial: serial

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
16x Samsung M393A4G43AB3-CWE 32 GB 2 rank 3200

BIOS:
BIOS Vendor: INSYDE Corp.
BIOS Version: 1.05
BIOS Date: 05/16/2022
BIOS Revision: 1.5

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C | 519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)
-----------------------------------------------------------------------------

(Continued on next page)
SPEC CPU®2017 Floating Point Rate Result

xFusion

xFusion XH321 V6 (Intel Xeon Platinum 8352Y)

SPECrater®2017_fp_base = 432
SPECrater®2017_fp_peak = Not Run

CPU2017 License: 6488
Test Sponsor: xFusion

Test Date: Apr-2022
Hardware Availability: Apr-2021

Tested by: xFusion
Software Availability: Sep-2021

Compiler Version Notes (Continued)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.4.0 Build 20210924
Copyright (C) 1985-2021 Intel Corporation. All rights reserved.

==============================================================================
| C++ | 508.namd_r(base) 510.parest_r(base) |
|-----------------------------------------------|
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.4.0 Build 20210924
Copyright (C) 1985-2021 Intel Corporation. All rights reserved.

==============================================================================
| C++, C | 511.povray_r(base) 526.blender_r(base) |
|-----------------------------------------------|
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.4.0 Build 20210924
Copyright (C) 1985-2021 Intel Corporation. All rights reserved.

==============================================================================
| C++, C, Fortran | 507.cactuBSSN_r(base) |
|-----------------------------------------------|
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.4.0 Build 20210924
Copyright (C) 1985-2021 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.4.0 Build 20210924
Copyright (C) 1985-2021 Intel Corporation. All rights reserved.
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.4.0 Build 20210910_000000
Copyright (C) 1985-2021 Intel Corporation. All rights reserved.

==============================================================================
| Fortran | 503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base) |
|-----------------------------------------------|
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.4.0 Build 20210910_000000
Copyright (C) 1985-2021 Intel Corporation. All rights reserved.

(Continued on next page)
xFusion

xFusion XH321 V6 (Intel Xeon Platinum 8352Y)

SPECrated®2017_fp_base = 432

SPECrated®2017_fp_peak = Not Run

CPU2017 License: 6488
Test Sponsor: xFusion
Tested by: xFusion

Test Date: Apr-2022
Hardware Availability: Apr-2021
Software Availability: Sep-2021

Compiler Version Notes (Continued)

Fortran, C | 521.wrf_r(base) 527.cam4_r(base)

--------------------------------------------------------------------------------

Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on
Intel(R) 64, Version 2021.4.0 Build 20210910_000000
Copyright (C) 1985-2021 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.4.0 Build 20210924
Copyright (C) 1985-2021 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------

Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
ifort icx

Benchmarks using both C and C++:
icpx icx

Benchmarks using Fortran, C, and C++:
icpx icx ifort

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.ibm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64

(Continued on next page)
**Base Portability Flags (Continued)**

549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

**Base Optimization Flags**

**C benchmarks:**
- `-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math`
- `-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4`
- `-mbranches-within-32B-boundaries -ljemalloc`
- `-L/usr/local/jemalloc64-5.0.1/lib`

**C++ benchmarks:**
- `-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto`
- `-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4`
- `-mbranches-within-32B-boundaries -ljemalloc`
- `-L/usr/local/jemalloc64-5.0.1/lib`

**Fortran benchmarks:**
- `-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ipo -no-prec-div`
- `-qopt-prefetch -ffinite-math-only`
- `-qopt-multiple-gather-scatter-by-shuffles -qopt-mem-layout-trans=4`
- `-nostandard-realloc-lhs -align array32byte -auto`
- `-mbranches-within-32B-boundaries -ljemalloc`
- `-L/usr/local/jemalloc64-5.0.1/lib`

**Benchmarks using both Fortran and C:**
- `-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math`
- `-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -O3 -ipo`
- `-no-prec-div -qopt-prefetch -ffinite-math-only`
- `-qopt-multiple-gather-scatter-by-shuffles`
- `-mbranches-within-32B-boundaries -nostandard-realloc-lhs`
- `-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib`

**Benchmarks using both C and C++:**
- `-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math`
- `-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4`
- `-mbranches-within-32B-boundaries -ljemalloc`
- `-L/usr/local/jemalloc64-5.0.1/lib`

**Benchmarks using Fortran, C, and C++:**
- `-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math`
- `-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -O3`
- `-no-prec-div -qopt-prefetch -ffinite-math-only`
- `-qopt-multiple-gather-scatter-by-shuffles`

(Continued on next page)
xFusion

xFusion XH321 V6 (Intel Xeon Platinum 8352Y)

<table>
<thead>
<tr>
<th>SPECrate®2017_fp_base = 432</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate®2017_fp_peak = Not Run</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>CPU2017 License: 6488</th>
<th>Test Date: Apr-2022</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor: xFusion</td>
<td>Hardware Availability: Apr-2021</td>
</tr>
<tr>
<td>Tested by: xFusion</td>
<td>Software Availability: Sep-2021</td>
</tr>
</tbody>
</table>

**Base Optimization Flags (Continued)**

Benchmarks using Fortran, C, and C++ (continued):

- `mbranches-within-32B-boundaries`
- `nostandard-realloc-lhs`
- `align array32byte`
- `auto`
- `ljemalloc`
- `L/usr/local/jemalloc64-5.0.1/lib`

The flags files that were used to format this result can be browsed at:


http://www.spec.org/cpu2017/flags/xFusion-Platform-Settings-ICX-V1.1.html

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml

http://www.spec.org/cpu2017/flags/xFusion-Platform-Settings-ICX-V1.1.xml

---

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.8 on 2022-04-01 08:25:35-0400.


Originally published on 2022-07-05.