**SPEC CPU®2017 Integer Rate Result**

Hewlett Packard Enterprise  
(Test Sponsor: HPE)  
MicroServer Gen10 Plus v2  
(2.90 GHz, Intel Xeon E-2336)

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

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<th>Test Date:</th>
<th>Aug-2022</th>
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<td><strong>Hardware Availability:</strong></td>
<td>Sep-2022</td>
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<tbody>
<tr>
<td>SPECrate®2017_int_peak</td>
<td>58.3</td>
</tr>
</tbody>
</table>

### Hardware

- **CPU Name:** Intel Xeon E-2336  
- **Max MHz:** 4800  
- **Nominal:** 2900  
- **Enabled:** 6 cores, 1 chip, 2 threads/core  
- **Orderable:** 1 chip  
- **Cache L1:** 32 KB I + 48 KB D on chip per core  
- **L2:** 512 KB I+D on chip per core  
- **L3:** 12 MB I+D on chip per chip  
- **Other:** None  
- **Memory:** 64 GB (2 x 32 GB 2Rx8 PC4-3200AA-E)  
- **Storage:** 2 x 400 GB SATA SSD  
- **Other:** None

### Software

- **OS:** Red Hat Enterprise Linux release 8.5 (Ootpa)  
- **Compiler:**  
  - C/C++: Version 2021.4.0 of Intel oneAPI  
  - DPC++/C++  
  - Compiler Build 20210924 for Linux:  
  - Fortran: Version 2021.4.0 of Intel Fortran Compiler  
  - Classic Build 20210910 for Linux:  
  - Compiler Classic Build 20210910 for Linux:  
- **Parallel:** No  
- **Firmware:** HPE BIOS Version U64 v1.60 (06/30/2022) released Jun-2022  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 32/64-bit  
- **Other:** jemalloc memory allocator V5.0.1  
- **Power Management:** BIOS and OS set to prefer performance at the cost of additional power usage
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SPECrate®2017 int_base = 56.1
SPECrate®2017 int_peak = 58.3

Results Table

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<td>541.leela_r</td>
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<td>548.exchange2_r</td>
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<td>557.xz_r</td>
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<td>426</td>
<td>30.4</td>
<td>426</td>
<td>30.4</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3 > /proc/sys/vm/drop_caches

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "\home/cpu2017_newbinaries/lib/intel64:\home/cpu2017_newbinaries/lib/ia32:\home/cpu2017_newbinaries/je5.0.1-32"
MALLOCC_CONF = "retain:true"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Red Hat Enterprise Linux 8.1
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)

(Continued on next page)
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**Test Sponsor:** HPE  
**Tested by:** HPE

---

### General Notes (Continued)

is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.  

---

### Platform Notes

**BIOS Configuration:**
- Workload Profile set to General Throughput Compute
- Thermal Configuration set to Maximum Cooling
- Enhanced Processor Performance set to Enabled
- Minimum Processor Idle Power Package C-State set to No Package State

**Sysinfo program /home/cpu2017_newbinaries/bin/sysinfo**
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d
running on localhost.localdomain Sun Aug 7 07:46:45 2022

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
```
model name : Intel(R) Xeon(R) E-2336 CPU @ 2.90GHz
  1 "physical id"s (chips)
  12 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 6
  siblings : 12
  physical 0: cores 0 1 2 3 4 5
```

From lscpu from util-linux 2.32.1:
```
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 12
On-line CPU(s) list: 0-11
Thread(s) per core: 2
Core(s) per socket: 6
Socket(s): 1
NUMA node(s): 1
Vendor ID: GenuineIntel
```

(Continued on next page)
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Test Sponsor: HPE
Hardware Availability: Sep-2022
Software Availability: Nov-2021

HPE

SPECrate®2017_int_base = 56.1
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Platform Notes (Continued)

BIOS Vendor ID: Intel(R) Corporation
CPU family: 6
Model: 167
Model name: Intel(R) Xeon(R) E-2336 CPU @ 2.90GHz
BIOS Model name: Intel(R) Xeon(R) E-2336 CPU @ 2.90GHz
Stepping: 1
CPU MHz: 2900.000
BogoMIPS: 5808.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 512K
L3 cache: 12288K
NUMA node0 CPU(s): 0-11
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtpsc
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpref tsc_known_freq pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3
sdkg fma cx16 xptr pdc船上 sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer
aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb invpcid_single
ssbd ibrs stibp ibrs_reduced tpr_shadow vtvnmi flexpriority ept vpid ept_ad
fsgsbase tsc_adjust sxg bml1 avx2 smep bmi2 erms invpcid mpx avx512f avx512dq rdseed
adx smap avx512ifma clflushopt intel_pt avx512cd sha ni avx512bw avx512vl xsaveopt
xsavec xgetbv1 xsave dtherm sri arat pin pts avx512vbmi umip pku ospke avx512_vbmi2
gfn vaes vpcmMulqdq avx512_vnni avx512_bitalg avx512_vpopcntdq rdpid sgx lc fsrn
md_clear flush_l1d arch_capabilities

From /proc/cpuinfo cache data
  cache size: 12288 KB

From numactl --hardware
  WARNING: a numactl 'node' might or might not correspond to a physical chip.
  available: 1 nodes (0)
  node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11
  node 0 size: 6432 MB
  node 0 free: 63579 MB

From /proc/meminfo
  MemTotal: 65867608 kB
  HugePages_Total: 0
  Hugepagesize: 2048 kB

/sbin/tuned-adm active
  Current active profile: throughput-performance

(Continued on next page)
Platform Notes (Continued)

From /etc/*release* /etc/*version*
  os-release:
    NAME="Red Hat Enterprise Linux"
    VERSION="8.5 (Ootpa)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="8.5"
    PLATFORM_ID="platform:el8"
    PRETTY_NAME="Red Hat Enterprise Linux 8.5 (Ootpa)"
    ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.5 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.5 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8::baseos

uname -a:
  Linux localhost.localdomain 4.18.0-348.el8.x86_64 #1 SMP Mon Oct 4 12:17:22 EDT 2021
  x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit):                          Not affected
CVE-2018-3620 (L1 Terminal Fault):                      Not affected
Microarchitectural Data Sampling:                       Not affected
CVE-2017-5754 (Meltdown):                               Not affected
CVE-2018-3639 (Speculative Store Bypass):               Mitigation: Speculative Store
  Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1):                      Mitigation: usercopy/swapps
  barriers and __user pointer
  sanitization
CVE-2017-5715 (Spectre variant 2):                      Mitigation: Enhanced IBRS, IBPB:
  conditional, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling):  Not affected
CVE-2019-11135 (TSX Asynchronous Abort):                 Not affected

run-level 3 Aug 7 07:42

SPEC is set to: /home/cpu2017_newbinaries
  Filesystem Type Size Used Avail Use% Mounted on
  /dev/mapper/rhel-home xfs 270G 69G 202G 26% /home

From /sys/devices/virtual/dmi/id
  Vendor: HPE
  Product: ProLiant MicroServer Gen10 Plus v2
  Product Family: ProLiant
  Serial: MSG10PV2001
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Test Date: Aug-2022
Hardware Availability: Sep-2022
Software Availability: Nov-2021

Platform Notes (Continued)

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

  2x Micron 18ASF4G72AZ-3G2B1 32 GB 2 rank 3200

BIOS:

  BIOS Vendor:       HPE
  BIOS Version:      U64
  BIOS Date:         06/30/2022
  BIOS Revision:     1.60
  Firmware Revision: 2.70

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C       | 500.perlbench_r(peak)
==============================================================================

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.4.0 Build 20210910_000000
Copyright (C) 1985-2021 Intel Corporation. All rights reserved.

==============================================================================
C       | 502.gcc_r(peak)
==============================================================================

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2021.4.0 Build 20210924
Copyright (C) 1985-2021 Intel Corporation. All rights reserved.

==============================================================================
C       | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak) 557.xz_r(base, peak)
==============================================================================

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.4.0 Build 20210924
Copyright (C) 1985-2021 Intel Corporation. All rights reserved.

==============================================================================
C       | 500.perlbench_r(peak)

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Compiler Version Notes (Continued)

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------------------------------------------------------------------------------
C |  502.gcc_r(peak)
------------------------------------------------------------------------------

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Copyright (C) 1985-2021 Intel Corporation. All rights reserved.

------------------------------------------------------------------------------
C |  500.perlbench_r(base)  502.gcc_r(base)  505.mcf_r(base, peak)
|  525.x264_r(base, peak)  557.xz_r(base, peak)
------------------------------------------------------------------------------

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------------------------------------------------------------------------------
C |  500.perlbench_r(peak)
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C |  502.gcc_r(peak)
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------------------------------------------------------------------------------
C |  500.perlbench_r(base)  502.gcc_r(base)  505.mcf_r(base, peak)
|  525.x264_r(base, peak)  557.xz_r(base, peak)
------------------------------------------------------------------------------

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.4.0 Build 20210924
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**Compiler Version Notes (Continued)**

```
C++     | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak)
| 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2021.4.0 Build 20210924  
Copyright (C) 1985-2021 Intel Corporation. All rights reserved.
```

```
Fortran | 548.exchange2_r(base, peak)
```

```
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on  
Intel(R) 64, Version 2021.4.0 Build 20210910_000000  
Copyright (C) 1985-2021 Intel Corporation. All rights reserved.
```

**Base Compiler Invocation**

C benchmarks:  
icx

C++ benchmarks:  
icpx

Fortran benchmarks:  
ifort

**Base Portability Flags**

```
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64  
502.gcc_r: -DSPEC_LP64  
505.mcf_r: -DSPEC_LP64  
520.omnetpp_r: -DSPEC_LP64  
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX  
525.x264_r: -DSPEC_LP64  
531.deepsjeng_r: -DSPEC_LP64  
541.leela_r: -DSPEC_LP64  
548.exchange2_r: -DSPEC_LP64  
557.xz_r: -DSPEC_LP64
```
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CPU2017 License: 3
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Tested by: HPE

Base Optimization Flags

C benchmarks:
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/usr/local/intel/compiler/2021.4.0/linux/compiler/lib/intel64_lin
-lqkmalloc

C++ benchmarks:
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/usr/local/intel/compiler/2021.4.0/linux/compiler/lib/intel64_lin
-lqkmalloc

Fortran benchmarks:
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ipo -no-prec-div
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte
-auto -mbranches-within-32B-boundaries
-L/usr/local/intel/compiler/2021.4.0/linux/compiler/lib/intel64_lin
-lqkmalloc

Peak Compiler Invocation

C benchmarks (except as noted below):
icx
500.perlbench_r: icc

C++ benchmarks:
icpx

Fortran benchmarks:
ifort

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX

(Continued on next page)
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HPE

Peak Portability Flags (Continued)

525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leea_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:

500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-strict-overflow
-mbranches-within-32B-boundaries
-L/usr/local/intel/compiler/2021.4.0/linux/compiler/lib/intel64_lin
-lqkmalloc

502.gcc_r: -m32
-L/usr/local/intel/compiler/2021.4.0/linux/compiler/lib/ia32_lin
-std=gnu89 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -flto
-ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc32-5.0.1/lib -ljemalloc

505.mcf_r: basepeak = yes

525.x264_r: -w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -flto
-O3 -ffast-math -qopt-mem-layout-trans=4 -fno-alias
-mbranches-within-32B-boundaries
-L/usr/local/intel/compiler/2021.4.0/linux/compiler/lib/intel64_lin
-lqkmalloc

557.xz_r: basepeak = yes

C++ benchmarks:

520.omnetpp_r: basepeak = yes
523.xalancbmk_r: basepeak = yes
531.deepsjeng_r: basepeak = yes

(Continued on next page)
### SPEC CPU®2017 Integer Rate Result

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**SPECrate®2017_int_base = 56.1**  
**SPECrate®2017_int_peak = 58.3**

---

#### Peak Optimization Flags (Continued)

541.leela_r: basepeak = yes  

Fortran benchmarks:  

548.exchange2_r: basepeak = yes

---

The flags files that were used to format this result can be browsed at  
http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.0-ICX-revF.html  

You can also download the XML flags sources by saving the following links:  
http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.0-ICX-revF.xml  
http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.8 on 2022-08-07 07:46:45-0400.  
Originally published on 2022-09-13.