# SPEC CPU® 2017 Integer Rate Result

## Inspur Corporation

**Inspur NF5180M6 (Intel Xeon Gold 5320)**

<table>
<thead>
<tr>
<th>SPECrate® 2017 int_base = 386</th>
<th>SPECrate® 2017 int_peak = 397</th>
</tr>
</thead>
</table>

### Hardware

**CPU Name:** Intel Xeon Gold 5320  
**Max MHz:** 3400  
**Nominal:** 2200  
**Enabled:** 52 cores, 2 chips, 2 threads/core  
**Orderable:** 1.2 chips  
**Cache L1:** 32 KB I + 48 KB D on chip per core  
**L2:** 1.25 MB I+D on chip per core  
**L3:** 39 MB I+D on chip per core  
**Other:** None  
**Memory:** 1 TB (32 x 32 GB 2Rx8 PC4-3200AA-R, running at 2933)  
**Storage:** 1 x 2 TB NVME SSD  
**Other:** None

### Software

**OS:** Red Hat Enterprise Linux release 8.3 (Ootpa)  
4.18.0-240.el8.x86_64  
**Compiler:** C/C++: Version 2022.1 of Intel oneAPI DPC++/C++  
Compiler Build 20220316 for Linux;  
Fortran: Version 2022.1 of Intel Fortran Compiler  
Build 20220316 for Linux;  
**Parallel:** No  
**Firmware:** Version 04.12.02 released Apr-2021  
**File System:** xfs  
**System State:** Run level 3 (multi-user)  
**Base Pointers:** 64-bit  
**Peak Pointers:** 32/64-bit  
**Other:** jemalloc memory allocator V5.0.1  
**Power Management:** BIOS and OS set to prefer performance at the cost of additional power usage.
Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
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<tbody>
<tr>
<td>BASE</td>
<td>500.perlbench_r</td>
<td>104</td>
<td>606</td>
<td>273</td>
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<td>607</td>
<td>273</td>
<td>104</td>
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<td>505.mcf_r</td>
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<td>520.omnetpp_r</td>
<td>104</td>
<td>580</td>
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<td>581</td>
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<tr>
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<td>523.xalancbmk_r</td>
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<td>620</td>
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<td>525.x264_r</td>
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<tr>
<td></td>
<td>531.deepsjeng_r</td>
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<td>541.leela_r</td>
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<tr>
<td></td>
<td>548.exchange2_r</td>
<td>104</td>
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<td>800</td>
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<tr>
<td></td>
<td>557.xz_r</td>
<td>104</td>
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<td>541</td>
<td>208</td>
<td>542</td>
<td>207</td>
<td></td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

SPEC has ruled that the compiler used for this result was performing a compilation that specifically improves the performance of the 523.xalancbmk_r / 623.xalanchmk_s benchmarks using a priori knowledge of the SPEC code and dataset to perform a transformation that has narrow applicability.

In order to encourage optimizations that have wide applicability (see rule 1.4 https://www.spec.org/cpu2017/Docs/runrules.html#rule_1.4), SPEC will no longer publish results using this optimization.

This result is left in the SPEC results database for historical reference.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Scaling_Governor set to Performance

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/CPU2017/lib/intel64:/home/CPU2017/lib/ia32:/home/CPU2017/je5.0.1-32"
MALLOC_CONF = "retain:true"
**SPEC CPU®2017 Integer Rate Result**

**Inspur Corporation**

**Inspur NF5180M6 (Intel Xeon Gold 5320)**

<table>
<thead>
<tr>
<th>SPECrate®2017_int_base</th>
<th>SPECrate®2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>386</td>
<td>397</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 3358  
**Test Sponsor:** Inspur Corporation  
**Test Date:** Aug-2022  
**Hardware Availability:** Apr-2021  
**Tested by:** Inspur Corporation  
**Software Availability:** May-2022

**General Notes**

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM  
Memory using Red Hat Enterprise Linux 8.4  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop_caches  

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)  
is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)  
is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)  
is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation  
built with the RedHat Enterprise 7.5,  
and the system compiler gcc 4.8.5;  
sources available from jemalloc.net or  

**Platform Notes**

BIOS configuration:  
ENERGY_PERF_BIAS_CFG mode set to Performance  
Hardware Prefetch set to Disable  
VT Support set to Disable  
Sub NUMA Cluster (SNC) set to Enable  

Sysinfo program /home/CPU2017/bin/sysinfo  
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16ac564d  
running on localhost.localdomain Sat Aug 20 03:55:22 2022

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) Gold 5320 CPU @ 2.20GHz
  2 "physical id"s (chips)
  104 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 26
  siblings : 52
  physical 0: cores  0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25
  physical 1: cores  0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25
```

From lscpu from util-linux 2.32.1:

```
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 104
On-line CPU(s) list: 0-103
Thread(s) per core: 2
Core(s) per socket: 26
Socket(s): 2
NUMA node(s): 4
```
Platform Notes (Continued)

Vendor ID:          GenuineIntel
CPU family:        6
Model:             106
Model name:        Intel(R) Xeon(R) Gold 5320 CPU @ 2.20GHz
Stepping:          6
CPU MHz:           2800.000
CPU max MHz:       3400.0000
CPU min MHz:       800.0000
BogoMIPS:          4400.00
Virtualization:   VT-x
L1d cache:         48K
L1i cache:         32K
L2 cache:          1280K
L3 cache:          39936K
NUMA node0 CPU(s): 0-12, 52-64
NUMA node1 CPU(s): 13-25, 65-77
NUMA node2 CPU(s): 26-38, 78-90
NUMA node3 CPU(s): 39-51, 91-103
Flags:            fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
                   pat pse36 cpuid cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht
                   tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art_perfmon pebs
                   rep_good nopl xtopology nonstop_tsc cpuid aperf perfctr pni pclmulqdq
                   dtes64 ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm
                   pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
                   xsave avx f16c rdrand lahf_lm abm 3nowprefetch cpuid_fault epb cat_l3
disabled single intel_pnip ssbd mba ibrs ibpb stibp ibrs_enhanced fsxgbase tsc_adjust
                   bmi1 hle avx2 smep bmi2 emms invpcid cqm rdt_a avx512ifma
                   clflushopt clwb intel_pt avx512cd sha ni avx512bw avx512vl
                   xsaveopt xsavec xgetbv1 xsave xsaveopt xsavec xsaveopt xsaves
                   cqm_llc cqm_occap llc cqm_mbb_total cqm_mbb_local split_lock_detect
                   wbnoinvd dtherm ida arat pln pts avx512v bmi umip pkup ospe avx512_vbmi
                   2 gfn ni vaes vpcmuldqg avx512_vnni avx512_bitalg tme avx512_vpopcntdq
                   la57 rdpid md_clear pconfig flush_l1d

/proc/cpuinfo cache data
    cache size: 39936 KB

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
          available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 52 53 54 55 56 57 58 59 60 61 62 63 64
node 0 free: 257296 MB
node 1 cpus: 13 14 15 16 17 18 19 20 21 22 23 24 25 65 66 67 68 69 70 71 72 73 74 75 76
node 1 free: 257691 MB
node 2 cpus: 26 27 28 29 30 31 32 33 34 35 36 37 38 78 79 80 81 82 83 84 85 86 87 88 89
node 2 free: 257813 MB
node 3 cpus: 39 40 41 42 43 44 45 46 47 48 49 50 51 91 92 93 94 95 96 97 98 99 100 101
node 3 free: 257691 MB
node distances:
    node 0 1 2 3
    0: 10 11 20 20
    1: 11 10 20 20
    2: 20 20 10 11
    3: 20 20 11 10

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Platform Notes (Continued)

From /proc/meminfo
MemTotal: 1056484100 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/sbin/tuned-adm active
Current active profile: throughput-performance

/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has performance

From /etc/*release* /etc/*version*
NAME="Red Hat Enterprise Linux"
VERSION="8.3 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.3"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"
ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga
uname -a:
Linux localhost.localdomain 4.18.0-240.el8.x86_64 #1 SMP Wed Sep 23 05:13:10 EDT 2020
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Melttdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swaps barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Aug 20 03:48

SPEC is set to: /home/CPU2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs 1.4T 86G 1.3T 7% /home

From /sys/devices/virtual/dmi/id
Vendor: Inspur
Product: NF5180M6
Product Family: Family
Serial: 390827124

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to

(Continued on next page)
Inspur Corporation

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 SPECrate®2017_int_base = 386
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Platform Notes (Continued)

allow hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory;
32x Micron 18ASF4G72PDZ-3G2E1 32 GB 2 rank 3200, configured at 2933

BIOS:
BIOS Vendor: American Megatrends Inc.
BIOS Version: 04.12.02
BIOS Date: 04/02/2021
BIOS Revision: 5.21

(End of data from sysinfo program)

Compiler Version Notes

<p>| | |</p>
<table>
<thead>
<tr>
<th></th>
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</tr>
</thead>
<tbody>
<tr>
<td>C</td>
<td>502.gcc_r(peak)</td>
</tr>
<tr>
<td>Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2022.1.0 Build 20220316</td>
<td></td>
</tr>
<tr>
<td>C</td>
<td>500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak) 557.xz_r(base, peak)</td>
</tr>
<tr>
<td>Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316</td>
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<tr>
<td>Fortran</td>
<td>548.exchange2_r(base, peak)</td>
</tr>
<tr>
<td>Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316</td>
<td></td>
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Inspur Corporation
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SPECrate®2017_int_base = 386
SPECrate®2017_int_peak = 397

CPU2017 License: 3358
Test Sponsor: Inspur Corporation
Test Date: Aug-2022
Hardware Availability: Apr-2021
Tested by: Inspur Corporation
Software Availability: May-2022

Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifx

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64_lin
-lqkmalloc

C++ benchmarks:
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64_lin
-lqkmalloc

Fortran benchmarks:
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64_lin
-lqkmalloc
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**SPECrate®2017_int_base = 386**

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**CPU2017 License:** 3358  
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**Tested by:** Inspur Corporation  
**Test Date:** Aug-2022  
**Hardware Availability:** Apr-2021  
**Software Availability:** May-2022

---

**Peak Compiler Invocation**

C benchmarks:  
icx

C++ benchmarks:  
icpx

Fortran benchmarks:  
ifx

---

**Peak Portability Flags**

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64  
502.gcc_r: -D_FILE_OFFSET_BITS=64  
505.mc_r: -DSPEC_LP64  
520.omnetpp_r: -DSPEC_LP64  
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX  
525.x264_r: -DSPEC_LP64  
531.deepsjeng_r: -DSPEC_LP64  
541.leela_r: -DSPEC_LP64  
548.exchange2_r: -DSPEC_LP64  
557.xz_r: -DSPEC_LP64

---

**Peak Optimization Flags**

C benchmarks:  
500.perlbench_r: -w -std=c11 -m64 -Wl,-z,muldefs  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-oqopt-mem-layout-trans=4 -fno-strict-overflow  
-L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64_lin  
-lqkmalloc

502.gcc_r: -m32  
-L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/ia32_lin  
-std=gnu89 -Wl,-z,muldefs -fprofile-generate(pass 1)  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-oqopt-mem-layout-trans=4 -L/usr/local/jemalloc32-5.0.1/lib  
-ljemalloc

(Continued on next page)
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Peak Optimization Flags (Continued)

505.mcf_r: basepeak = yes

525.x264_r: -w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast
-ffast-math -ftl0 -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fno-alias
-L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64_lin
-lqkmalloc

557.xz_r: basepeak = yes

C++ benchmarks:

520.omnetpp_r: basepeak = yes

523.xalancbmk_r: basepeak = yes

531.deepsjeng_r: basepeak = yes

541.leela_r: basepeak = yes

Fortran benchmarks:

548.exchange2_r: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-V2.5.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-V2.5.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.8 on 2022-08-20 03:55:21-0400.
Report generated on 2024-01-29 17:05:42 by CPU2017 PDF formatter v6716.
Originally published on 2022-09-13.